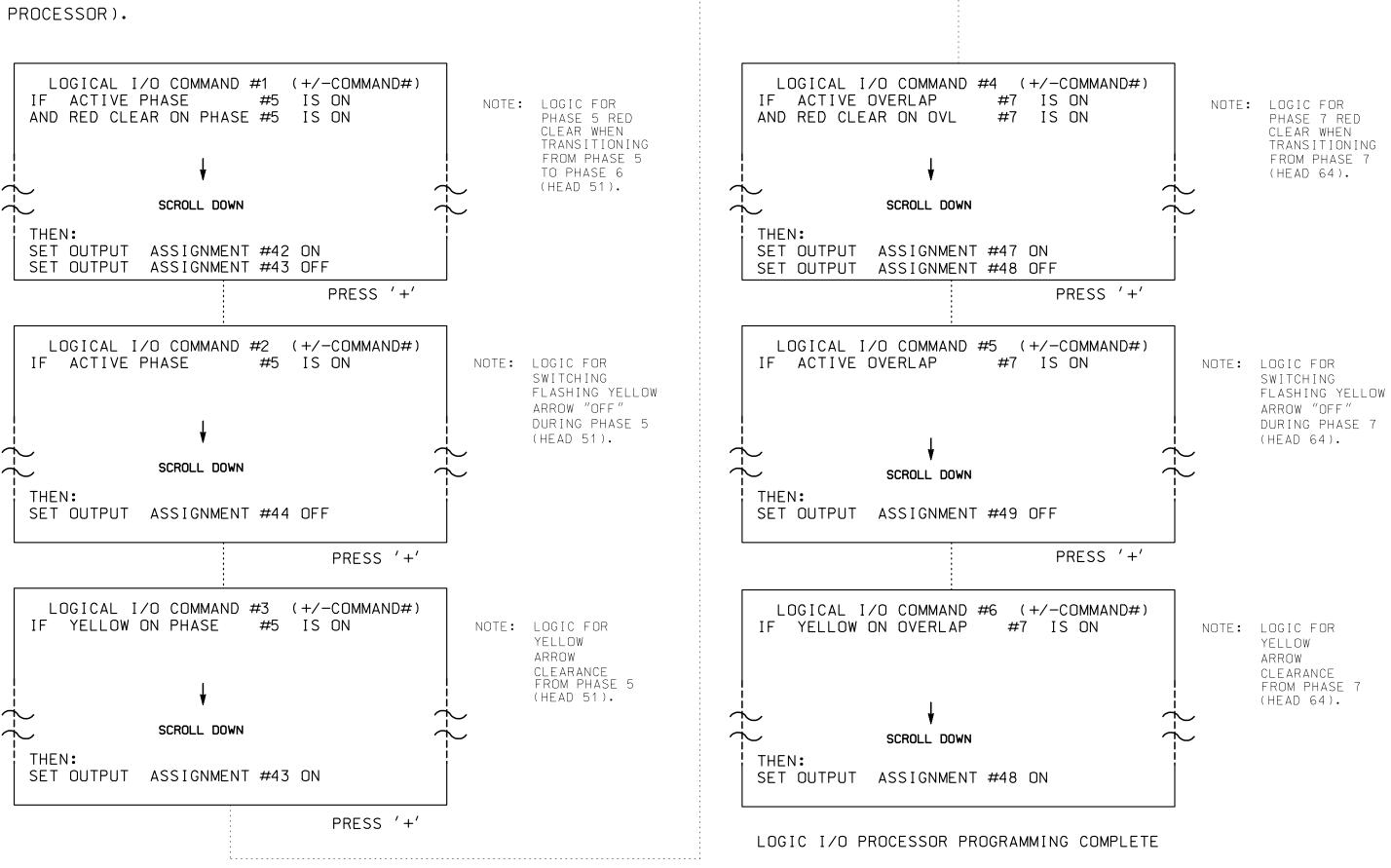
PROJECT REFERENCE NO. U-5738 Sig 4.4

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O



OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 47 = Overlap B Red OUTPUT 48 = Overlap B Yellow OUTPUT 49 = Overlap B Green

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø9-Ø992 DESIGNED: NOVEMBER 2021 SEALED: 12/3/2021 REVISED: N/A

Final Design Electrical Detail - Sheet 4 of 6

ELECTRICAL AND PROGRAMMING

750 N.Greenfield Pkwy, Garner, NC 27529

SR 2528 (Julian Rd) I-85/US 601 NB Ramps and SR 2761 (Truck Ave)

Rowan County PLAN DATE: November 2021 REVIEWED BY: B. Phillips

PREPARED BY: Z. "Gavin" Teng | REVIEWED BY: REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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SIG. INVENTORY NO. 09-0992

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PREPARED IN THE OFFICE OF: Accelerate Engineering, PLLC 875 Walnut Street, Suite 316 Cary, NC 27511 Tel: 919.263.5678 Fax: 919.263.5687 NC License No. P-1442