FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2. 2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3. 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

EMERGENCY VEHICLE PREEMPTION PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu press 'A' (Preemption), then '1' (Standard Preemptions). Press 'NEXT' as needed to advance to Preempts 3 and 5.

PREEMPTION #3 SETTINGS (NEXT:1-10) INTERVAL/TIMING CLEAR/DWELL PHASES GRN YEL RED 12345678910111213141516 1 255 0.0 0.0 X X 2 0 0.0 0.0 3 0 0.0 0.0 4 0 0.0 0.0 5 1 0.0 0.0 X X	
EXIT CALLS OPTIONS PRIORITY (Y/N TO SELECT)MED DELAY TIMER (0-255 SEC)0 MIN GREEN BEFORE PRE (0= DEFAULT)1 PED CLEAR BEFORE PRE (0= DEFAULT)0 YELLOW CLEAR BEFORE PRE (0= DEFAULT)0.0 RED CLEAR BEFORE PRE (0= DEFAULT)0.0 DWELL MIN TIMER (0-255 SEC)7 DWELL MAX TIMER (0=OFF,1-255MIN)2 DWELL HOLD-OVER TIMER (0-255)0 LATCH CALL?N LINK TO NEXT PREEMPT?N ENABLE BACKUP PROTECTION?N HOLD CLEAR 1 PHASES DURING DELAY?N FAST GREEN FLASH DWELL PHASES?N PED CLEARANCE THROUGH YELLOW?N INHIBIT OVERLAP GREEN EXTENSION?N REST IN RED DURING DWELL INTERVAL?N REST IN RED DURING DWELL INTERVAL?N ALLOW PEDS IN DWELL INTERVAL?N RE-TIME DWELL INTERVAL?N OVERLAPS: DWELL INT FLASH YELLOW OMIT OVERLAPS:	
PRESS 'NEXT' TW	ICE

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PREEMPTION #5 INTERVAL/TIMING GRN YEL RED 112 1 255 0.0 0.0 12 2 0 0.0 0.0 13 3 0 0.0 0.0 14 4 0 0.0 0.0 15 5 1 0.0 0.0 12 EXIT CALLS DELAY TIMER (0-255 MIN GREEN BEFORE PE PED CLEAR BEFORE PE PED CLEAR BEFORE PE YELLOW CLEAR BEFORE PE VELLOW CLEAR BEFORE PE DWELL MIN TIMER (0- DWELL MIN TIMER (0- DWELL MAX TIMER (0- DWELL MAX TIMER (0- DWELL HOLD-OVER TIM LATCH CALL? LINK TO NEXT PREEME ENABLE BACKUP PROTE HOLD CLEAR 1 PHASES FAST GREEN FLASH DW PED CLEARANCE THROU INHIBIT OVERLAP GRE SERVICE DURING SOF REST IN RED DURING FLASH DWELL INTERVA ALLOW PEDS IN DWELL RE-TIME DWELL INTERVA
OPTION PRIORITY (Y/N TO SE DELAY TIMER (O-255 MIN GREEN BEFORE PE PED CLEAR BEFORE PE YELLOW CLEAR BEFORE RED CLEAR BEFORE PE DWELL MIN TIMER (O DWELL MAX TIMER (O DWELL HOLD-OVER TIN LATCH CALL? LINK TO NEXT PREEME ENABLE BACKUP PROTE HOLD CLEAR 1 PHASES FAST GREEN FLASH DW PED CLEARANCE THROU INHIBIT OVERLAP GRE SERVICE DURING SOFT REST IN RED DURING FLASH DWELL INTERVA ALLOW PEDS IN DWELL RE-TIME DWELL INTERVA

Program e detector unit

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING <u>COORDINATION</u> - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED, NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

PHASING

ACTIVE PAGES REQUIRED TO RUN DEFAULT PHASING ACTIVE PAGES REQUIRED TO RUN ALTERNATE PHASIN

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING PAGE CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN THESE OVERLAP/INPUT PAGE CHANGES ACTIVATE TO CALL THE "ALTERNATE PHASING":

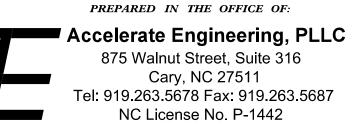
OVERLAPS PAGE 2: Modifies overlap parent phases turns only. INPUTS PAGE 2:

Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

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PROGRAMMING COMPLETE

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PROJECT REFERENCE NO.	SHEET NO.
U - 5738	Sig 2.7

	INPUTS	PAGE	OVERLAPS	PAGE
	1		1	
<u>NG</u>	2		2	

for heads 11 and 51 to run protected

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø9-1212 DESIGNED: NOVEMBER 2021 SEALED: 12/3/2021 REVISED: N/A

lectrical Detail -	Sheet 7 of 7		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED	
TRICAL AND PROGRAMMING DETAILS FOR:	SR 2528 (Jul at SR 2667 (Summi	SEAL CARO SEAL SEAL 032179		
Iranspo	Division 9 Rowan Cou	nty Salisbury	032179	
T. ION	PLAN DATE: November 2021 REVI	EWED BY: B. Phillips	FICT ENGINEER	
	PREPARED BY: Z. "Gavin" Teng REVI	EWED BY:	LONG	
Si OF TRANSPORT	REVISIONS	INIT. DATE		
Manage.			Eliaslong ting 12/3/2021	
N.Greenfield Pkwy,Garner,NC 27529	l		- 6B 够 A B B B B B B B B B B B B B B B B B	
			SIG. INVENTORY NO. 09-1212	