PROJECT REFERENCE NO. Sig 2.4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

SIGNATURES COMPLETED

SEAL

032179

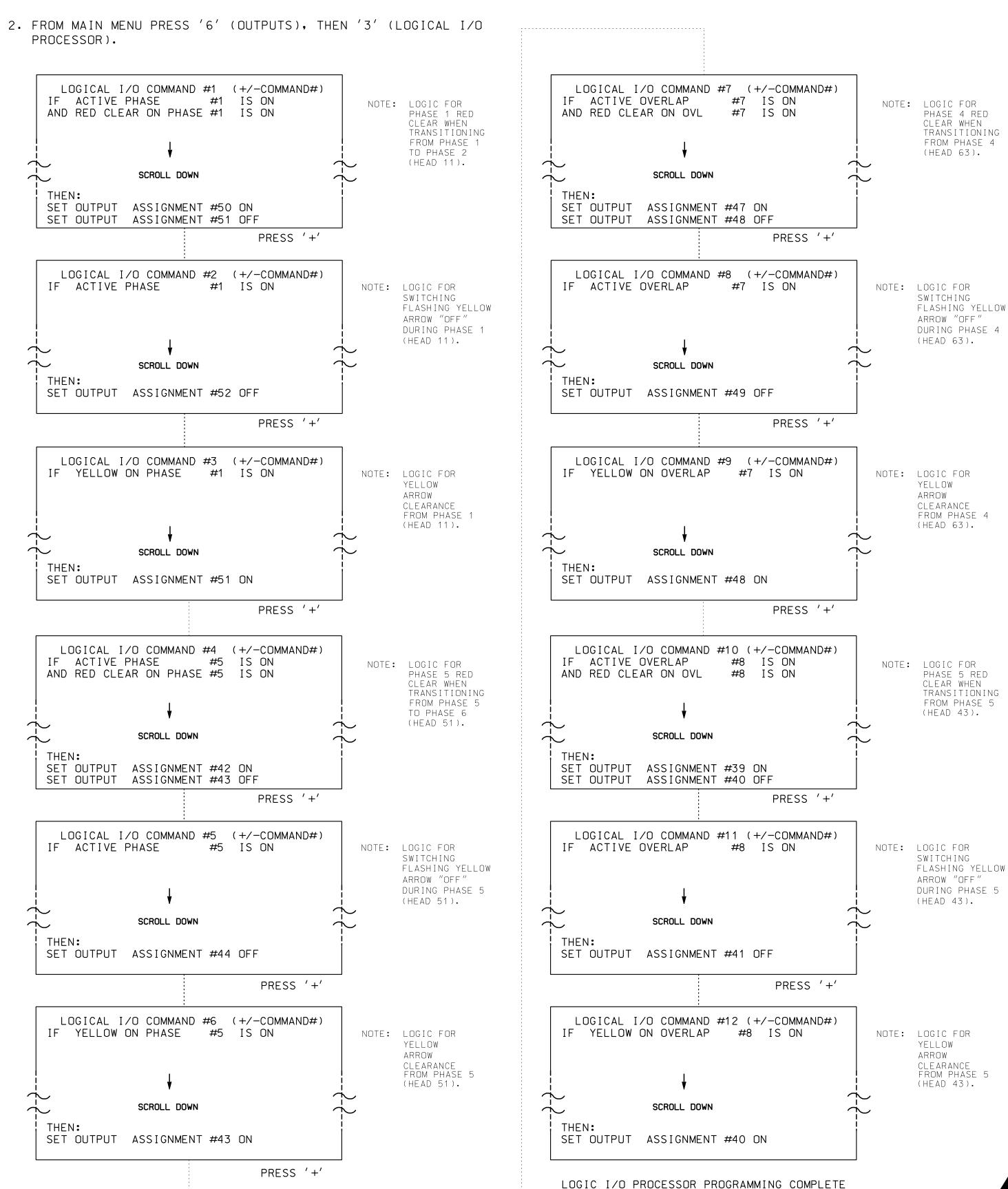
SIG. INVENTORY NO. 09-1212

Eliadong ting

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11 AND 12.



OUTPUT REFERENCE SCHEDULE

PHASE 4 RED

CLEAR WHEN

(HEAD 63).

PHASE 5 RED

CLEAR WHEN

(HEAD 43).

TRANSITIONING

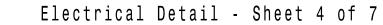
TRANSITIONING

FROM PHASE 4

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39 = Overlap D Red OUTPUT 40 = Overlap D Yellow OUTPUT 41 = Overlap D Green OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 47 = Overlap B Red OUTPUT 48 = Overlap B Yellow OUTPUT 49 = Overlap B Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø9-1212 DESIGNED: NOVEMBER 2021 SEALED: 12/3/2021 REVISED: N/A



ELECTRICAL AND PROGRAMMING DETAILS FOR:

SR 2528 (Julian Rd) SR 2667 (Summit Park Dr)

ivision 9 PLAN DATE: November 2021 REVIEWED BY: B. Phillips PREPARED BY: Z. "Gavin" Teng | REVIEWED BY: REVISIONS INIT. DATE

750 N.Greenfield Pkwy, Garner, NC 27529

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