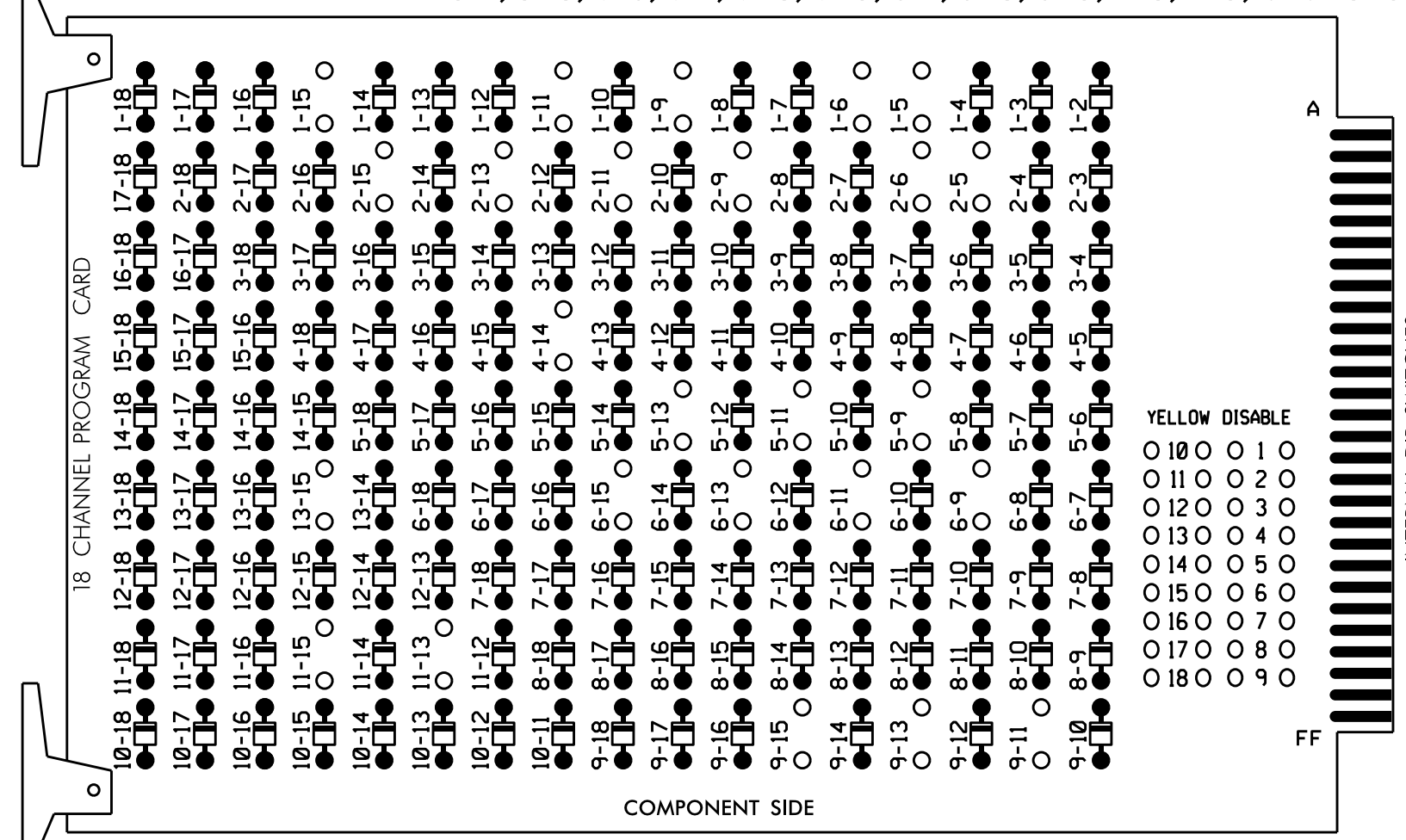


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL
(remove jumpers and set switches as shown)

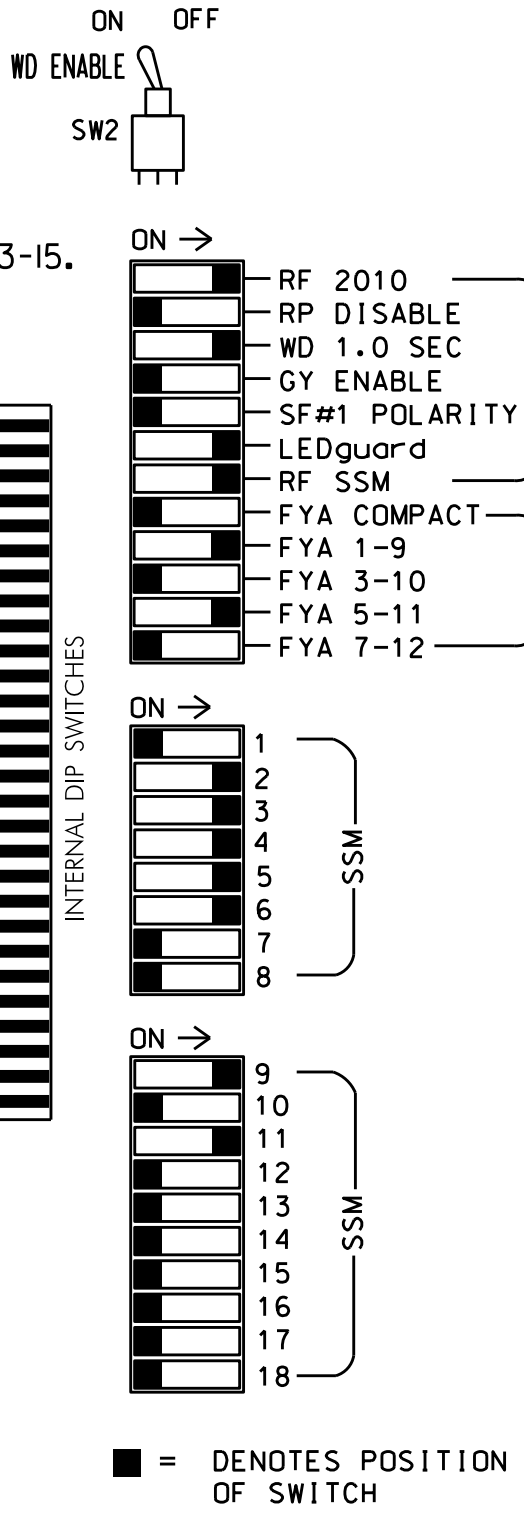
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 4-14, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 9-11, 9-13, 9-15, 11-13, 11-15, and 13-15.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phase 4 for Startup Ped Call.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all enabled detectors.
- The cabinet and controller are part of the US 158 (Croatan Hwy) South CLS, Signal System #10107.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,
 AUX S1,AUX S4
 PHASES USED.....1,2,2 PED,3,4,4 PED,5,6,6 PED
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6		
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18		
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE		
SIGNAL HEAD NO.	11	21,22	P21, P22	31	32	41,42	43,44	62	P41, P42	44	51	61,62	P61, P62	NU	NU	NU	11	51	NU	
RED		128		116	116		101			*		134								
YELLOW	*	129		117	117		102					135								
GREEN		130		118	118		103					136								
RED ARROW							101												A121	A114
YELLOW ARROW							102		102	132									A122	A115
FLASHING YELLOW ARROW																			A123	A116
GREEN ARROW	127			118		103		103		133	133									
Hand				113					104					119						
Walking				115					106											

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail on sheet 2.
 ★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT (front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 2	∅ 3	∅ 3	∅ 4	∅ 4	S	SYS. DET. S07	S	S	∅ 2 PED	∅ 6 PED	FS	
L	1A	2A	3A	3C	4A	4C	Y	SYS. DET. S08	Y	Y	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	
U	NOT USED	∅ 2	∅ 3	NOT USED	∅ 4	NOT USED	Y	SYS. DET. S09	Y	Y	∅ 4 PED	NOT USED	ST	
L		2B	3B		4B		Y	SYS. DET. S10	Y	Y	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	
U	∅ 5	∅ 5	∅ 6	∅ 6	∅ 6	∅ 6	Y	SYS. DET. S09	Y	Y	∅ 6 PED	NOT USED	ST	
L	5A	5B	6A	6B	6B	6B	Y	SYS. DET. S10	Y	Y	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	
U	NOT USED	NOT USED	∅ 6	∅ 6	∅ 6	∅ 6	Y	SYS. DET. S10	Y	Y	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	
L			6B				Y	SYS. DET. S10	Y	Y	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	

EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

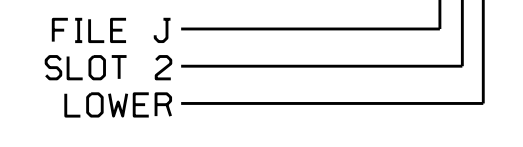
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10 ★	26	6	Y	Y	Y		3
	-	I1U	56	18 ★	51	1	Y	Y			3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A	TB2-9,10	I3U	63	25	32	3	Y	Y			3
3B	TB2-11,12	I3L	76	38	42	3	Y	Y			10
3C	TB4-5,6	I5U	58	20	3	3	Y	Y			15
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			
4C	TB6-1,2	I7U	65	27	34	4	Y	Y			
5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9 ★	22	2	Y	Y	Y		3
	-	J1U	55	17 ★	55	5	Y	Y			3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			
*S07	TB6-9,10	I9U	60	22	11	SYS					
*S08	TB6-11,12	I9L	62	24	13	SYS					
*S09	TB7-9,10	J9U	59	21	15	SYS					
*S10	TB7-11,12	J9L	61	23	17	SYS					
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

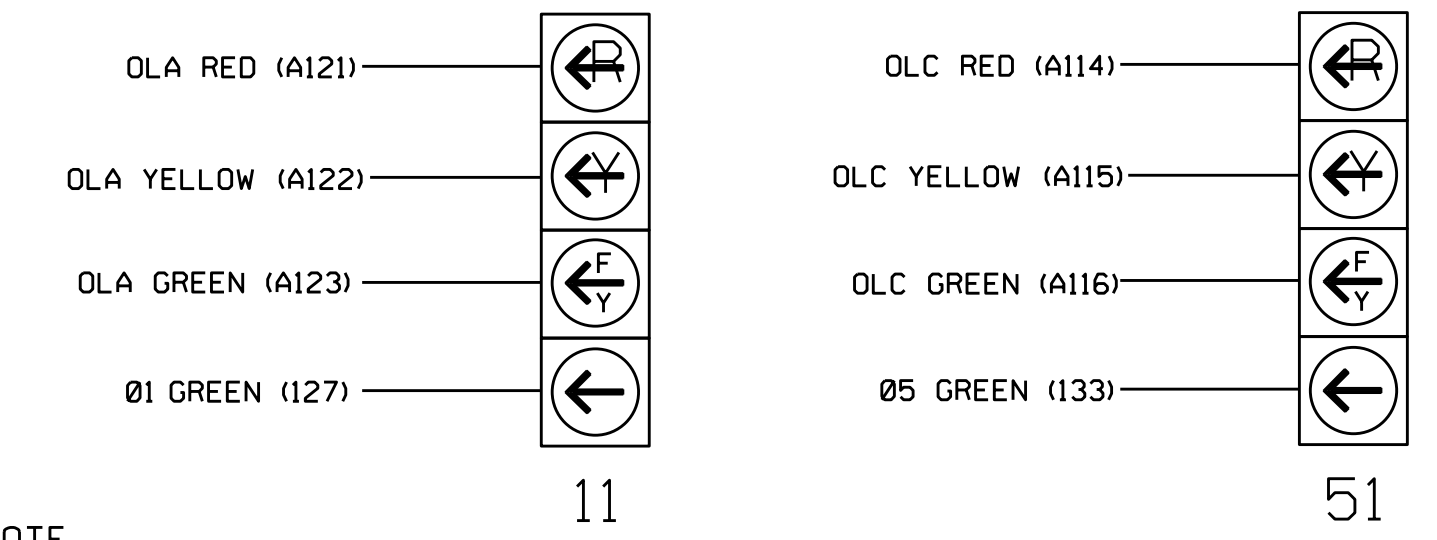
- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.
- See Input Page Assignment programming details on sheets 3 and 4.
- System detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0030
 DESIGNED: January 2020
 SEALED: 2/12/2020
 REVISED: N/A

FYA SIGNAL WIRING DETAIL (wire signal heads as shown)



NOTE

The sequence display for signal signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

ADVANCE WALK NOTE (program controller as shown below)

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phases 2, 4, and 6 for 'Advanced Walk'. Make sure the Walk Advance Time shown on the Signal Design plans are programmed in the 'Phase Timing' menu.

Electrical Detail - Sheet 1 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	US 158 (Croatan Highway) at SR 1217 (Collington Road/Ocean Bay Boulevard)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL RYAN W. HOUGH PROFESSIONAL ENGINEER SEAL 036833 DATE 2/17/2020
	Division 1 Dare County Kill Devil Hills PLAN DATE: January 2020 REVIEWED BY: PREPARED BY: S. Armstrong REVIEWED BY:	REVISIONS INIT. DATE	