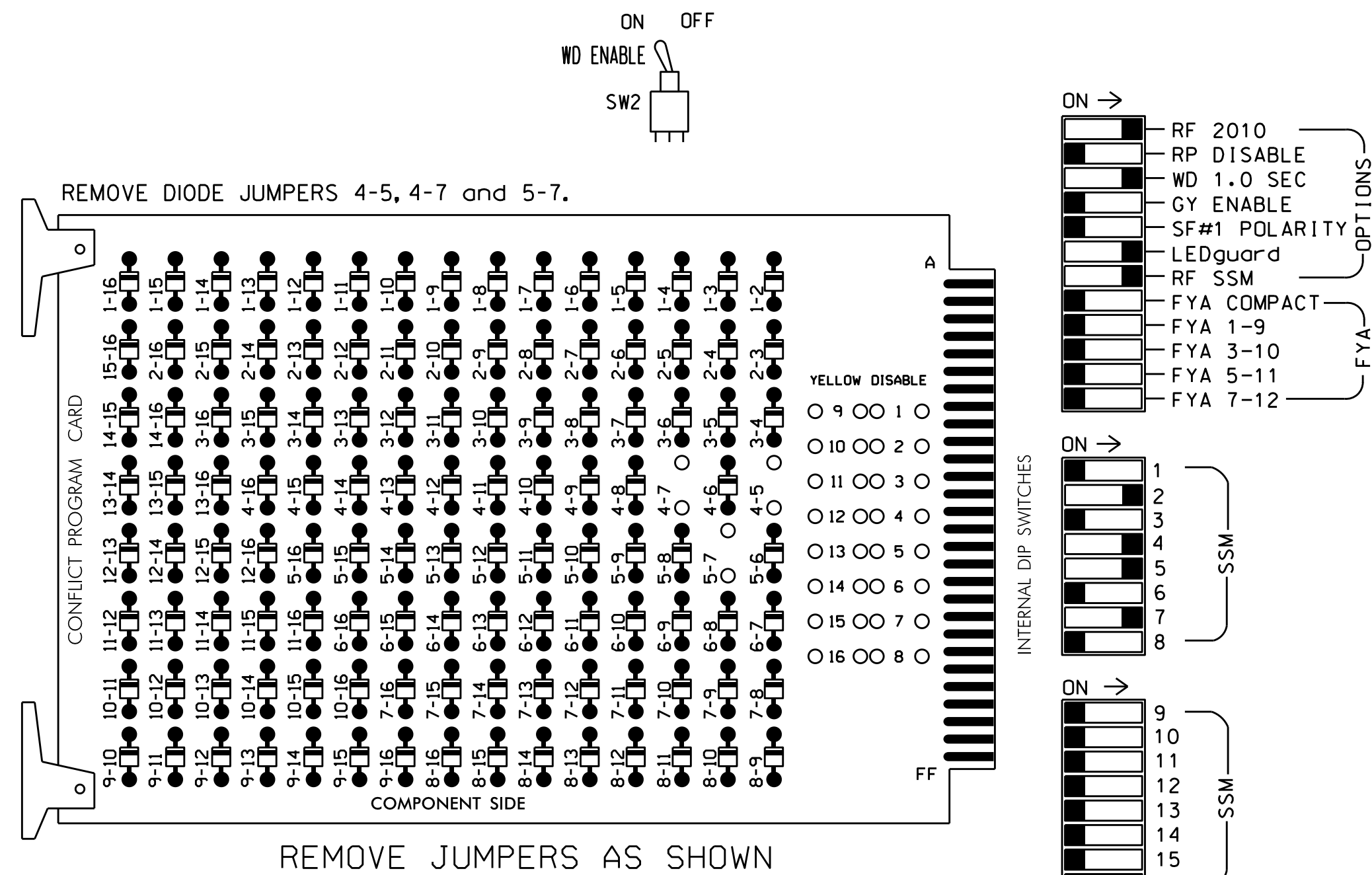


EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 1,3,6, 8,9,10,11,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phases 4 and 7 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phase 2 for Variable Initial and Gap Reduction.
- Program phase 2 for Startup In Green.
- Program phase 2 for Yellow Flash.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of the US 17 (Ocean Highway) - Leland Superstreet D03-12 Leland.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P
PHASE	1	2	2 PED	3	4	4 PED	** OLG	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	73,74	NU	NU	71,72	NU	NU
RED		128			101					122		
YELLOW		129								123		
GREEN		130								124		
RED ARROW							131					
YELLOW ARROW					102		132					
GREEN ARROW					103		133					

NU = Not Used

** Requires special programming and output remapping. See sheet 2.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...12
 LOAD SWITCHES USED.....S2,S4,S5,S7
 PHASES USED.....2,4,7
 OVERLAP G.....7

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	S	∅ 7	∅ 2/SYS	-O/S	-O/S	-O/S	-O/S	-O/S	-O/S	-O/S	-O/S	-O/S	-O/S	FS
L	7A	2A/S15												DC ISOLATOR
L	7B	2B/S16												ST
U						∅ 4								DC ISOLATOR
L						4A								
L						4B								

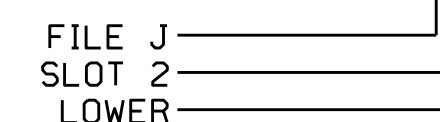
EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A/S15	TB2-9,10	I3U	63	25	32	2/SYS	Y	Y			
2B/S16	TB2-11,12	I3L	76	38	42	2/SYS	Y	Y			
4A	TB5-9,10	J6U	42	4	8	4	Y	Y			15
4B	TB5-11,12	J6L	46	8	18	4	Y	Y			15
7A	TB2-5,6	I2U	39	1	2	7	Y	Y			
7B	TB2-7,8	I2L	43	5	12	7	Y	Y			

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0966
 DESIGNED: October 2021
 SEALED: 10/25/2021
 REVISED:

Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	US 17 (Ocean Highway) at SR 1566 (Ploof Road)		SEAL SEAL 031001 ENGINEER TODD JOYCE
	Division 3 Brunswick County Leland PLAN DATE: October 2021 REVIEWED BY: T. Joyce PREPARED BY: C. Strickland REVIEWED BY:	REVISIONS INIT. DATE	

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED