

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING COORDINATION - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED, NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

| <u>PHASING</u> | <u>INPUTS PAGE</u> | <u>OVERLAPS PAGE</u> |
|---|--------------------|----------------------|
| ACTIVE PAGES REQUIRED TO RUN <u>DEFAULT PHASING</u> | 1 | 1 |
| ACTIVE PAGES REQUIRED TO RUN <u>ALTERNATE PHASING</u> | 2 | 2 |

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

ALTERNATE PHASING PAGE CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN THESE OVERLAP/INPUT PAGE CHANGES ACTIVATE TO CALL THE "ALTERNATE PHASING":


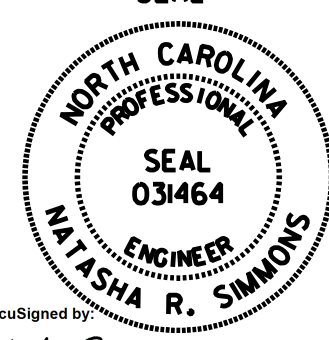
- OVERLAPS PAGE 2:** Modifies overlap parent phases for heads 11, 31, 51, and 71 to run protected turns only.
- INPUTS PAGE 2:** Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.
- Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds.
- Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.
- Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0215T4
DESIGNED: June 2017
SEALED: 9/10/2021
REVISED: N/A

Electrical Detail - Sheet 8 of 8
Signal Upgrade
Temporary Design 4

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

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| <p style="font-size: small;">ELECTRICAL AND PROGRAMMING DETAILS FOR:</p> <div style="text-align: center;">  <p style="font-size: x-small;">Prepared for: Department of Transportation Division of Signal Management</p> </div> <p style="font-size: x-small;">750 N. Greenfield Pkwy, Corner, NC 27529</p> | <p style="text-align: center;">NC 211 (Howe Street) at NC 87 (River Road)/ SR 1852 (Robert Ruark Drive)</p> <p style="font-size: x-small;">Division 03 Brunswick Co. Southport</p> <p style="font-size: x-small;">PLAN DATE: June 2017 REVIEWED BY: A.D. Klinksiek</p> <p style="font-size: x-small;">PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="font-size: x-small;">REVISIONS</th> <th style="font-size: x-small;">INIT.</th> <th style="font-size: x-small;">DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table> | REVISIONS | INIT. | DATE | | | | <p style="text-align: center;">SEAL</p> <div style="text-align: center;">  <p style="font-size: x-small;">NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 031464 NATASHA R. SIMMONS</p> </div> <p style="font-size: x-small;">DocuSigned by: Natasha Simmons 9/10/2021</p> <p style="font-size: x-small;">SIGNATURE DATE</p> <p style="font-size: x-small;">SIG. INVENTORY NO. 03-0215T4</p> |
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