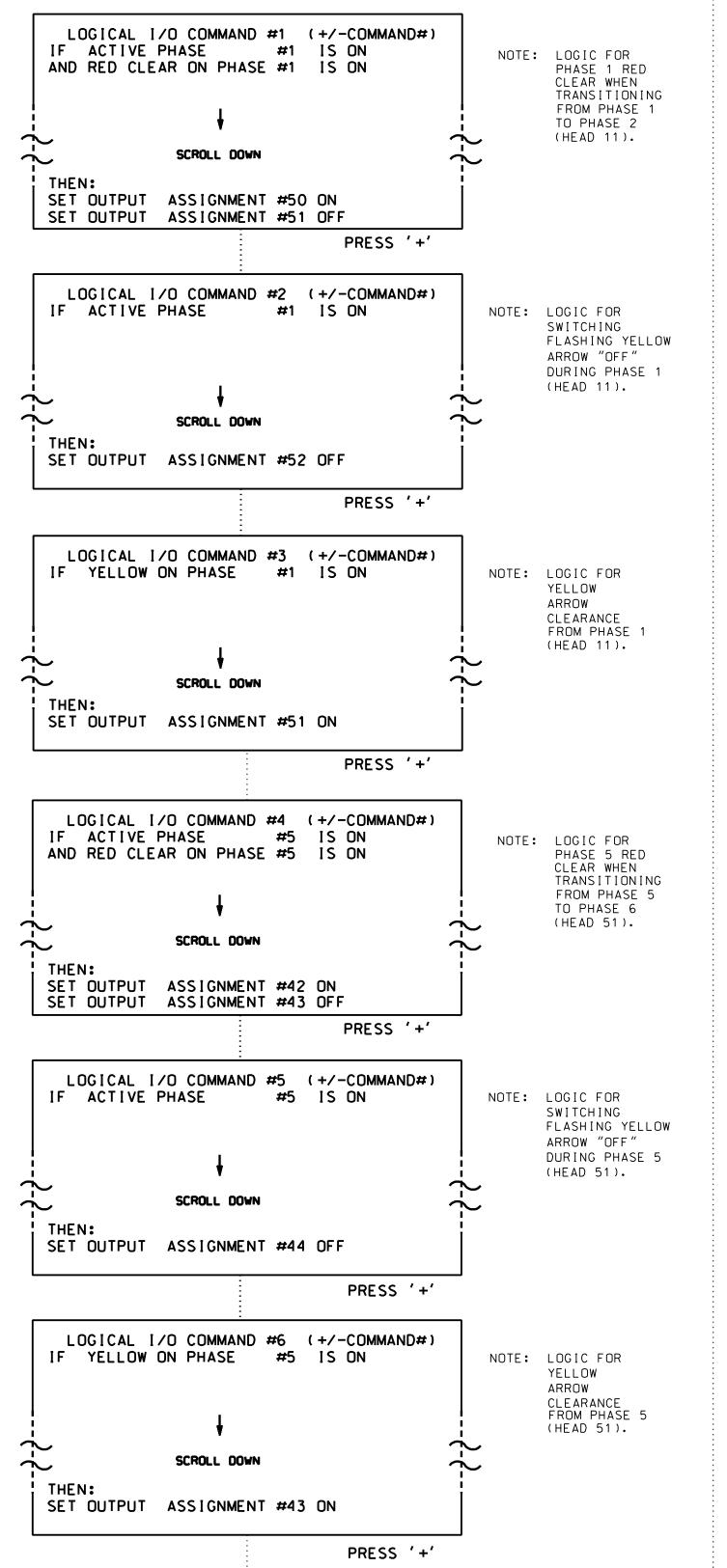
## R-5021 Sig. 41.2

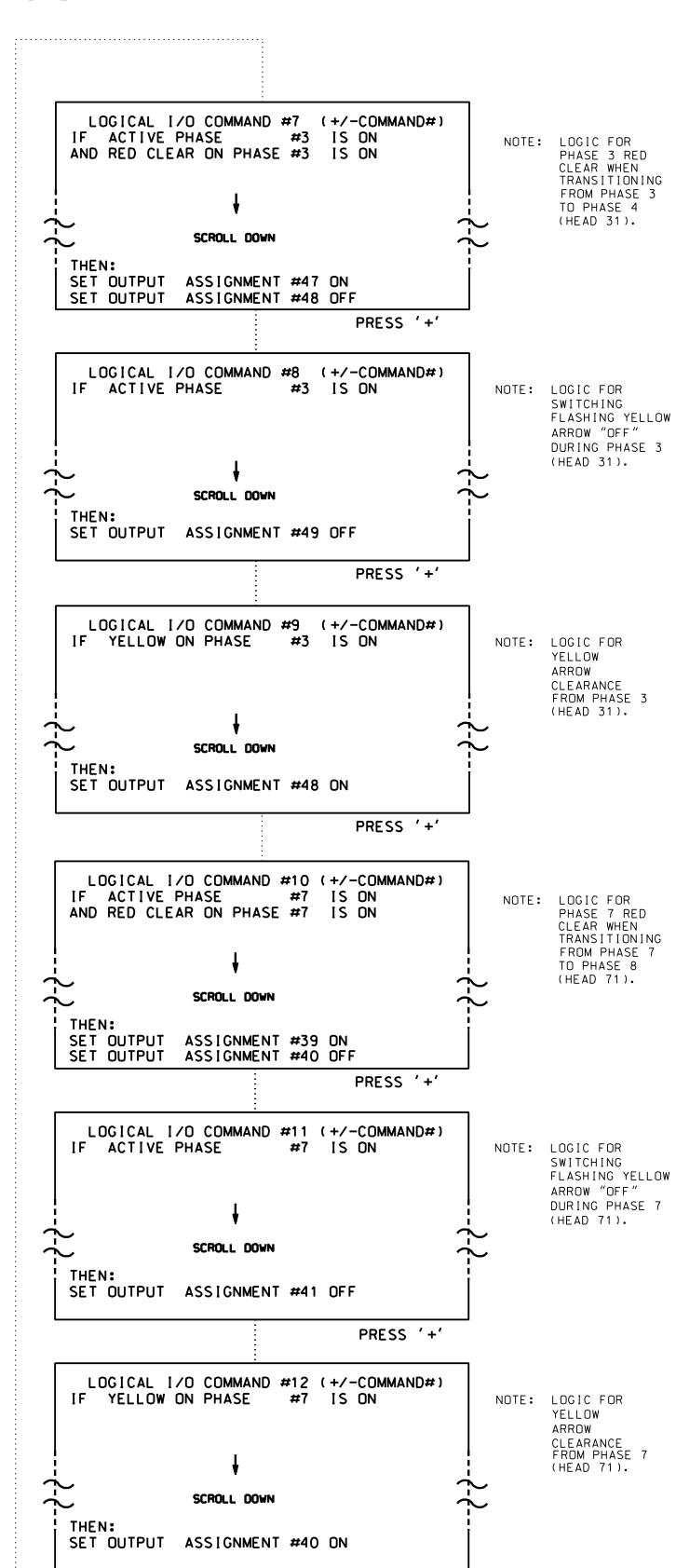
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, AND 12.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).





LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

## OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR

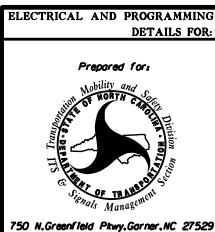
OUTPUT 39 = Overlap D Red
OUTPUT 40 = Overlap D Yellow
OUTPUT 41 = Overlap D Green
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
OUTPUT 47 = Overlap B Red
OUTPUT 48 = Overlap B Yellow
OUTPUT 49 = Overlap B Green
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0215T3 DESIGNED: June 2017 SEALED: 9/10/2021 REVISED: N/A

Electrical Detail - Sheet 2 of 8

Signal Upgrade Temporary Design 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



NC 211 (Howe Street) at NC 87 (River Road)/ SR 1852 (Robert Ruark Drive)

SR 1852 (Robert Ruark Drive)

Division 03 Brunswick Co. Southport

PLAN DATE: June 2017 REVIEWED BY: A.D. Klinksiek

PLAN DATE: June 2017 REVIEWED BY: A.D. Klinksiek
PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons
REVISIONS INIT. DATE

SEAL
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Pocusigned by:

Matasha Simmons

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