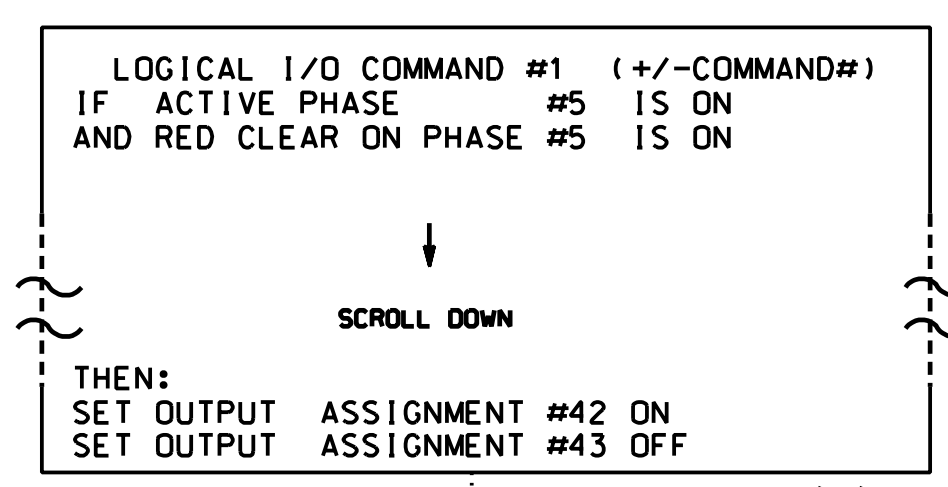


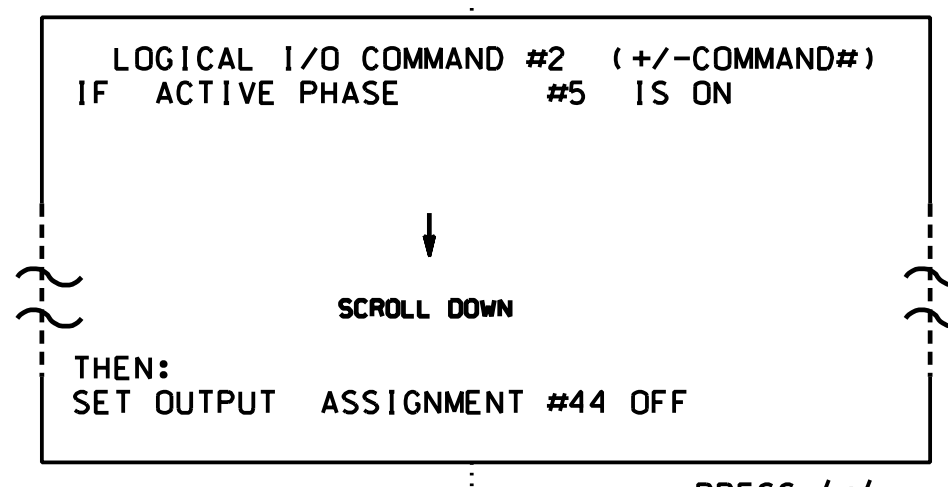
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

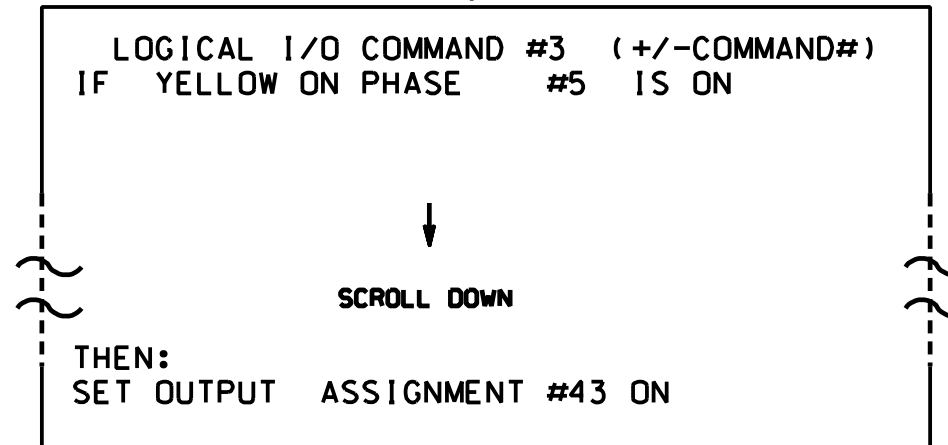
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
USE TO INTERPRET LOGIC PROCESSOR	
OUTPUT 42	= Overlap C Red
OUTPUT 43	= Overlap C Yellow
OUTPUT 44	= Overlap C Green

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).
PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: :12345678910111213141516
VEH OVL PARENTS: : XX
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE: :12345678910111213141516
VEH OVL PARENTS: : XX
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW - GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).
PRESS 'NEXT' TO ADVANCE TO PAGE 2.
PRESS '+' TWICE

NOTICE PAGE 2 →

```

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS
PHASE: :12345678910111213141516
VEH OVL PARENTS: : X
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW - GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

NOTICE PAGE 2 →

```

PAGE 2: VEHICLE OVERLAP 'D' SETTINGS
PHASE: :12345678910111213141516
VEH OVL PARENTS: : XX
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW - GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 03-0267T2
DESIGNED: June 2017
SEALED: 9/10/2021
REVISED: N/A

Electrical Detail - Sheet 2 of 4
Signal Upgrade
Temporary Design 2

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

HNTB HNTB NORTH CAROLINA, P.C.
343 E. Six Forks Road, Suite 200
Raleigh, North Carolina 27609
NC license No: C-1554
(919) 546-8997

	Prepared for: NC 211 (Southport-Supply Road) at Dosher Cut Off		
	Division 03 Brunswick Co. Southport		
PLAN DATE: June 2017	REVIEWED BY: A.D. Klinksiek		
PREPARED BY: A.H. Thornburg	REVIEWED BY: N.R. Simmons		
REVISIONS	INIT.	DATE	
Signature of <i>Natasha Simmons</i>			DATE: 9/10/2021
Signature of _____			DATE: _____
SIG. INVENTORY NO. 03-0267T2			