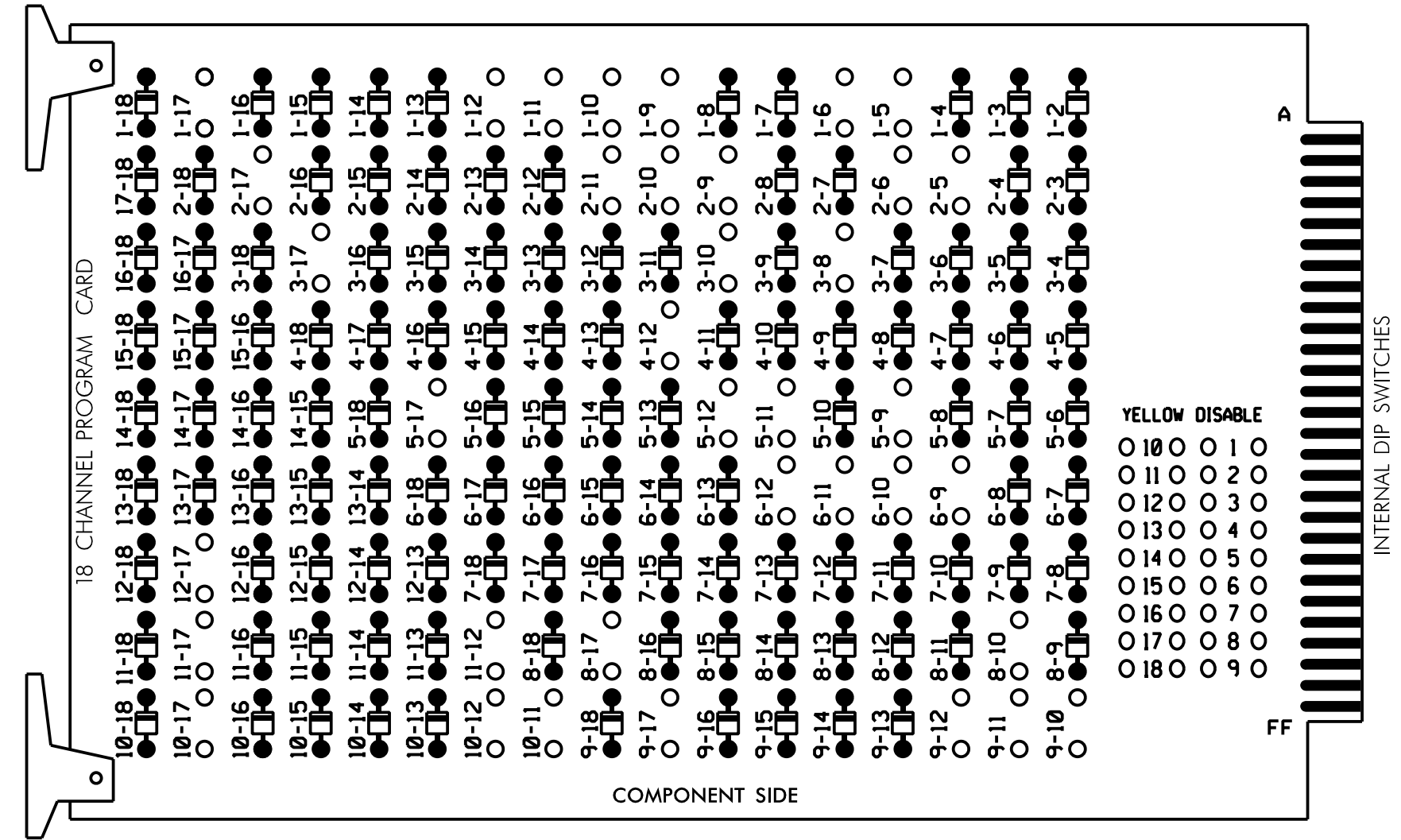


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

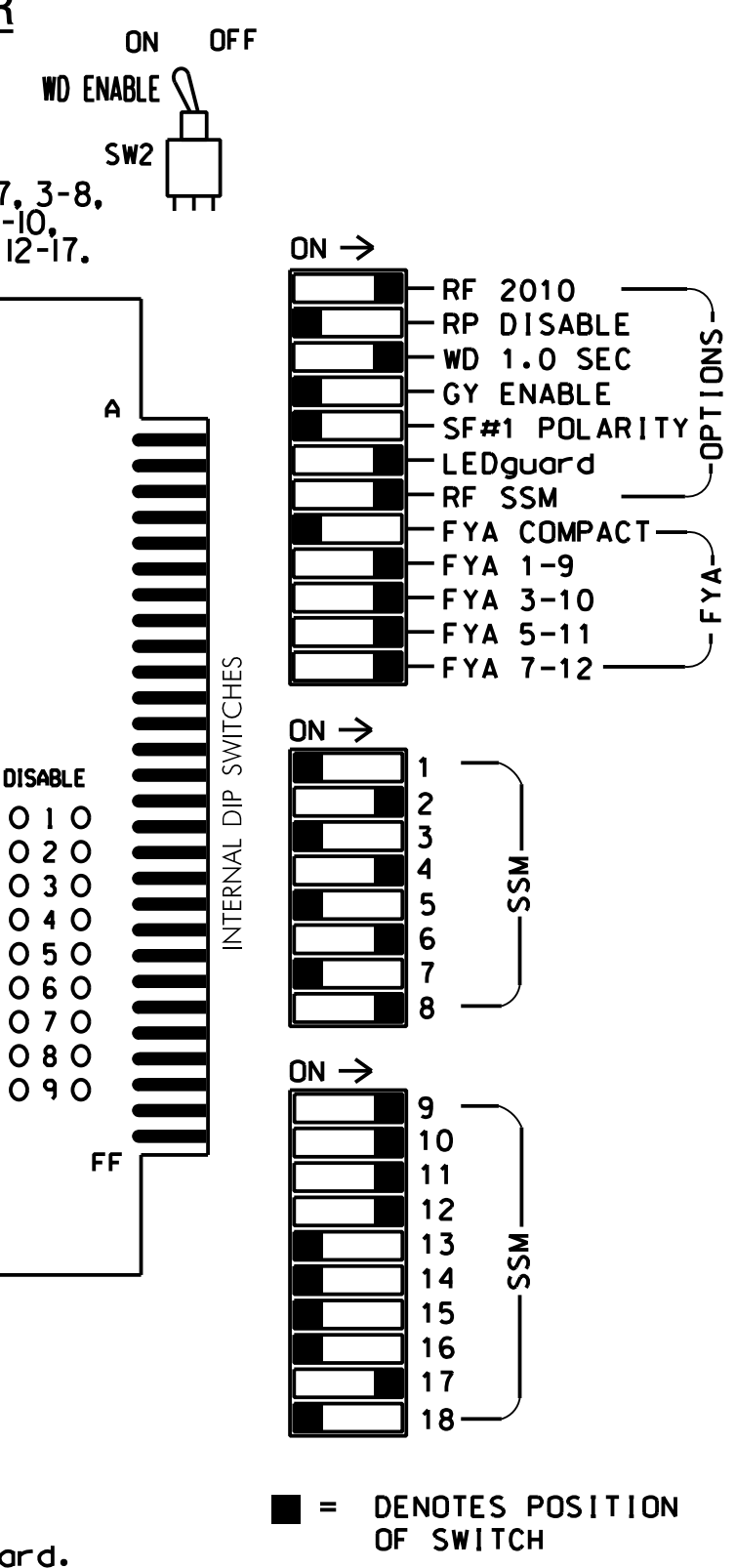
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-10, 1-11, 1-12, 1-17, 2-5, 2-6, 2-9, 2-10, 2-11, 2-17, 3-8, 3-10, 3-17, 4-12, 5-9, 5-11, 5-12, 5-17, 6-9, 6-10, 6-11, 6-12, 8-10, 8-17, 9-10, 9-11, 9-12, 9-17, 10-11, 10-12, 10-17, 11-12, 11-17, and 12-17.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1, 2, and 5 as Wag Overlaps.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- The cabinet and controller are part of the NC 133 Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S11,AUX S1, AUX S2,AUX S3,AUX S4,AUX S5

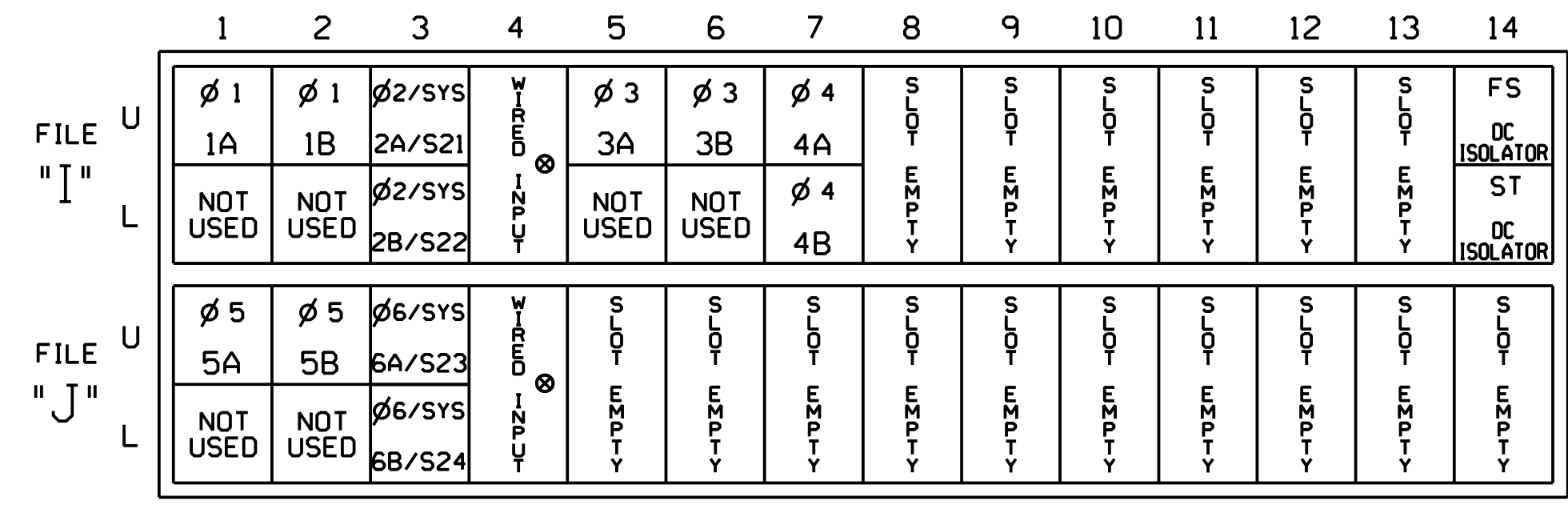
PHASES USED.....1,2,3,4,5,6
 OVERLAP "A".....1+2
 OVERLAP "B".....3+6
 OVERLAP "C".....5+6
 OVERLAP "D".....1+4
 OVERLAP "E".....3+5
 OVERLAP "F".....NOT USED
 OVERLAP "G".....3

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	OLG	8 PED	OLA	OLA	OLE	OLC	OLD	NU	
SIGNAL HEAD NO.	11	21,22	NU	63	41	42	NU	51	61,62,64	NU	31	32	NU	11	63	33	51	43	
RED		128			101	101			134		107	107			A124	A111		A101	
YELLOW	*	129		*	102	102		*	135		108	108							
GREEN		130			103	103			136		109	109							
RED ARROW															A121			A114	
YELLOW ARROW															A122	A125	A112	A115	A102
FLASHING YELLOW ARROW															A123	A126	A113	A116	A103
GREEN ARROW	127				118	103			133		109								

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail this sheet.
 NOTE: Load switches S11 and AUX S3 require output remapping. See sheets 7 and 8 of this electrical detail for instructions.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

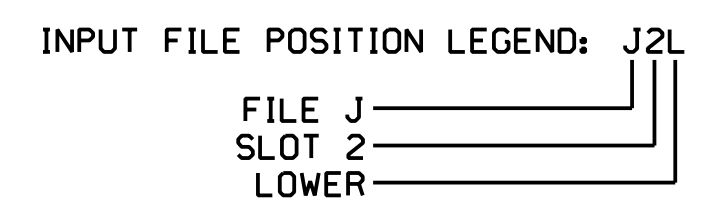
⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	J1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	J1U	56	18★	51	1	Y	Y			3
1B	TB2-5,6	J2U	39	1	2	1	Y	Y			15
2A/S21	TB2-9,10	J3U	63	25	32	2/SYS	Y	Y			
2B/S22	TB2-11,12	J3L	76	38	42	2/SYS	Y	Y			
3A	TB4-5,6	J5U	58	20	3	3	Y	Y			
3B	TB4-9,10	J6U	41	3	3	3	Y	Y			
4A	TB6-1,2	J7U	65	27	34	4	Y	Y			
4B	TB6-3,4	J7L	78	40	44	4	Y	Y			
5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	J4U	47	9★	22	2	Y	Y	Y		3
	-	J1U	55	17★	55	5	Y	Y			3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			15
6A/S23	TB3-9,10	J3U	64	26	36	6/SYS	Y	Y			
6B/S24	TB3-11,12	J3L	77	39	46	6/SYS	Y	Y			

- Add jumper from J1-W to J4-W, on rear of input file.
- Add jumper from J1-W to J4-W, on rear of input file.

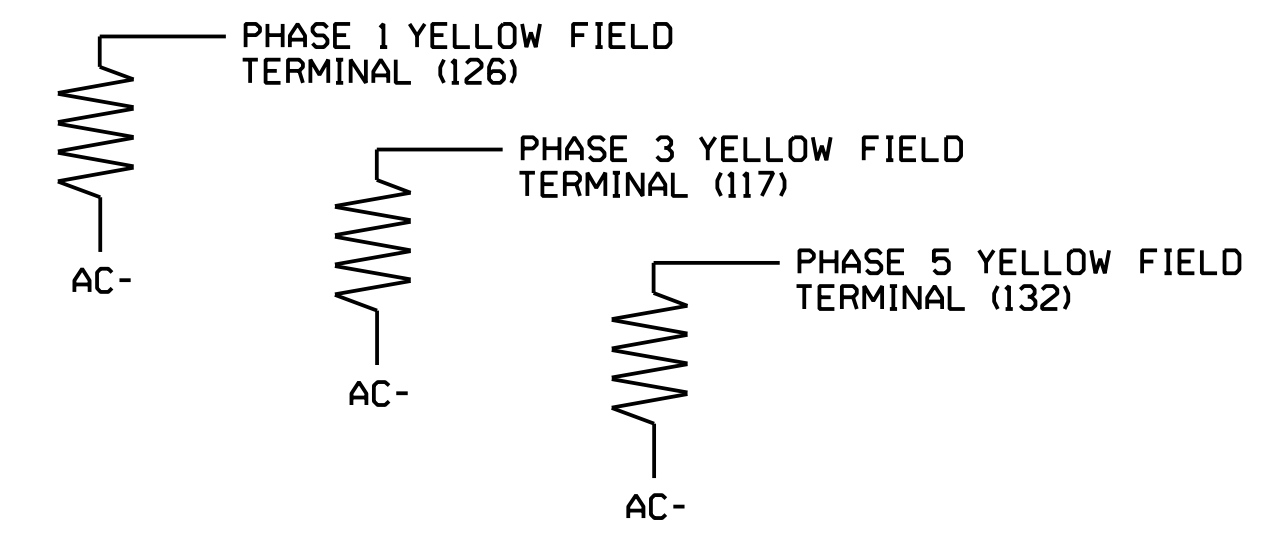
★ See vehicle detector setup programming detail for alternate phasing on sheets 4 and 5.



LOAD RESISTOR INSTALLATION DETAIL

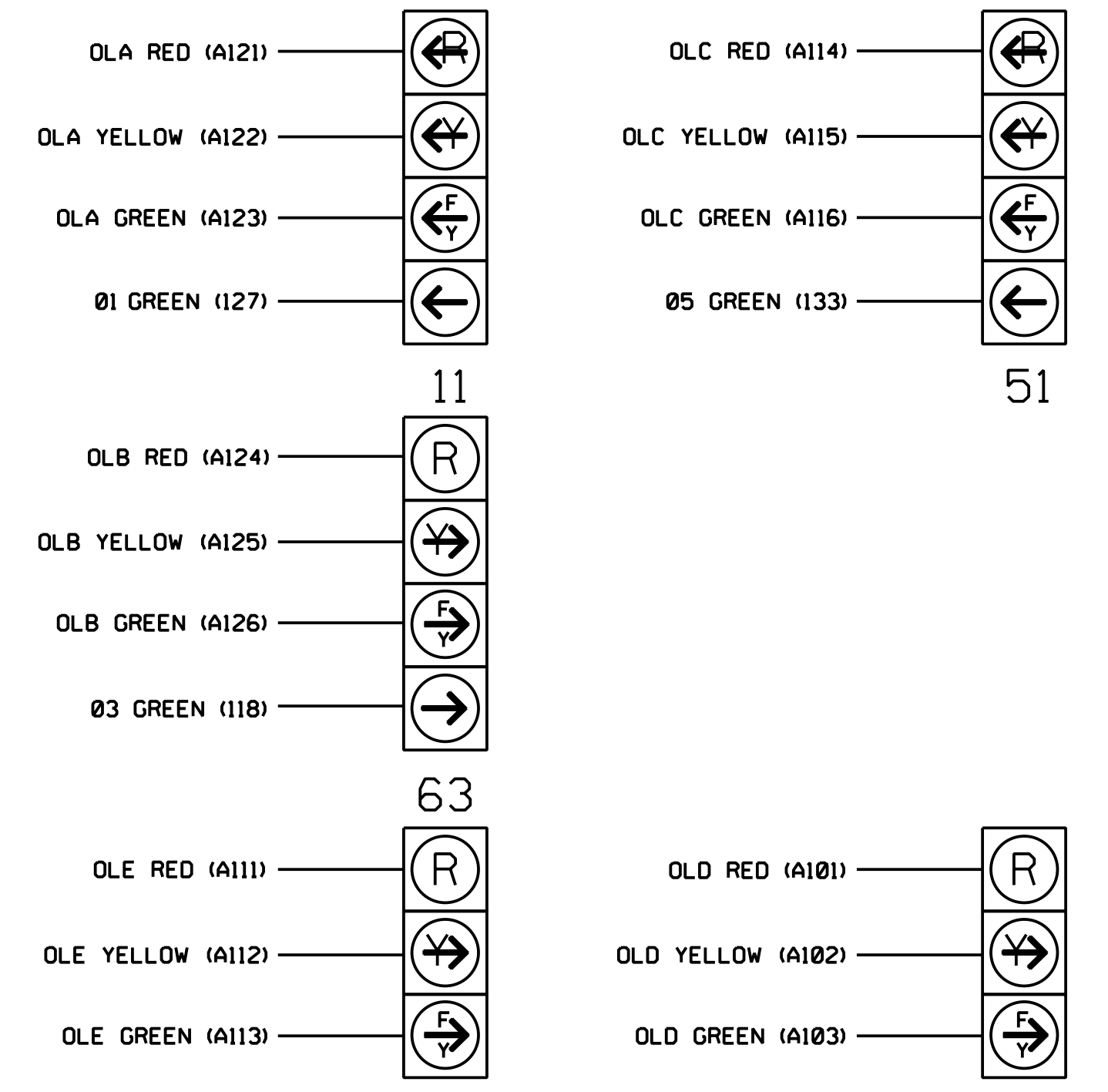
(install resistors as shown)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal heads 11, 51, and 63 requires special logic programming. See sheet 2 for programming instructions.

Electrical Detail - Sheet 1 of 8

New Installation
Final Design

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared for:

NC 133 (Long Beach Road) at SR 1969 (Old Long Beach Road) / Southport Crossing Entrance	
Division 03 Brunswick Co.	Southport
PLAN DATE: June 2017	REVIEWED BY: A.D. Klinskiak
PREPARED BY: A.H. Thornburg	REVIEWED BY: N.R. Simmons
REVISIONS	INIT. DATE

SEAL

 Natasha R. Simmons
 ENGINEER
 9/10/2021
 SIGNATURE DATE
 SIG. INVENTORY NO. 03-1125

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THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-1125
 DESIGNED: June 2017
 SEALED: 9/10/2021
 REVISED: N/A