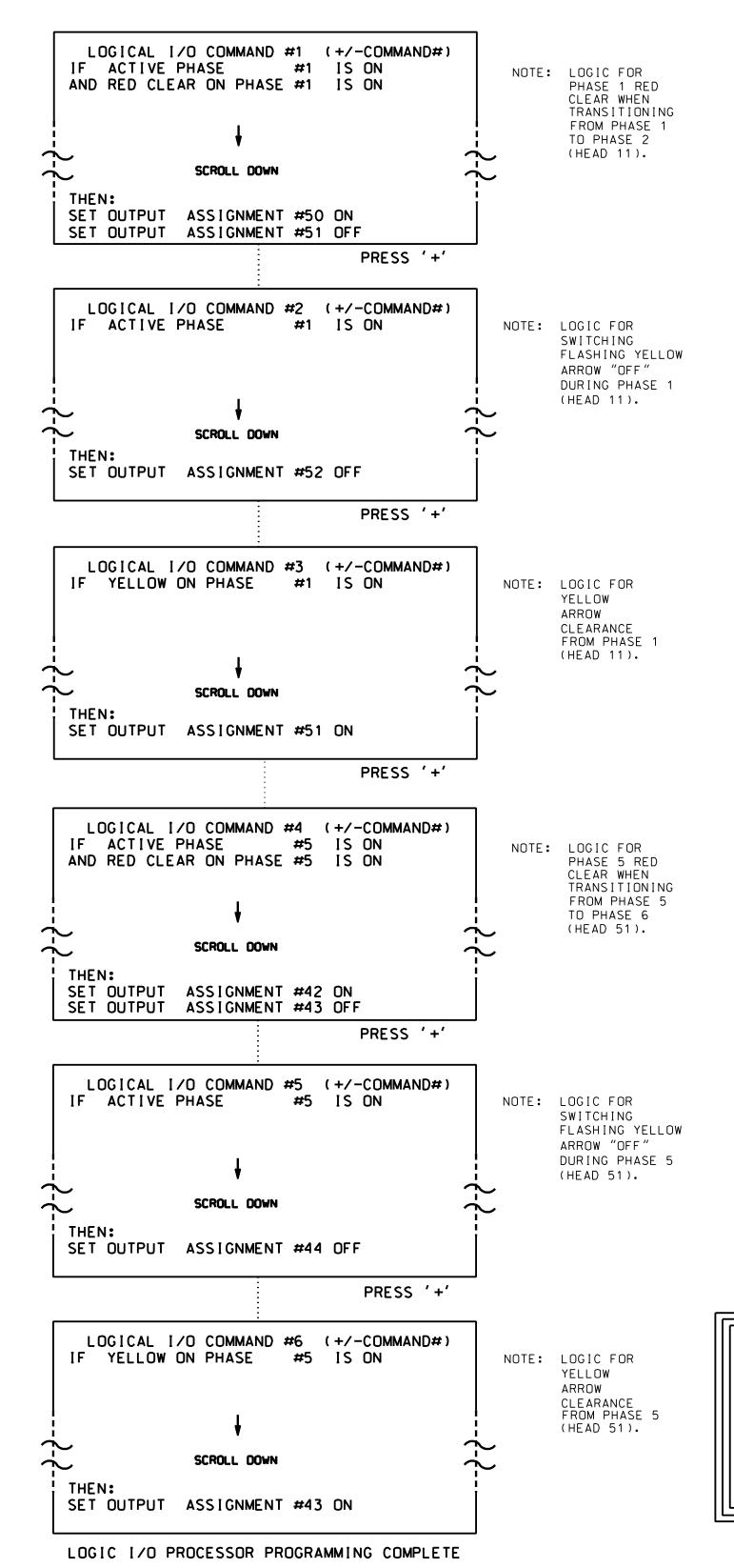
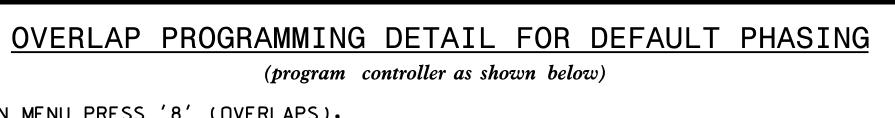
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).





FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PAGE 1: VEHICLE OVERLAP 'A' SETTINGS PAGE 1: VEHICLE OVERLAP 'C' SETTINGS PHASE: 12345678910111213141516 ÷12345678910111213141516 VEH OVL PARENTS: VEH OVL PARENTS: :XX VEH OVL NOT VEH: : VEH OVL NOT VEH: VEH OVL NOT PED: : VEH OVL NOT PED: 1 VEH OVL GRN EXT: VEH OVL GRN EXT: : STARTUP COLOR: _ RED _ YELLOW _ GREEN STARTUP COLOR: _ RED _ YELLOW FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN FLASH YELLOW IN CONTROLLER FLASH?...Y FLASH FLASH YELLOW IN CONTROLLER FLASH?...Y FLASH GREEN EXTENSION (0-255 SEC)..... GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 PRESS '+' PRESS '+' PAGE 1: VEHICLE OVERLAP 'B' SETTINGS PAGE 1: VEHICLE OVERLAP 'D' SETTINGS PHASE: ¦12345678910111213141516 12345678910111213141516 VEH OVL PARENTS: X VEH OVL PARENTS: | XX VEH OVL NOT VEH: VEH OVL NOT VEH: ; VEH OVL NOT PED: VEH OVL NOT PED: : VEH OVL GRN EXT: VEH OVL GRN EXT: STARTUP COLOR: _ RED _ YELLOW _ GREEN STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE **←** NOTICE SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N FLASH FLASH FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)..... GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 PRESS '+' OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

GREEN

FLASH

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2. PAGE 2: VEHICLE OVERLAP 'A' SETTINGS 12345678910111213141516 PAGE 2 PHASE: PAGE 2 VEH OVL PARENTS: X VEH OVL NOT VEH: | VEH OVL NOT PED: VEH OVL GRN EXT: : STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW _ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 PRESS '+' PAGE 2: VEHICLE OVERLAP 'B' SETTINGS NOTICE NOTICE -PAGE 2 12345678910111213141516 PAGE 2 PHASE: VEH OVL PARENTS: X VEH OVL NOT VEH: | VEH OVL NOT PED: VEH OVL GRN EXT:: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS PHASE: 112345678910111213141516 VEH OVL PARENTS: VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: : STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW _ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 PRESS '+'

PAGE 2: VEHICLE OVERLAP 'D' SETTINGS 12345678910111213141516 VEH OVL PARENTS: | XX VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: : STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0

OVERLAP PROGRAMMING COMPLETE

OUTPUT AS PHASE # (0=NONE, 1-16)....0

OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow

OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0977 DESIGNED: June 2017 SEALED: 9/10/2021 REVISED: N/A

PRESS '+'

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)

YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0

RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0

FLASH YELLOW IN CONTROLLER FLASH?...N

GREEN EXTENSION (0-255 SEC).....

OUTPUT AS PHASE # (0=NONE, 1-16)....0

750 N.Greenfield Pkwy, Garner, NC 27529

ELECTRICAL AND PROGRAMMING

Electrical Detail - Sheet 2 of 5 Signal Upgrade **DOCUMENT NOT CONSIDERED FINAL** Final Design **UNLESS ALL SIGNATURES COMPLETED**

DETAILS FOR: NC 211 (Southport-Supply Road) St James Drive oivision 03 Brunswick Co. June 2017 PLAN DATE: PREPARED BY: A.H. Thornburg | REVIEWED BY: N.R. Simmons REVISIONS

TH CARO, 031464 Southport REVIEWED BY: A.D. Klinksiek MOINEER INIT. DATE

GREEN

FLASH

SIG. INVENTORY NO. 03-0977

PROJECT REFERENCE NO.

R-5021

SHEET NO

Sig 13

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