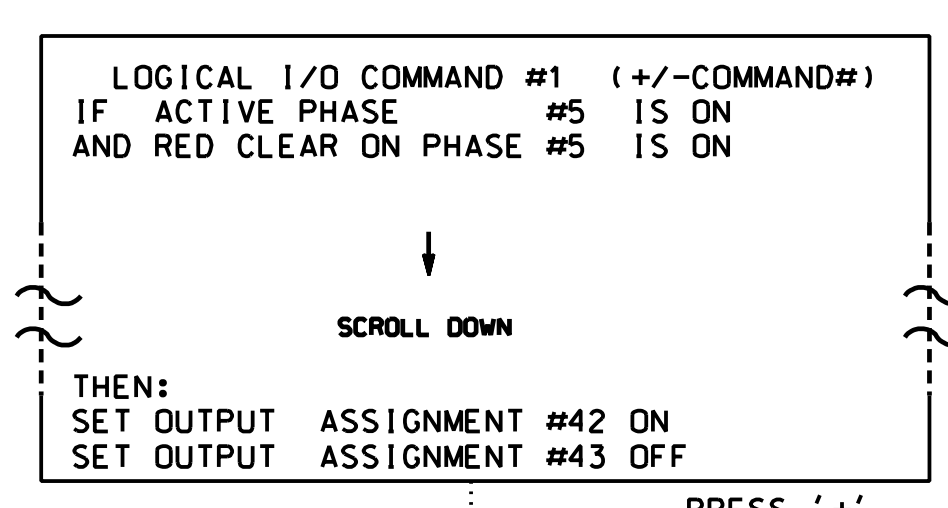


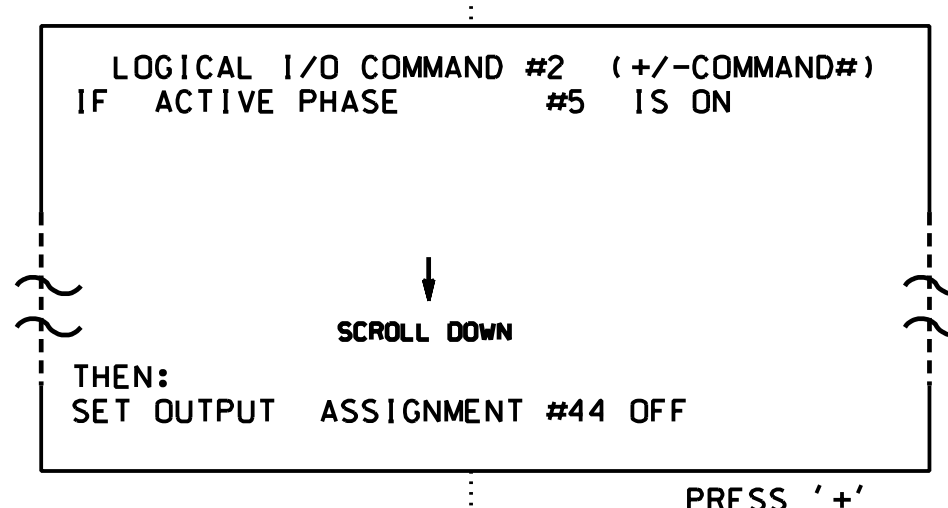
### LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

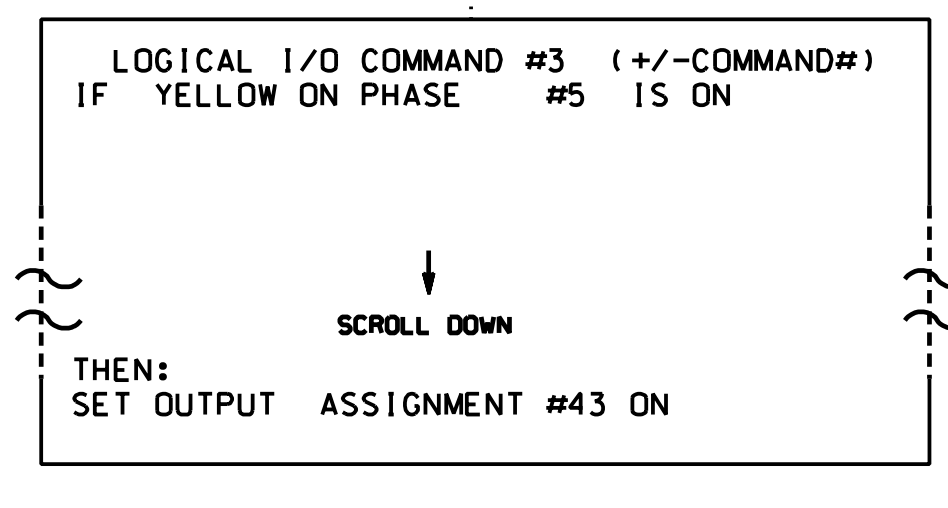
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.



NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEADS 51.52).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEADS 51.52).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEADS 51.52).

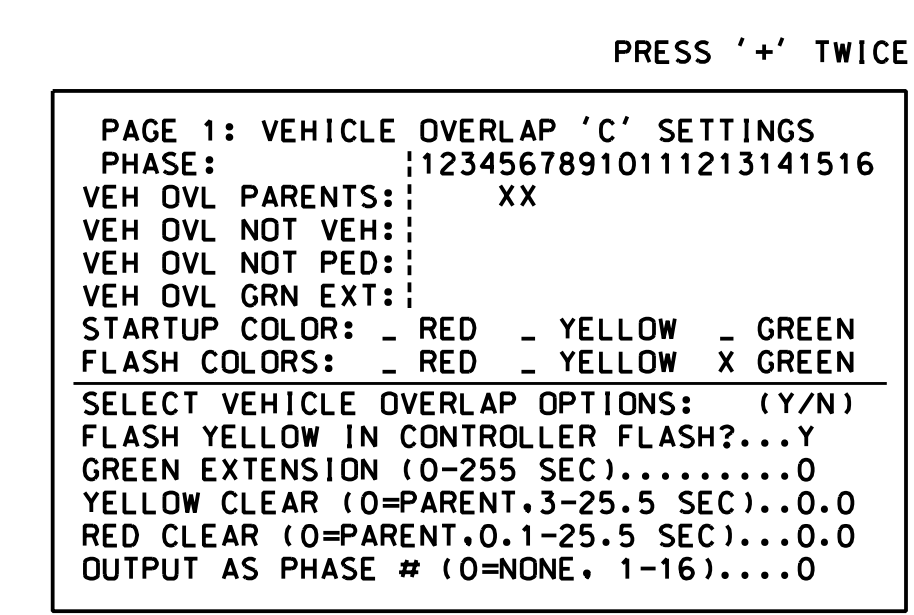
LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

<u>OUTPUT REFERENCE SCHEDULE</u>	
USE TO INTERPRET LOGIC PROCESSOR	
OUTPUT 42 = Overlap C Red	
OUTPUT 43 = Overlap C Yellow	
OUTPUT 44 = Overlap C Green	

### OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



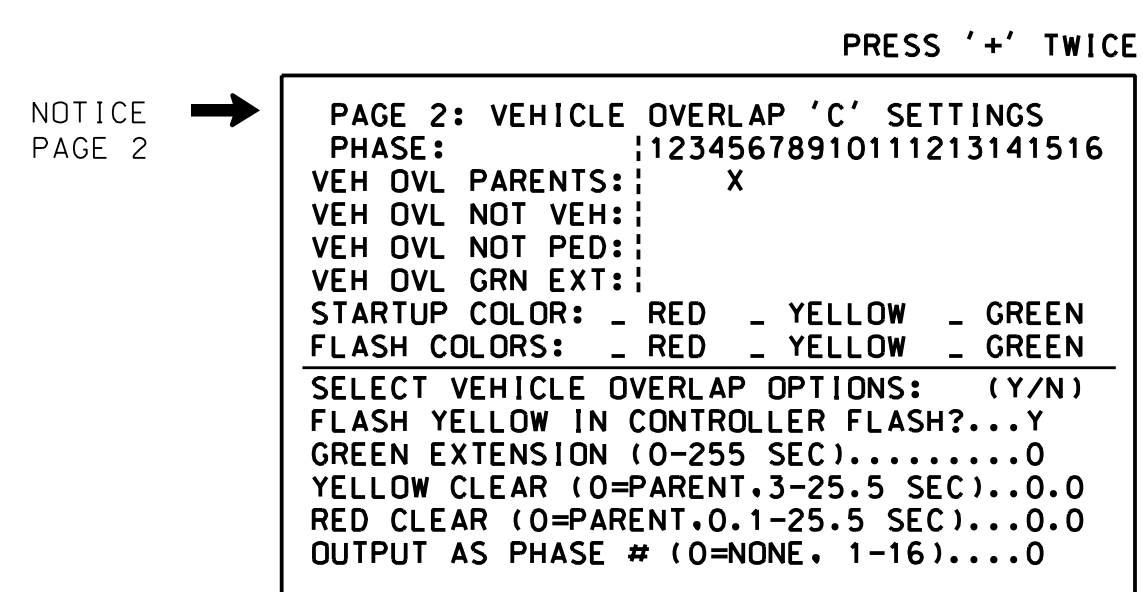
← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

### OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.



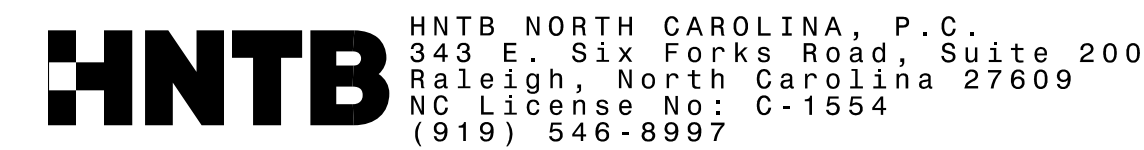
→ NOTICE PAGE 2

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-1123  
DESIGNED: June 2017  
SEALED: 9/10/2021  
REVISED: N/A

Electrical Detail - Sheet 2 of 4  
New Installation  
Final Design

**DOCUMENT NOT CONSIDERED FINAL  
UNLESS ALL SIGNATURES COMPLETED**



	NC 906 (Middleton Boulevard) at NC 211 Southbound Ramp	SEAL NORTH CAROLINA PROFESSIONAL ENGINEER MAYASHA R. SIMMONS
	Prepared for: PLAN DATE: June 2017 PREPARED BY: A.H. Thornburg	Reviewed by: A.D. Klinksiek REVIEWED BY: N.R. Simmons