## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER |
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

Toggle Until Positioned on Overlap G

## OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .....PPLT FYA

PROTECTED LEFT TURN.... PHASE 1
OPPOSING THROUGH..... OVERLAP G

FLASHING ARROW OUTPUT....CH9 ISOLATE

DELAY START OF: FYA..O.O CLEARANCE..O.O
ACTION PLAN SF BIT DISABLE.......

END PROGRAMMING

## FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH. MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

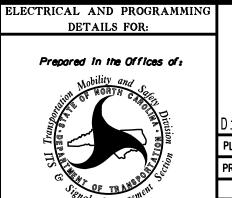
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0274T1 DESIGNED: June 2019 SEALED: 6/5/2019 REVISED: N/A

Temporary Design 1 - TMP Phase I Electrical Detail - Sheet 2 of 2



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US 401 (Raeford Road) at NC 162 (Bunce Road)/ SR 1411 (Bunce Road)

SR 1411 (Bunce Road)

Division 6 Cumberland County Fayetteville

PLAN DATE: June 2019 REVIEWED BY: L Overn

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PREPARED BY: G B Spell REVIEWED BY:

REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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SIG. INVENTORY NO. 06-0274TI

J:\*Traffic\*Signals\*Design\*Electrical Details – U-4405A\*Temporary Signals\*U-4405A\_sig Jser:jhambright