1. From Main Menu select 5. TIME BASE

2. From TIME BASE Submenu select 2. ACTION PLAN

ACTION PLAN...[1] PATTERN..... AUTO SYS OVERRIDE.... NO TIMING PLAN..... O SEQUENCE..... O VEH DETECTOR PLAN.. 2 DET LOG.....NONE FLASH..... -- RED REST..... NO VEH DET DIAG PLN... O PED DET DIAG PLN...O DIMMING ENABLE.. NO PRIORITY RETURN. NO PED PR RETURN.. NO QUEUE DELAY.... NO PMT COND DELAY NO PHASE 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 PHASE 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 SPC FCT X . . . X . . . (1-8) AUX FCT . . (1-3) 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

END PROGRAMMING

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

U-4405A SIG-3.2

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING FREE RUN — PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN,

SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED

TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

TO RUN ALT, PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

| PHAS I NG | VEH DET PLAN | SF BITS ENABLED |
|--|--------------|-----------------|
| ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u> | 1 | NONE |
| ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u> | 2 | 1, 5 |
| | | |

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE

TO CALL THE "ALTERNATE PHASING":

SF BITS 1.5: Modifies overlap parent phases for heads 11 and 51 to

run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1

call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0845

DESIGNED: July 2021

SEALED: 7/9/2021

REVISED: N/A

THIS PLAN SUPERSEDES THE PLAN SIGNED AND SEALED ON 06/05/2019

Final Design

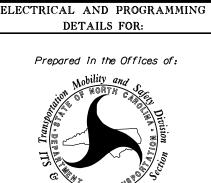
Electrical Detail - Sheet 2 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



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US 401 (South Raeford Road)

at

SR 3569 (Raeford Rd
Old US 401)/Bentridge In

Old US 401)/Bentridge Ln.

Division 6 Cumberland County Fayetteville

Division 6 Cumberland County Fayetteville
PLAN DATE: July 2021 REVIEWED BY: L Overn

PREPARED BY: R M Muncey REVIEWED BY:

REVISIONS INIT. DATE

SEAL
43239

NG INE ELLEGINA

Rigina M. MUZNAKA021

Regina M. Manaka021

SIG. INVENTORY NO. 06-0845

9:55:29 AM U:*Traffic*Signals*Design*Elect User:jhambright