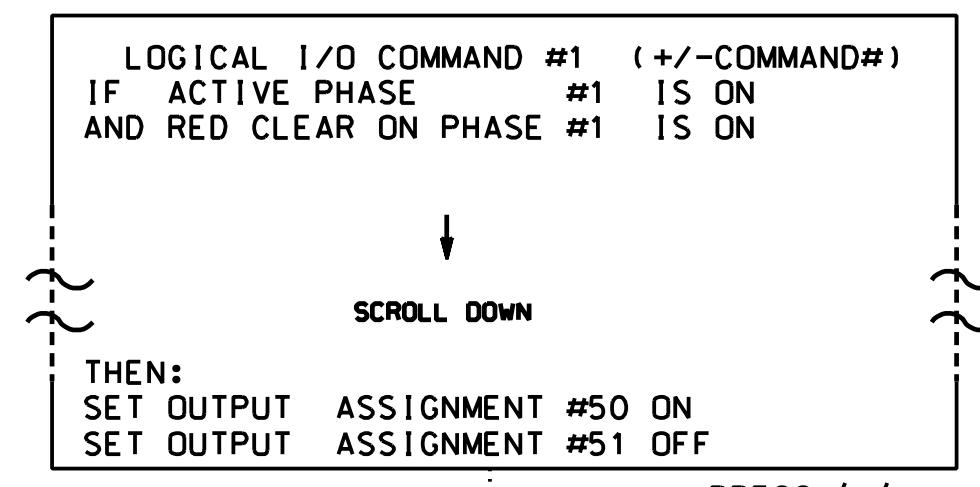


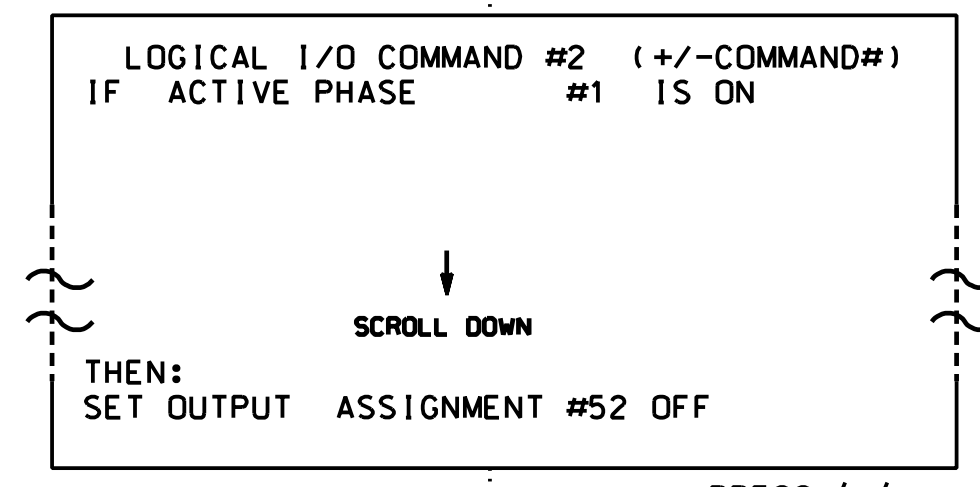
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

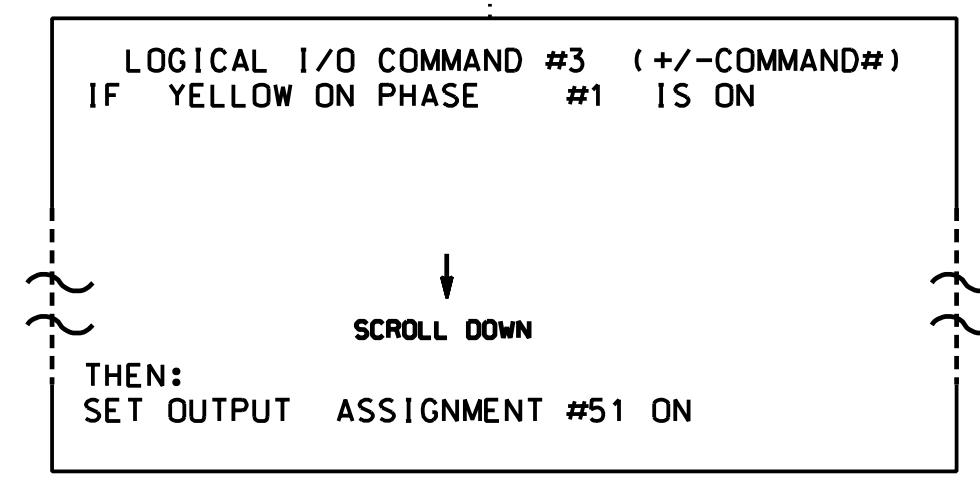
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 1 (HEAD 11).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

<u>OUTPUT REFERENCE SCHEDULE</u>	
USE TO INTERPRET LOGIC PROCESSOR	
OUTPUT 50	= Overlap A Red
OUTPUT 51	= Overlap A Yellow
OUTPUT 52	= Overlap A Green

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

    PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
    PHASE: :12345678910111213141516
    VEH OVL PARENTS: :XX
    VEH OVL NOT VEH: :
    VEH OVL NOT PED: :
    VEH OVL GRN EXT: :
    STARTUP COLOR: - RED - YELLOW - GREEN
    FLASH COLORS: - RED - YELLOW X GREEN
    SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...Y
    GREEN EXTENSION (0-255 SEC)...0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

NOTICE PAGE 2 →

```

    PAGE 2: VEHICLE OVERLAP 'A' SETTINGS
    PHASE: :12345678910111213141516
    VEH OVL PARENTS: :X
    VEH OVL NOT VEH: :
    VEH OVL NOT PED: :
    VEH OVL GRN EXT: :
    STARTUP COLOR: - RED - YELLOW - GREEN
    FLASH COLORS: - RED - YELLOW - GREEN
    SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...Y
    GREEN EXTENSION (0-255 SEC)...0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

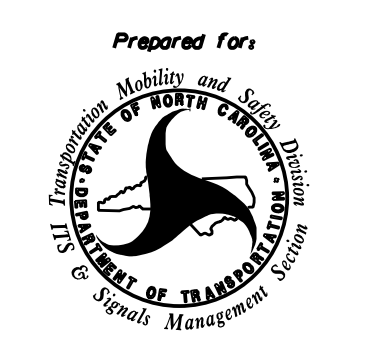
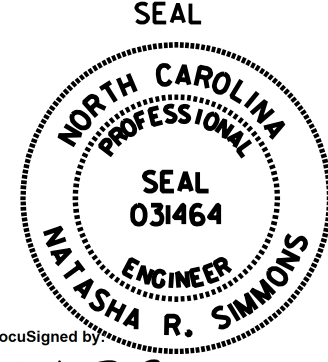
OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0889
 DESIGNED: October 2018
 SEALED: 6/30/2021
 REVISED: N/A

Electrical Detail - Sheet 2 of 4
 Signal Upgrade

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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	Prepared for: SR 1308 (Gum Branch Road) at SR 1324 (Ramsey Road)		
	Division 03 PLAN DATE: October 2018 PREPARED BY: A.H. Thornburg	Onslow Co. REVIEWED BY: A.D. Klinksiek REVIEWED BY: N.R. Simmons	
REVISIONS			INIT. DATE
SIGNATURE			DATE
750 N. Greenfield Pkwy, Corner, NC 27529			SIG. INVENTORY NO. 03-0889