

## ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING COORDINATION - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED, NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

<u>PHASING</u>	<u>INPUTS PAGE</u>	<u>OVERLAPS PAGE</u>
ACTIVE PAGES REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	1
ACTIVE PAGES REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	2

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

### ALTERNATE PHASING PAGE CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN THESE OVERLAP/INPUT PAGE CHANGES ACTIVATE TO CALL THE "ALTERNATE PHASING":

- OVERLAPS PAGE 2: Modifies overlap parent phases for heads 11, 31, 51, and 71 to run protected turns only.
- INPUTS PAGE 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 3 seconds.
- Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds.
- Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.
- Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

## FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-1029  
 DESIGNED: October 2018  
 SEALED: 6/30/2021  
 REVISED: N/A

Electrical Detail - Sheet 7 of 7  
Signal Upgrade

**DOCUMENT NOT CONSIDERED FINAL  
UNLESS ALL SIGNATURES COMPLETED**

<p style="font-size: small;">ELECTRICAL AND PROGRAMMING DETAILS FOR:</p> <p style="font-size: x-small;">Prepared for:                    Department of Transportation                  Mobility and Safety Division                  State of North Carolina</p> <p style="font-size: x-small;">750 N. Greenfield Pkwy, Corner, NC 27529</p>	<p>SR 1308 (Gum Branch Road) at                  SR 1316 (Rhodestown Road)/                  SR 2710 (Stateside Boulevard)</p> <p>Division 03      Onslow Co.      Jacksonville</p> <p>PLAN DATE: October 2018      REVIEWED BY: A.D. Klinksiek</p> <p>PREPARED BY: A.H. Thornburg      REVIEWED BY: N.R. Simmons</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="font-size: x-small;">REVISIONS</th> <th style="font-size: x-small;">INIT.</th> <th style="font-size: x-small;">DATE</th> </tr> </thead> <tbody> <tr><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td></tr> </tbody> </table>	REVISIONS	INIT.	DATE										<p style="text-align: center;">SEAL</p> <p style="text-align: center;"></p> <p style="font-size: x-small;">DocuSigned by:                  Natasha R. Simmons                  6/30/2021                  6/30/2021                  SIGNATURE      DATE                  SIG. INVENTORY NO. 03-1029</p>
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