PROJECT REFERENCE NO. Sig.14.2 R-5020B

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'OTHER/ECONOLITE'

TMG VEH OVLP...[A] TYPE:OTHER/ECONOLITE PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0

Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O

END PROGRAMMING

ACTION PLAN SF BIT DISABLE..... 0

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1251 DESIGNED: November 2012 SEALED: 12-18-12 REVISED: 05/14/2020

ELECTRICAL DETAIL SHEET 2 OF 2 Prepared in the Offices of:

ELECTRICAL AND PROGRAMMING US 701 (James B. White Highway) Columbus Corners Drive

Columbus County PLAN DATE: 12-06-12 REVIEWED BY: PREPARED BY: D. H. Spaulding Reviewed BY: REVISIONS INIT. DATE ...MBC... .05/14/20 Controller..changed..to..ASC/3...

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document.

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SIGNATURES COMPLETED

SEAL

SIG. INVENTORY NO. 06-1251

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Revision ▽ Seal

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