ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

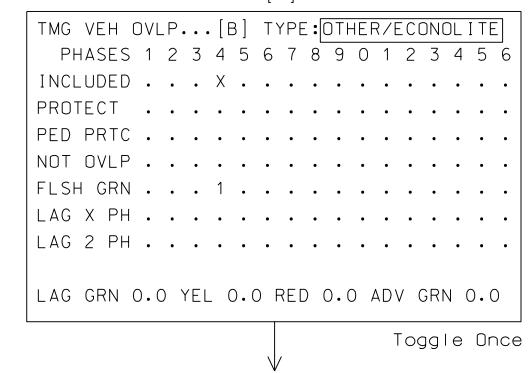
- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP[A] TYPE:PPL	T FYA					
PROTECTED LEFT TURN PHASE OPPOSING THROUGH PHASE						
FLASHING ARROW OUTPUTCH9 ISO	DLATE					
DELAY START OF: FYAO.O CLEARANCE ACTION PLAN SF BIT DISABLE						
Togg	le Once					
OVERLAP B						

Select TMG VEH OVLP [B] and 'OTHER/ECONOLITE'



Select TMG VEH OVLP [C] and 'PPLT FYA'

OVERLAP C

TMG VEH OVLP[C] TYPE:PPLT	FYA
PROTECTED LEFT TURN PHASE OPPOSING THROUGH PHASE	
FLASHING ARROW OUTPUTCH11 ISOL	_ATE
DELAY START OF: FYAO.O CLEARANCE ACTION PLAN SF BIT DISABLE	
ToggI	e Once
OVERLAP D	

Select TMG VEH OVLP [D] and 'OTHER/ECONOLITE'

TMG VEH OV	LP	. [D]	TYP	E:OT	HER/	ECON	OL I TE	
PHASES 1	2 3	4 5	6 7	8 9	0 1	2 3	4 5 6	
INCLUDED .				8.				
PROTECT .								
PED PRTC .								
NOT OVLP .								
FLSH GRN .				1.				
LAG X PH .								
LAG 2 PH .								
LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0								

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

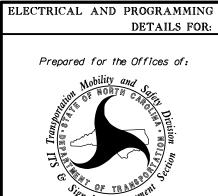
THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0514T2 DESIGNED: November 2019 SEALED: 05/15/2020 REVISED: N/A



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Temporary Design 2 - (TMP Phase 1A) Electrical Detail - Sheet 2 of 2



US 701 Bypass/NC 130 (N. J.K. Powell Boulevard)

Burkhead Street Columbus County PLAN DATE: December 2019 REVIEWED BY:

REVISIONS

PREPARED BY: M.B. Copple | REVIEWED BY: GG Murr Jr INIT. DATE

Matthew Copple

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED