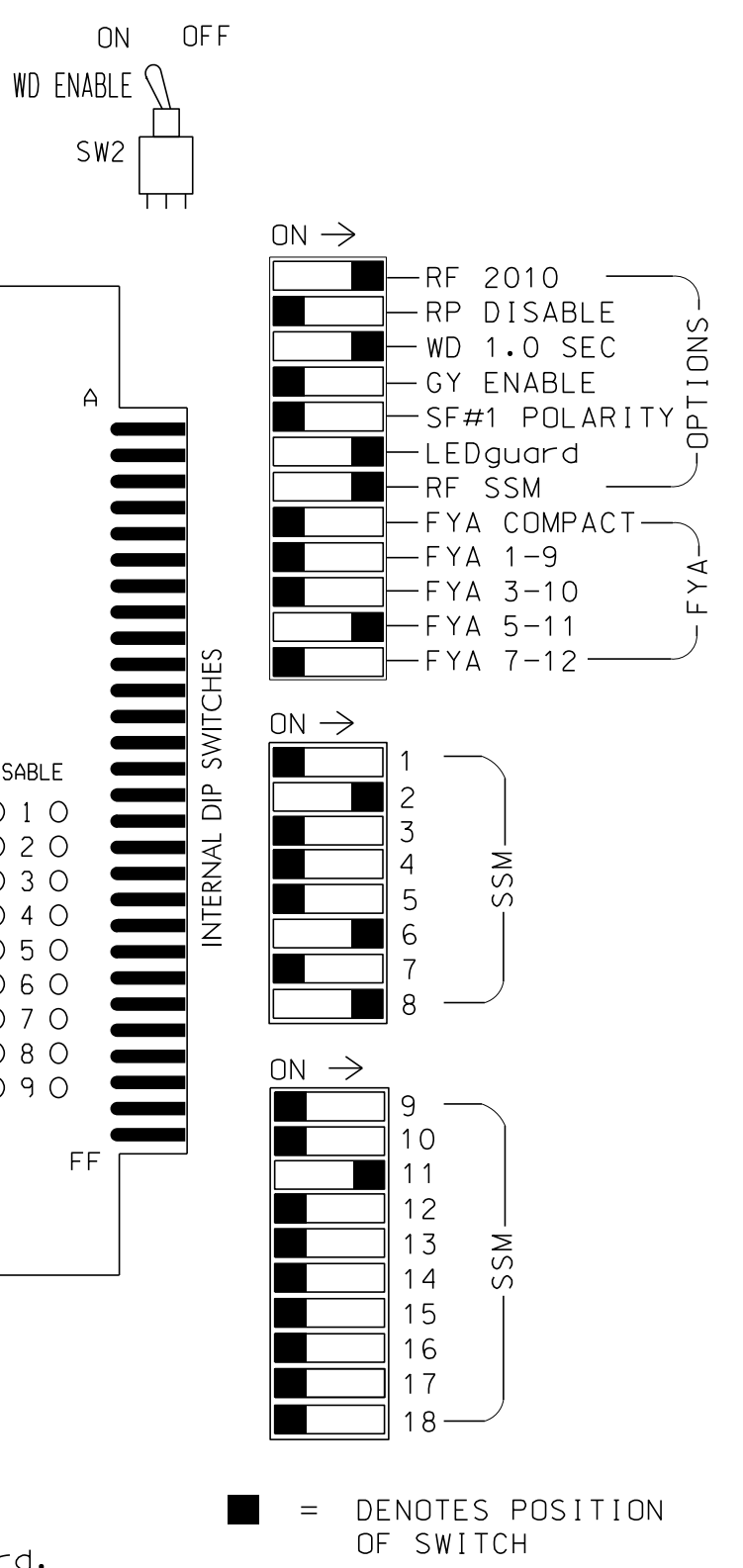
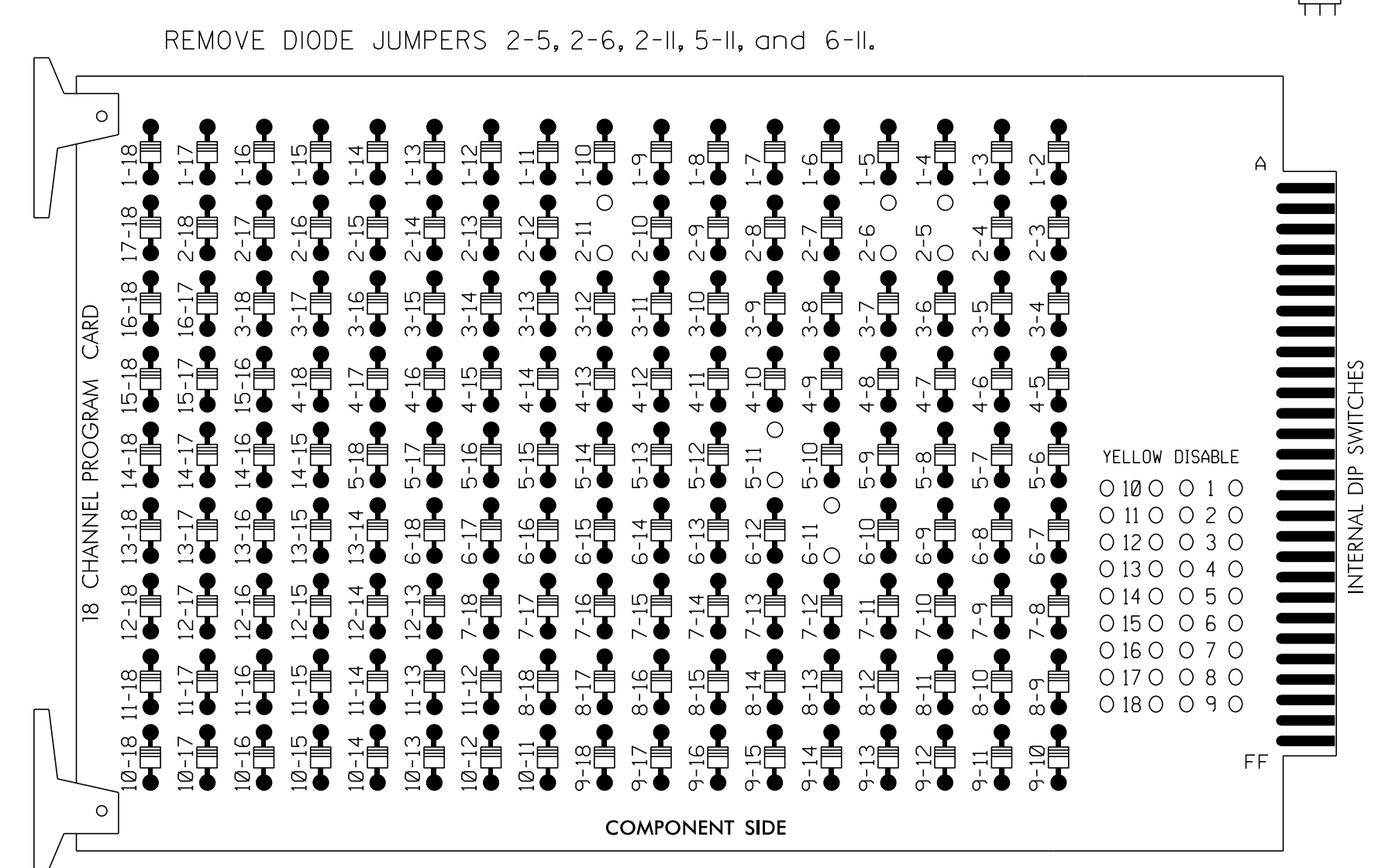


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program controller to start up in phase 2 Green and 6 Green.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all enabled detectors.
- The cabinet and controller are part of the Signal System #D06-Q2 Dunn.

EQUIPMENT INFORMATION

CONTROLLER.....2070LX
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE ASC3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 W/ AUX OUTPUT FILE

LOAD SWITCHES USED.....S2,S7,S8,S11,AUX S4
 PHASES USED.....2,5,6,8,
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....*
 OVERLAP "D".....NOT USED

* See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

| LOAD SWITCH NO. | S1 | S2 | S3 | S4 | S5 | S6 | S7 | S8 | S9 | S10 | S11 | S12 | AUX S1 | AUX S2 | AUX S3 | AUX S4 | AUX S5 | AUX S6 | |
|-----------------------|----|-------|-------|----|----|-------|-----|-------|-------|-----|-----|-------|--------|--------|--------|--------|--------|--------|------|
| CMU CHANNEL NO. | 1 | 2 | 13 | 3 | 4 | 14 | 5 | 6 | 15 | 7 | 8 | 16 | 9 | 10 | 17 | 11 | 12 | 18 | |
| PHASE | 1 | 2 | 2 PED | 3 | 4 | 4 PED | 5 | 6 | 6 PED | 7 | 8 | 8 PED | OLA | OLB | SPARE | OLC | OLD | SPARE | |
| SIGNAL HEAD NO. | NU | 21,22 | NU | NU | NU | NU | 51* | 61,62 | NU | NU | 81 | 82,83 | NU | NU | NU | 51* | NU | NU | |
| RED | | 128 | | | | | | 134 | | | 107 | | | | | | | | |
| YELLOW | | 129 | | | | | * | 135 | | | 108 | | | | | | | | |
| GREEN | | 130 | | | | | | 136 | | | 109 | | | | | | | | |
| RED ARROW | | | | | | | | | | | 107 | | | | | | | A114 | |
| YELLOW ARROW | | | | | | | | | | | 108 | | | | | | | | A115 |
| FLASHING YELLOW ARROW | | | | | | | | | | | | | | | | | | | A116 |
| GREEN ARROW | | | | | | | | 133 | | | 109 | | | | | | | | |

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

| FILE "I" | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | FILE "J" |
|----------|----------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-------------|
| U | S | ∅ 2 | ∅ 2 | ∅ 2 | ∅ 2 | ∅ 2 | ∅ 2 | ∅ 2 | ∅ 2 | ∅ 2 | ∅ 2 | ∅ 2 | ∅ 2 | ∅ 2 | FS |
| L | 2A | 2A | 2B | 2C | 2D | 2E | 2F | 2G | 2H | 2I | 2J | 2K | 2L | 2M | DC ISOLATOR |
| U | ∅ 5 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ST |
| L | 5A | 6A | 6B | 6C | 6D | 6E | 6F | 6G | 6H | 6I | 6J | 6K | 6L | 6M | DC ISOLATOR |
| U | NOT USED | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | ∅ 6 | |
| L | 6B | 6B | 6B | 6B | 6B | 6B | 6B | 6B | 6B | 6B | 6B | 6B | 6B | 6B | |

EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

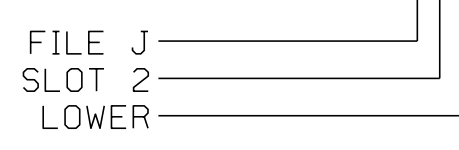
⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO. | LOOP TERMINAL | INPUT FILE POS. | PIN NO. | DETECTOR NO. | NEMA PHASE | CALL | EXTEND TIME | DELAY TIME | ADDED INITIAL | DETECTOR TYPE |
|-----------------|---------------|-----------------|---------|--------------|------------|------|-------------|------------|---------------|---------------|
| 2A | TB2-5,6 | I2U | 39 | 2 | 2 | YES | | | | N |
| 2B | TB2-7,8 | I2L | 43 | 12 | 2 | YES | | | | N |
| 5A ³ | TB3-1,2 | J1U | 55 | 5 | 5 | YES | | 15 | | N |
| | | I4U | 47 | 22 | 2 | YES | | | | N |
| 6A | TB3-5,6 | J2U | 40 | 6 | 6 | YES | | | | N |
| 6B | TB3-7,8 | J2L | 44 | 16 | 6 | YES | | | | N |
| 8A | TB5-9,10 | J6U | 42 | 8 | 8 | YES | | | | N |
| 8B | TB5-11,12 | J6L | 46 | 18 | 8 | YES | | | | N |
| 8C | TB7-1,2 | J7U | 66 | 38 | 8 | YES | | 15 | | N |

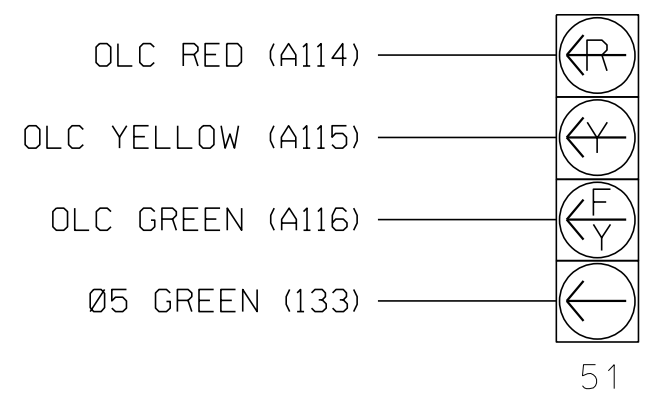
³Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



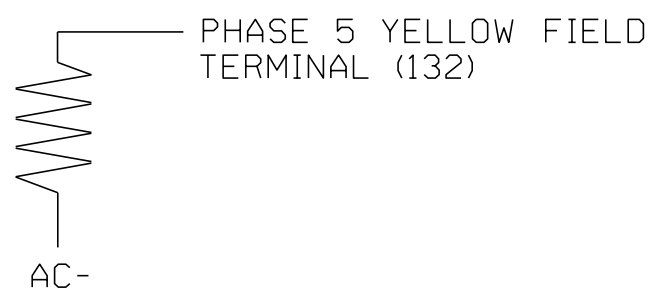
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

| ACCEPTABLE VALUES | |
|-------------------|-----------|
| VALUE (ohms) | WATTAGE |
| 1.5K - 1.9K | 25W (min) |
| 2.0K - 3.0K | 10W (min) |



Electrical Detail - Sheet 1 of 2

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Prepared for:

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US 421 - NC 55
 at
 I-95 NB Ramp/CD Road

Division 6 Harnett County Dunn
 PLAN DATE: March 2021 REVIEWED BY: W M Yalch
 PREPARED BY: K M Cory REVIEWED BY:

| REVISIONS | INIT. | DATE |
|-----------|-------|------|
| | | |

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

 Kelly M Cory 5/9/2021
 DATE

*****ACTUAL ESESS *****
 *****DO NOT SIGN *****
 *****USER NAME *****