## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

Toggle Once

## OVERLAP B

Select TMG VEH OVLP [B] and 'NORMAL'

TMG VEH OVLP...[B] TYPE: ......NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 INCLUDED . X . . . . . X . . . . . . LAG GRN 0.1 YEL 4.5 RED 1.3 Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'NORMAL'

TMG VEH OVLP...[C] TYPE: ......NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.1 YEL 4.5 RED 1.3 Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'NORMAL'

TMG VEH OVLP...[D] TYPE: .....NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 INCLUDED . . . . . . X . . . . . . LAG GRN 0.0 YEL 0.0 RED 0.0

END PROGRAMMING

# ECONOLITE ASC/3-2070 CONTROLLER SEQUENCE PROGRAMMING DETAIL

### (program controller as shown)

- 1. From Main Menu select | 1. CONFIGURATION |
- 2. From CONFIGURATION Submenu select 1. CONTROLLER SEQ
- 3. From CONTROLLER SEQUENCE Submenu select | 1. PHASE RING SEQUENCE AND ASSIGNMENT

CONTROLLER SEQUENCE [ 1] SEQUENCE COMMANDS . HW ALT SEQ ENA. R1-R4=RING 1-4, DATA ENTRY, PHASES 1-16 BC=BARRIER CONTROL, VALUES; B,C B=BARRIER MODE C=COMPATIBILITY MODE

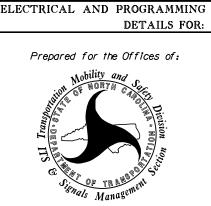
END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø1-Ø674 DESIGNED: September 2018 SEALED: 09/21/2018 REVISED: N/A

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PLANS PREPARED BY: RKSK

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Pasquotank County Elizabeth City PLAN DATE: September 2018 REVIEWED BY: J O Deaton PREPARED BY: M W Yalch REVIEWED BY: REVISIONS

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

James O. Deaton SIG. INVENTORY NO. 01-0674