DocuSign Envelope ID: 298FF5ED-1578-4C5D-8EBD-DAA267E00380

PROJECT REFERENCE NO.	SHEET NO.
U-5942	Sig. 1.3

ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL FOR LEADING PED INTERVAL (DELAYED GREEN) & PREEMPT ONLY PHASE OMIT

(program controller as shown)

The following logic processor configuration holds the FYA's on signal heads 31 and 71 red for the duration of the delayed green time (leading ped interval) when serving a ped call on the opposing through phase.

1. From Main Menu select [1. CONFIGURATION]
2. From CONFIGURATION Submenu select [8. LOGIC PROCESSOR]
3. From the LOGIC PROCESSOR Submenu select [2. LOGIC STATEMENTS]

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```
LP#:   1  COPY FROM:   1  ACTIVE:  M   (T/F)
IF    PED ON PH WALK        4     IS   ON
AND   VEH GREEN ON PH       4     IS   OFF

THEN  SIG SET OLP RED       2          ON
      SIG SET OLP YELLOW    2          OFF
      SIG SET OVLP GREEN    2          OFF

ELSE
```

HOLD SIGNAL HEAD 31 FYA RED DURING THE PHASE 4 DELAYED GREEN TIME (LEADING PED INTERVAL)

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```
LP#:   2  COPY FROM:   2  ACTIVE:  M   (T/F)
IF    PED ON PH WALK        8     IS   ON
AND   VEH GREEN ON PH       8     IS   OFF

THEN  SIG SET OLP RED       4          ON
      SIG SET OLP YELLOW    4          OFF
      SIG SET OVLP GREEN    4          OFF

ELSE
```

HOLD SIGNAL HEAD 71 FYA RED DURING THE PHASE 8 DELAYED GREEN TIME (LEADING PED INTERVAL)

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```
LP#:   3  COPY FROM:   3  ACTIVE:  M   (T/F)
IF    PMT PREEMPT ACTIVE    5     IS   OFF

THEN  CTR OMIT PHASE        3          ON

ELSE
```

LOGIC FOR OMITTING PHASE 3 AT STARTUP AND/OR WHEN NOT IN PREEMPT

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```
LP#:   4  COPY FROM:   4  ACTIVE:  M   (T/F)
IF    PMT PREEMPT ACTIVE    6     IS   OFF

THEN  CTR OMIT PHASE        7          ON

ELSE
```

LOGIC FOR OMITTING PHASE 7 AT STARTUP AND/OR WHEN NOT IN PREEMPT

4. From the LOGIC PROCESSOR Submenu select [1. LOGIC STATEMENT CONTROL]

ENABLE LOGIC PROCESSOR STATEMENTS 1-6 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM .

```
LOGIC STATEMENT CONTROL
          1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
LP 1-15   E E E E . . . . . . . . . . . .
LP 16-30  . . . . . . . . . . . . . . . .
LP 31-45  . . . . . . . . . . . . . . . .
LP 46-60  . . . . . . . . . . . . . . . .
LP 61-75  . . . . . . . . . . . . . . . .
LP 76-90  . . . . . . . . . . . . . . . .
```

END PROGRAMMING

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select [2. CONTROLLER]
2. From CONTROLLER Submenu select [2. VEHICLE OVERLAPS]

OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

```
TMG VEH OVLP...[B] TYPE: .....PPLT FYA

PROTECTED LEFT TURN....   PHASE    3
OPPOSING THROUGH.......   PHASE    4

FLASHING ARROW OUTPUT.....CH10 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```

Toggle Twice

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

```
TMG VEH OVLP...[D] TYPE: .....PPLT FYA

PROTECTED LEFT TURN....   PHASE    7
OPPOSING THROUGH.......   PHASE    8

FLASHING ARROW OUTPUT.....CH12 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```

END PROGRAMMING

ECONOLITE ASC/3-2070 PREEMPT FILTERING PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select [4. PREEMPTOR/TSP]
2. From PREEMPT/TSP/SCP Submenu select [2. ENABLE PREEMPT FILTERING & TSP/SCP]

```
ENABLE PREEMPT FILTERING & TSP/SCP
FILTERED     SOLID          PULSING
INPUT  1 ...BYPASSED.. ...BYPASSED..
       2 ...BYPASSED.. ...BYPASSED..
       3 ..PREEMPT  3. ...BYPASSED..
       4 ..PREEMPT  4. ...BYPASSED..
       5 ..PREEMPT  5. ...BYPASSED..
       6 ..PREEMPT  6. ...BYPASSED..
       7 ...BYPASSED.. ...BYPASSED..
       8 ...BYPASSED.. ...BYPASSED..
       9 ...BYPASSED.. ...BYPASSED..
      10 ...BYPASSED.. ...BYPASSED..
```

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 01-0001
DESIGNED: JUNE 2018
SEALED: 09/20/2018
REVISED: N/A

Plans Prepared By:

DRMP, Inc.
8000 Regency Parkway, Suite 175
Cary, NC 27518
NC License No. C-2213 (919) 650-1038

Electrical Detail - Sheet 2 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of: Transportation Mobility and Safety Division, Traffic Engineering Branch, Signals Management Section, Department of Transportation, State of North Carolina

750 N.Greenfield Pkwy,Garner,NC 27529

US 17 Bus./(W. Ehringhaus St)
SR 1268/(E. Ehringhaus St) at
US 17 Bus./(S. Road St)/
SR 1269 (S. Road St)

Division 1 Pasquotank County Elizabeth City

PLAN DATE:	June 2018	REVIEWED BY:	AJ Davis
PREPARED BY:	DJ White	REVIEWED BY:	LM Moon

REVISIONS	INIT.	DATE

SEAL

NORTH CAROLINA PROFESSIONAL SEAL 022516 ENGINEER LISA M. MOON

DocuSigned by: Lisa M. Moon 9/20/2018
58CE5B5D63DD421... DATE

SIG. INVENTORY NO. 01-0001

20-SEP-2018 13:51
R:\U5942_sig_prop\Design\Wiring\01-0001-20180821e.dgn
lmoon AT CAR-LMOON1-WT