PROJECT REFERENCE NO.	SHEET NO.
B-5980	Sig. 3.2

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE         #3  IS ON
AND RED CLEAR ON PHASE   #3  IS ON
          ↓
      SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #47 ON
SET OUTPUT  ASSIGNMENT #48 OFF
```

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE         #3  IS ON
          ↓
      SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #49 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE      #3  IS ON
          ↓
      SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #48 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #4  (+/-COMMAND#)
IF  ACTIVE PHASE         #7  IS ON
AND RED CLEAR ON PHASE   #7  IS ON
          ↓
      SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #39 ON
SET OUTPUT  ASSIGNMENT #40 OFF
```

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).

PRESS '+'

```
LOGICAL I/O COMMAND #5  (+/-COMMAND#)
IF  ACTIVE PHASE         #7  IS ON
          ↓
      SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #41 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 71).

PRESS '+'

```
LOGICAL I/O COMMAND #6  (+/-COMMAND#)
IF  YELLOW ON PHASE      #7  IS ON
          ↓
      SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #40 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39 = Overlap D Red
OUTPUT 40 = Overlap D Yellow
OUTPUT 41 = Overlap D Green
OUTPUT 47 = Overlap B Red
OUTPUT 48 = Overlap B Yellow
OUTPUT 49 = Overlap B Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
 PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
 PHASE:          ¦12345678910111213141516
VEH OVL PARENTS:¦  XX
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```
 PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
 PHASE:          ¦12345678910111213141516
VEH OVL PARENTS:¦      XX
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-0111T2
DESIGNED: June 2019
SEALED: 3/17/2020
REVISED: N/A

Temporary Design 2 (TMP Phase II)
Electrical Detail Sheet 2 of 2

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ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of: ITS & Signals Management Section, Transportation Mobility and Safety Division, State of North Carolina Department of Transportation
750 N.Greenfield Pkwy,Garner,NC 27529

I-95 Ramps and NC 4
at
NC 4 / NC 48

Division 4 | Nash County | Rocky Mount

PLAN DATE: June 2019	REVIEWED BY: J. O. Deaton
PREPARED BY: A. Ravipati	REVIEWED BY: M. W. Yalch

REVISIONS	INIT.	DATE

NORTH CAROLINA PROFESSIONAL SEAL 07438 ENGINEER JAMES O. DEATON

DocuSigned by: James O. Deaton 40FF8AC430BD40F... 3/17/2020

SIG. INVENTORY NO. 04-0111T2

3/17/2020
P:*60557176 - B-5980*900_CAD_GIS*910_CAD*70_NCDOT_TIP*Traffic*Signals*Design*Signals*Electricals*04-0111T2e.dgn
coxs1