# ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL FOR PREEMPT ONLY PHASE OMIT

(program controller as shown)

The following logic processor configuration holds the FYA's on signal heads 31 and 71 red during normal phasing opperation and only become an active phase when Preemtion calls are activated.

1. From Main Menu select | 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND

PROGRAM AS SHOWN.

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F) IF PMT PREEMT ACTIVE 4 IS OFF THEN CTR OMIT PHASE ELSE

LOGIC FOR OMMITTING PHASE 7 AT STARTUP AND/OR WHEN NOT IN PREEMPT

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#: 2 COPY FROM: 2 ACTIVE: M (T/F) IF PMT PREEMT ACTIVE 6 IS OFF THEN CTR OMIT PHASE ELSE

LOGIC FOR OMMITTING PHASE 3 AT STARTUP AND/OR WHEN NOT IN PREEMPT

END PROGRAMMING

4. From LOGIC PROCESSOR Submenu select | 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1 & 2 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING

THE TOGGLE KEY TO ENABLE THEM.

LOGIC STATEMENT CONTROL 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 

END PROGRAMMING

### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.

2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.

3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select | 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... O Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

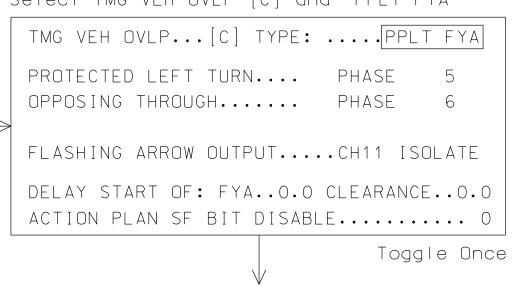
TMG VEH OVLP...[B] TYPE: .....PPLT FYA PROTECTED LEFT TURN.... PHASE 3 OPPOSING THROUGH..... PHASE 4 FLASHING ARROW OUTPUT....CH10 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

8000 Regency Parkway, Suite 175 Cary, NC 27518 NC License No. C-2213 (919) 650-1038

Toggle Once

### OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'



OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA' TMG VEH OVLP...[D] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 7 OPPOSING THROUGH..... PHASE 8 FLASHING ARROW OUTPUT....CH12 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

## ECONOLITE ASC/3-2070 PREEMPT

### FILTERING PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select | 4. PREEMPTOR/TSP

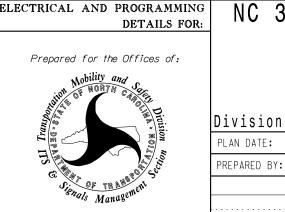
2. From PREEMPT/TSP/SCP Submenu

select | 2. ENABLE PREEMPT FILTERING & TSP/SCP

ENABLE PREEMPT FILTERING & TSP/SCP FILTERED SOLID PULSING INPUT 1 ...BYPASSED.. ...BYPASSED.. 2 ...BYPASSED.. ...BYPASSED.. 3 .. PREEMPT 3. ... BYPASSED.. 4 .. PREEMPT 4. ... BYPASSED.. 5 .. PREEMPT 5. ... BYPASSED.. 6 .. PREEMPT 6. ... BYPASSED.. 7 ...BYPASSED.. ...BYPASSED.. 8 ...BYPASSED.. ...BYPASSED.. 9 ...BYPASSED.. ...BYPASSED.. 10 ...BYPASSED.. ...BYPASSED..

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0755 DESIGNED: MARCH 2018 SEALED: 09/20/2018 REVISED: N/A

Electrical Detail - Sheet 2 of 3



NC 344 (Halstead Blvd. Ext.) Mt. Everest Way/ Mt. Everest Drive

Division 1 Pasquotank County Elizabeth City PLAN DATE: March 2018 REVIEWED BY: AJ Davis PREPARED BY: DJ White REVIEWED BY: LM Moon

INIT. DATE

REVISIONS

022516

**DOCUMENT NOT CONSIDERED** 

FINAL UNLESS ALL

SIGNATURES COMPLETED

Lisa M. Moon SIG. INVENTORY NO. 01-0755