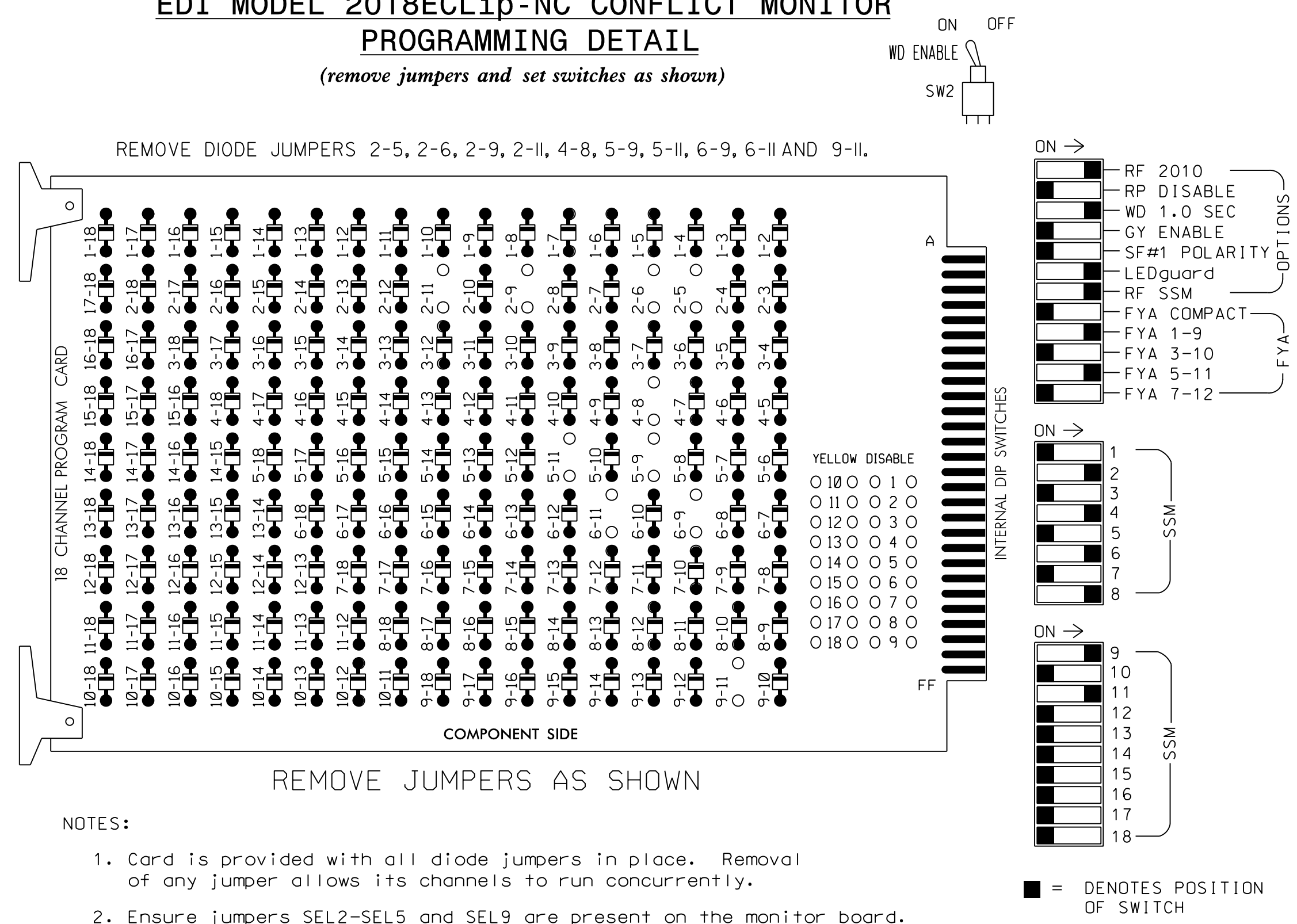


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Elizabeth City Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070LX
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S5,S7,S8,S11,AUX S1, AUX S4
 PHASES USED.....2,4,5,6,8
 OVERLAP "A".....*
 OVERLAP "B".....NOT USED
 OVERLAP "C".....*
 OVERLAP "D".....NOT USED

* See overlap programming detail on sheet 2.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	51	62,63	NU	NU	81,82	NU	61	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW		129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121			A114		
YELLOW ARROW													A122			A115		
FLASHING YELLOW ARROW													A123			A116		
GREEN ARROW								133										

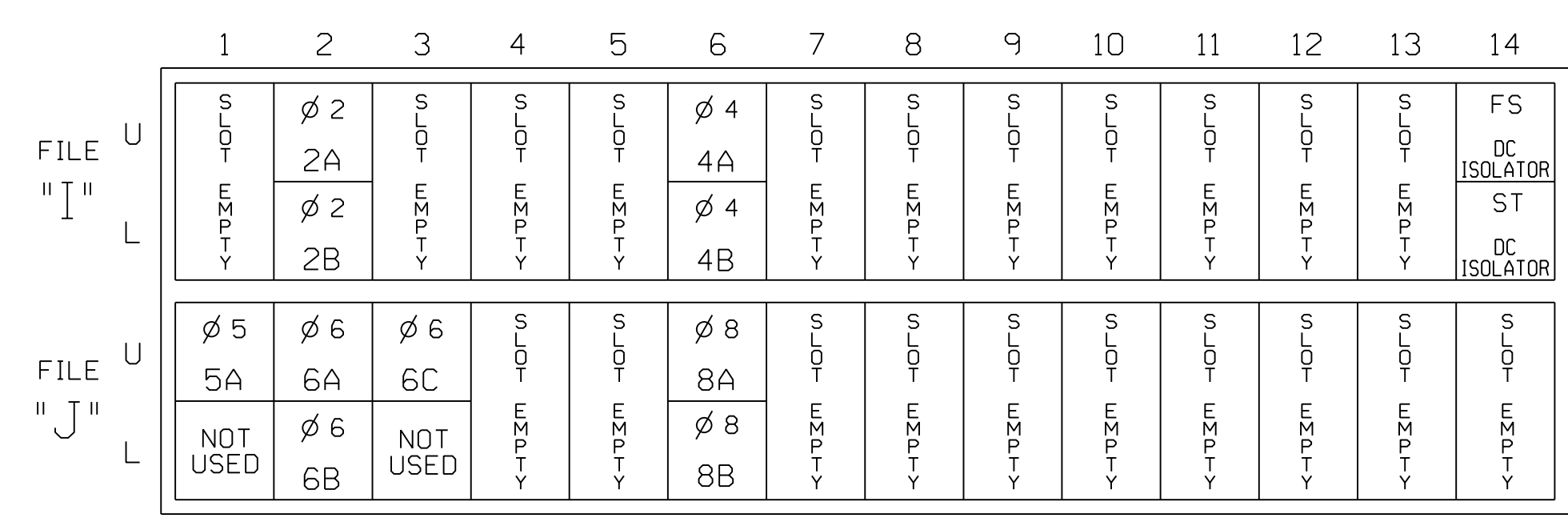
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

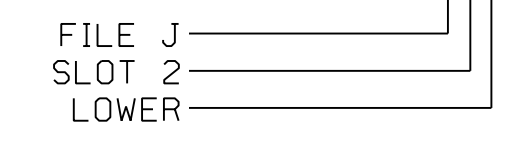


INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
2A	TB2-5,6	I2U	39	2	2	YES			X	N
2B	TB2-7,8	I2L	43	12	2	YES			X	N
4A	TB4-9,10	I6U	41	4	4	YES				S
4B	TB4-11,12	I6L	45	14	4	YES		10		S
5A ¹	TB3-1,2	J1U	55	5	5	YES		15		S
		I4U	47	22	2	YES		3		G
6A	TB3-5,6	J2U	40	6	6	YES			X	N
6B	TB3-7,8	J2L	44	16	6	YES			X	N
6C	TB3-9,10	J3U	64	36	6	YES		3		G
8A	TB5-9,10	J6U	42	8	8	YES		3		S
8B	TB5-11,12	J6L	46	18	8	YES		10		S

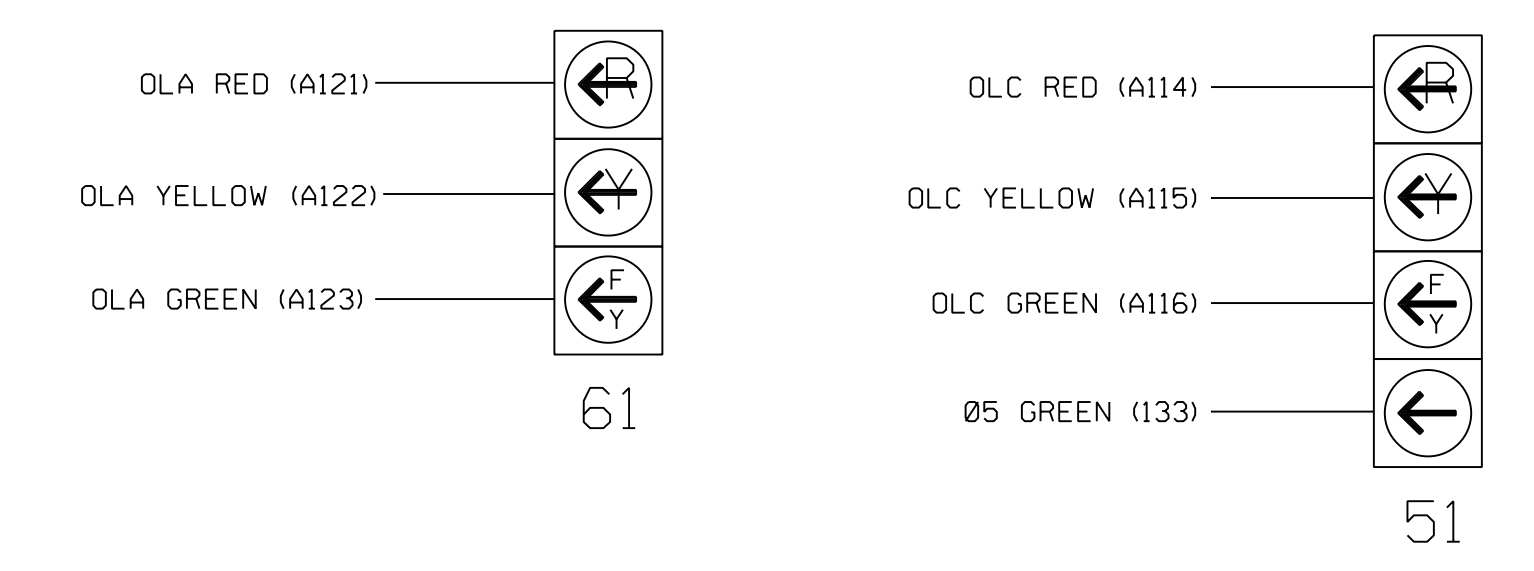
¹Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal head as shown)

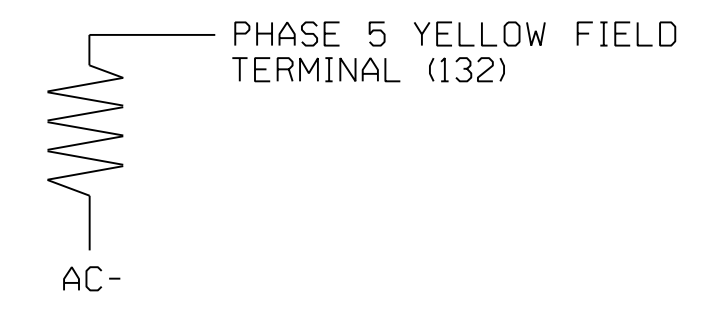


THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0407
 DESIGNED: January 2019
 SEALED: 1/4/2019
 REVISED: N/A

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PLANS PREPARED BY: **RK&K**

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Prepared for the Offices of:

 Pasquotank County Elizabeth City

Division 1
 PLAN DATE: January 2019 REVIEWED BY: J O Deaton
 PREPARED BY: M W Yalch REVIEWED BY:

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL

 SEAL 07438
 ENGINEER JAMES O. DEATON

DocuSigned by: James O. Deaton 1/4/2019
 ADE5AC430B04E DATE
 SIG. INVENTORY NO. 01-0407