Sig. 17.2 U-5942

ECONOLITE ASC/3-2070 ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL PED 4 PROGRAMMING ASSIGNMENT DETAIL (program controller as shown)

(program controller as shown)

1. From Main Menu select 6. DETECTORS

2. From DETECTOR Submenu select 3. PED DETECTOR INPUT ASSIGNMENT

PED DET PHASE ASSIGNMENT MODE: NTCIP NOTICE PED DETECTOR 8 ASSIGNED TO PHASE 4 & 8 PHASE 9 10 11 12 13 14 15 16 DETECTOR O O O O O O

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø1-ØØ22 DESIGNED: FEBRUARY 2018 SEALED: Ø8/21/2018 REVISED: N/A

Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING Prepared for the Offices of:

US 17 Bus. (Ehringhaus St.) Selden St.

Division 1 Pasquotank County Elizabeth City PLAN DATE: February 2018 REVIEWED BY: AJ Davis PREPARED BY: DJ White REVIEWED BY: LM Moon

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

1. From Main Menu select | 2. CONTROLLER | 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS |

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... O Toggle Twice OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

Plans Prepared By:

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REVISIONS INIT. DATE