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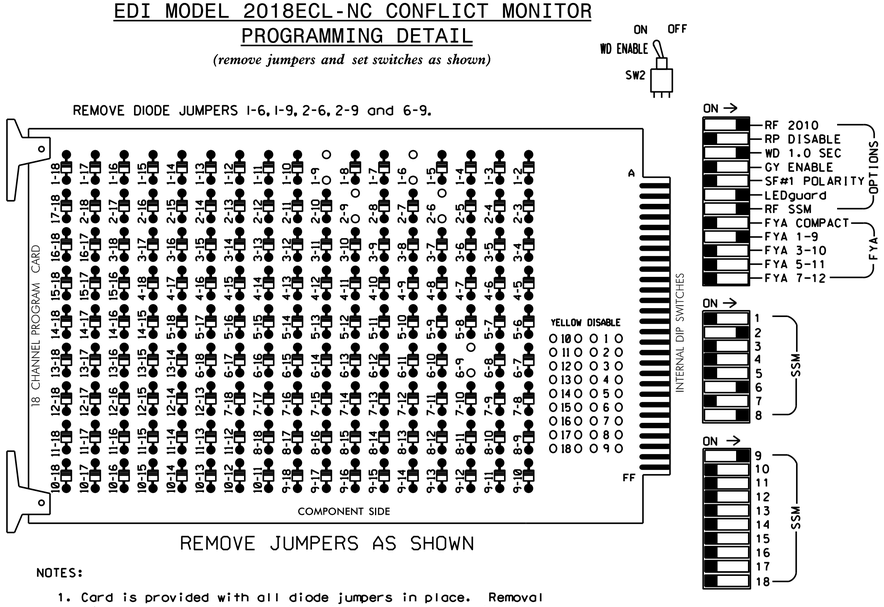
PROJECT REFERENCE NO.	SHEET NO.
R-4707	Sig.4.1

EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the SR 2526 (Summit Avenue) CLS.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	21,22	NU	NU	NU	NU	NU	61,62	NU	NU	81,82	NU	11★	NU	NU	NU	NU	NU
RED		128						134			107							
YELLOW	✱	129						135			108							
GREEN		130						136			109							
RED ARROW													A121					
YELLOW ARROW													A122					
FLASHING YELLOW ARROW													A123					
GREEN ARROW	127																	

NU = Not Used

✱ Denotes install load resistor. See load resistor installation detail this sheet.

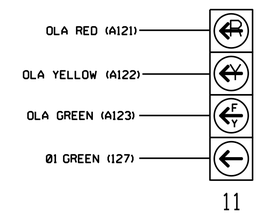
★ See pictorial of head wiring in detail below.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
CABINET.....332 /W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S8,S11,AUX S1
PHASES USED.....1,2,6,8
OVERLAP "A".....1+2
OVERLAP "B".....NOT USED
OVERLAP "C".....NOT USED
OVERLAP "D".....NOT USED

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for this signal requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø 1 / 1A	Ø2/SYS / 2A/S1	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	FS / DC ISOLATOR
	L	NOT USED	NOT USED	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	ST / DC ISOLATOR
FILE "J"	U	SLOT	SLOT	SLOT	WIRED INPUT ⊗	SLOT	Ø 8 / 8A	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT
	L	EMPTY	EMPTY	EMPTY		EMPTY	Ø 8 / 8B	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A[1]	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
2A/S1	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			5
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			15

[1] Add jumper from I1-W to J4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

SPECIAL DETECTOR NOTE

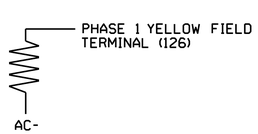
1. Install a video detection system for vehicle detection zones 1A and 6A/S2. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
2. For loop 1A, detector card placement and slot reserved for wired input is typical for a NCDOT installation.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Project #: 180914

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1498T1
DESIGNED: April 2020
SEALED: 04/20/2020
REVISED: N/A

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NCBELS FIRM LICENSE NO. C-2522

Electrical Details - Sheet 1 of 2
Temporary Design 1 (TMP Phase I - Step 2)

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for: Transportation Mobility and Safety Division, Signals Management Section, Department of Transportation, State of North Carolina
750 N.Greenfield Pkwy,Garner,NC 27529

SR 2526 (Summit Avenue)
at
US 29 Southbound Ramps

Division 7 Guilford County Greensboro

PLAN DATE:	April 2020	REVIEWED BY:	L Boyer
PREPARED BY:	K Dean	REVIEWED BY:	

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 030912 ENGINEER LORI M. BOYER
DocuSigned by: Lori M. Boyer
1F0C9ED50A0B12453
04/20/2020
SIGNATURE DATE

SIG. INVENTORY NO. 07-1498T1

$$$$$$SYSTIME$$$$
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