ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... O Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0507 DESIGNED: MARCH 2011 SEALED: 05/15/20 REVISED:

Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING Prepared for the Offices of:

US 74-76 Business (Jefferson Street)

Tram Road/SR 1706 (Spivey Road)

PLAN DATE: APRIL 2020 REVIEWED BY: J. Rowe

Matthew Copple

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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