

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select 2. CONTROLLER
- From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A
 Select TMG VEH OVLP [A] and 'NORMAL'

```

    TMG VEH OVLP...[A] TYPE: .....NORMAL
    PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
    INCLUDED . X . X . . . . .
    LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle Once

OVERLAP B
 Select TMG VEH OVLP [B] and 'NORMAL'

```

    TMG VEH OVLP...[B] TYPE: .....NORMAL
    PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
    INCLUDED . . X . . X . . . . .
    LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle Once

OVERLAP C
 Select TMG VEH OVLP [C] and 'NORMAL'

```

    TMG VEH OVLP...[C] TYPE: .....NORMAL
    PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
    INCLUDED . X X . . . X . . . . .
    LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle Once

OVERLAP D
 Select TMG VEH OVLP [D] and 'NORMAL'

```

    TMG VEH OVLP...[D] TYPE: .....NORMAL
    PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
    INCLUDED . . . X . X X . . . . .
    LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle Thrice

OVERLAP G
 Select TMG VEH OVLP [G] and 'NORMAL'

```

    TMG VEH OVLP...[G] TYPE: .....NORMAL
    PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
    INCLUDED . . X . . . . X . . . . .
    LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle Once

OVERLAP H
 Select TMG VEH OVLP [H] and 'NORMAL'

```

    TMG VEH OVLP...[H] TYPE: .....NORMAL
    PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
    INCLUDED . . . X . . . X . . . . .
    LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

END PROGRAMMING

ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL FOR PREEMPT ONLY PHASE OMIT

(program controller as shown)

- From Main Menu select 1. CONFIGURATION
- From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

    LP#: 1 COPY FROM: 1 ACTIVE: M (T/F)
    IF PMT PREEMPT ACTIVE 1 IS OFF
    THEN CTR OMIT PHASE 7 ON
    CTR OMIT PHASE 8 ON
    ELSE
    
```

LOGIC FOR OMITTING PHASES 7 AND 8 AT STARTUP AND/OR WHEN NOT IN PREEMPT

END PROGRAMMING

- From Main Menu select 1. CONFIGURATION
- From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- From LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1 BY POSITIONING THE CURSOR OVER THE FIELD SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE IT.

```

    LOGIC STATEMENT CONTROL
    1 2 3 4 5 6 7 8 9 0 1 2 3 4 5
    LP 1-15 E . . . . .
    LP 16-30 . . . . .
    LP 31-45 . . . . .
    LP 46-60 . . . . .
    LP 61-75 . . . . .
    LP 76-90 . . . . .
    
```

END PROGRAMMING

ECONOLITE ASC/3-2070 CONTROLLER SEQUENCE PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select 1. CONFIGURATION
- From CONFIGURATION Submenu select 1. CONTROLLER SEQ
- From CONTROLLER SEQUENCE Submenu select 1. PHASE RING SEQUENCE AND ASSIGNMENT

```

    CONTROLLER SEQUENCE [ 1 ]
    SEQUENCE COMMANDS . HW ALT SEQ ENA. NO.
    01 02 03 04 05 06 07 08 09 10 11 12 13 14 15 16
    BC-B - B - B - - - - -
    R1-01 02 03 04 07 08 . . . . .
    R2-05 06 . . . . .
    R3- . . . . .
    R4- . . . . .

    R1-R4=RING 1-4, DATA ENTRY, PHASES 1-16
    BC=BARRIER CONTROL, VALUES: B,C
    B=BARRIER MODE
    C=COMPATIBILITY MODE
    
```

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0111
 DESIGNED: AUGUST 2003
 SEALED: 11/4/03
 REVIS: 11/16/04
 REVIS: 05/15/20

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ELECTRICAL DETAIL - SHEET 2 of 3

US 701 BUSINESS (SOUTH MADISON STREET)
 at
 EAST MAIN STREET and
 WEST MAIN STREET

Prepared for the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEERS
 SEAL 27771
 MATTHEW B. COPPLE

DocuSigned by:
 Matthew Copple

2024/05/15 14:48:45

SIG. INVENTORY NO. 06-0111

5/15/2020
 11:46:01
 USER: MCopple