ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

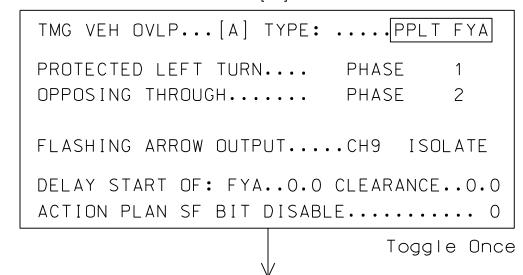
(program controller as shown)

1. From Main Menu select | 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'



OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

TMG VEH OVLP[B]	TYPE:PPLT	FYA
PROTECTED LEFT TURN OPPOSING THROUGH		
FLASHING ARROW OUTP	UTCH10 ISOL	ATE
DELAY START OF: FYA ACTION PLAN SF BIT		
	Toggle	e Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0 Toggle Once

OVERLAP D Select TMG VEH OVLP [D] and 'PPLT FYA'

TMG VEH OVLP...[D] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 7 OPPOSING THROUGH..... PHASE 8 FLASHING ARROW OUTPUT....CH12 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM, T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0109T2 DESIGNED: November 2019 SEALED: 05/15/2020 REVISED: N/A



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Electrical Detail - Sheet 2 of 2 ELECTRICAL AND PROGRAMMING Prepared for the Offices of:

Temporary Design 2 - (TMP Phase II)

US 701 Bypass/NC 130 (N. J.K. Powell Boulevard)

US 74-76 BUS/NC130(Washington St

Columbus County PLAN DATE: November 2019 REVIEWED BY: PREPARED BY: M.B. Copple REVIEWED BY: GG Murr Jr REVISIONS INIT. DATE

Matthew Copple

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 06-0109T2