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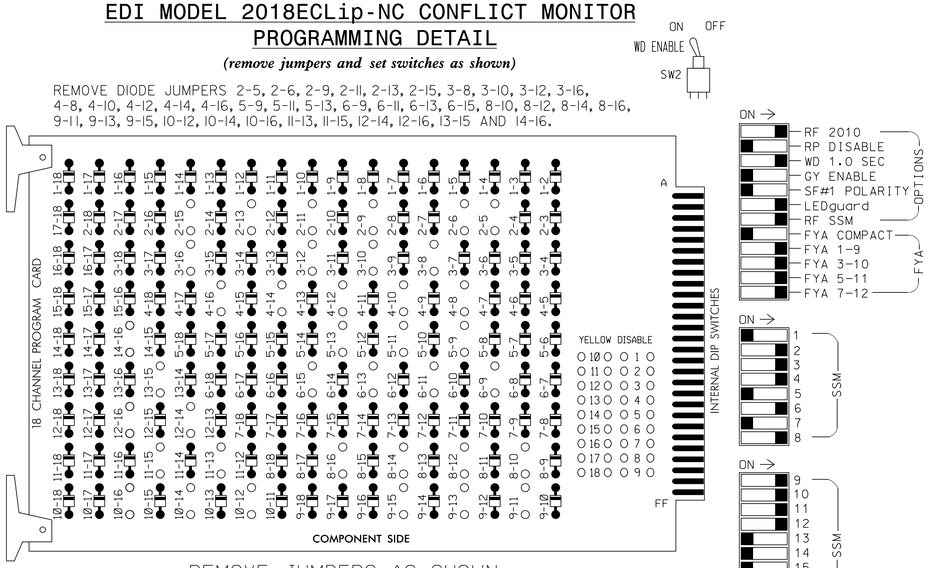
EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 3-8, 3-10, 3-12, 3-16, 4-8, 4-10, 4-12, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 8-10, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 10-12, 10-14, 10-16, 11-13, 11-15, 12-14, 12-16, 13-15 AND 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Program controller to start up in phase 2 Walk and phase 6 Walk.
4. The cabinet and controller are part of the SR 1007 (Mebane Oaks Rd) Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE ASC/3-2070
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S3,S4,S5,S6,S7,S8,S9, S11,S12,AUX S1,AUX S2, AUX S4,AUX S5
PHASES USED.....2,2PED,3,4,4PED,5,6,6PED, 8,8PED
OVERLAP "A".....*
OVERLAP "B".....*
OVERLAP "C".....*
OVERLAP "D".....*

* See overlap programming detail on sheet 2

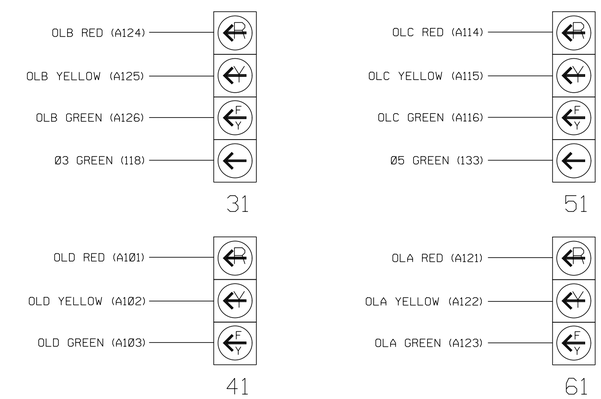
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4		S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3		4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3		4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	P21, P22	22	31 ★	42,43	P41, P42	51 ★	62,63	P61, P62	NU	81,82	P81, P82	61 ★	31 ★	NU	51 ★	41 ★	NU
RED		128		*		101			134			107							
YELLOW		129				102		*	135			108							
GREEN		130				103			136			109							
RED ARROW														A121	A124		A114	A101	
YELLOW ARROW				117										A122	A125		A115	A102	
FLASHING YELLOW ARROW														A123	A126		A116	A103	
GREEN ARROW				118	118			133											
✋			113				104			119			110						
🚶			115				106			121			112						

NU = Not Used
* Denotes install load resistor. See load resistor installation detail this sheet.
★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I" U	SLOT EMPTY	Ø2/SYS	SLOT EMPTY	WIRED INPUT ⊗	Ø 3	Ø 4	SLOT EMPTY	SLOT EMPTY	SYS. DET. S35	SLOT EMPTY	SLOT EMPTY	Ø2 PED DC ISOLATOR	Ø6 PED DC ISOLATOR	FS DC ISOLATOR
FILE "I" L		2A/S33 NOT USED			3A NOT USED	4A Ø 4 4B			NOT USED			Ø4 PED DC ISOLATOR	Ø8 PED DC ISOLATOR	ST DC ISOLATOR
FILE "J" U	Ø 5	Ø6/SYS	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø 8	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
FILE "J" L		5A 6A/S34 NOT USED Ø 6 6B				8A NOT USED								

EX.: 1A, 2A, ETC. = LOOP NO.'S
⊗ Wired Input - Do not populate slot with detector card
FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
2A/S33	TB2-5,6	I2U	39	2	2	YES			X	N
3A [1]	TB4-5,6	I5U	58	3	3	YES		15		S
	-	J8U	50	28	8	YES				S
4A	TB4-9,10	I6U	41	4	4	YES		3		S
4B	TB4-11,12	I6L	45	14	4	YES		10		S
5A [2]	TB3-1,2	J1U	55	5	5	YES		15		S
	-	I4U	47	22	2	YES		3		G
6A/S34	TB3-5,6	J2U	40	6	6	YES			X	N
6B	TB3-7,8	J2L	44	16	6	YES		3		G
8A	TB5-9,10	J6U	42	8	8	YES		10		S
*S35	TB6-9,10	I9U	60	11	SYS	NO				N
PED PUSH BUTTONS										
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	PED 8	8 PED					

NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

[1] Add jumper from I5-W to J8-W, on rear of input file.
[2] Add jumper from J1-W to I4-W, on rear of input file.
* System detector only. Remove any assigned vehicle phase.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

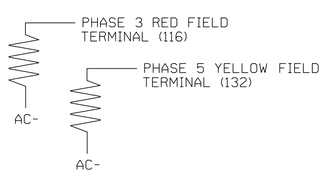
Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-2148
DESIGNED: November 2019
SEALED: 12/17/2019
REVISED: N/A

PREPARED IN THE OFFICE OF:
Accelerate Engineering, PLLC
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Cary, NC 27511
Tel: 919.263.5678 Fax: 919.263.5687
NC License No. P-1442

Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

ITS Transportation Mobility and Safety Division
STATE OF NORTH CAROLINA
DEPARTMENT OF TRANSPORTATION
Traffic Engineering Branch
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SR 2033 (Arrowhead Boulevard) at Applecross Drive/ Tanger Outlet Entrance

Division 7 | Alamance County | Mebane

PLAN DATE: November 2019 | REVIEWED BY: Z. "Gavin" Teng
PREPARED BY: Z. "Gavin" Teng | REVIEWED BY:

REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL NORTH CAROLINA PROFESSIONAL SEAL 032179 ENGINEER ZHAOLONG TENG

DocuSigned by: Zhaolong Teng 12/17/2019
SIGNATURE DATE

SIG. INVENTORY NO. 07-2148

$$$$$$SYSTIME$$$$$
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