## PROJECT REFERENCE NO. Sig.4.4 U-2581BA

## ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING FREE RUN — PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN, SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1, 3, 5, and 7.

TO RUN ALT, PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1, 3, 5, and 7.

NONE
1, 3, 5, 7

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

## ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1, 3, 5, AND 7 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1,3,5,7: Modifies overlap parent phases for heads 11, 31, 51, and 71 to

run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A

and reduces delay time for phase 1 call on loop 1A to 3 seconds.

Disables phase 8 call on loop 3A and reduces delay time for phase 3

call on loop 3A to 3 seconds. Disables phase 2 call on loop 5A

and reduces delay time for phase 5 call on loop 5A to 3 seconds.

Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

## ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select 5. TIME BASE

2. From TIME BASE Submenu select | 2. ACTION PLAN |

ACTION PLA	ΔN.	[	1	]												
PATTERN			A	UTO		SYS	OV	ERR	IDE		. N	0				
TIMING PLA	SEQ	UEN	CE.			•	0									
VEH DETECT	DET	LO	G			NON	Ε									
FLASH	RED REST NO															
VEH DET D	PED DET DIAG PLNO															
DIMMING EN	PRIORITY RETURN. NO															
PED PR RE	QUE	UE	DEL	АҮ.		. N	0									
PMT COND [	DEL	ДΥ		NO												
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
PED RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
WALK 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
VEX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
VEH RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
MAX RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
MAX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
MAX 3	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
CS INH	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
OMIT	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
SPC FCT	Χ	•	Χ	•	X	•	Χ	•	( 1	-8)						
AUX FCT	•	•	•		-3											
	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
LP 1-15	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 16-30	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 31-45	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 46-60	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 61-75	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 76-90	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 91-100	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0204 DESIGNED: September 2019 SEALED: 10/16/2019 REVISED: N/A



DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical Detail-Final Design-Sheet 4 of 4

ELECTRICAL AND PROGRAMMING Prepared for the Offices of:

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