

ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A, 5A, 3A, 7A

(program controller as shown)

IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

1. From Main Menu select **8. UTILITIES**
2. From UTILITIES Submenu select **1. COPY/CLEAR**
3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

```

COPY / CLEAR UTILITY
FROM          TO
PHASE TIMING... > PHASE TIMING...
TIMING PLAN... > TIMING PLAN...
PH DET OPT PLAN. > PH DET OPT PLAN.
DETECTOR PLAN... 1 > DETECTOR PLAN... 2
TOGGLE TO SELECT A "FROM" AND A "TO"
THEN PRESS ENTER
    
```

4. From Main Menu select **6. DETECTORS**
5. From DETECTOR Submenu select **2. VEHICLE DETECTOR SETUP**
6. Place cursor in VEH DET PLAN [] position and enter "2".

- Place cursor in VEH DETECTOR [] position and enter "1".
- Set delay time to "0".

```

VEH DETECTOR [ 1]  VEH DET PLAN [ 2]
TYPE: S-STANDARD
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
1 1
EXTEND TIME... 0.0 DELAY TIME... 3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

- Place cursor in VEH DETECTOR [] position and enter "26".
- Set assigned phase to "0".

```

VEH DETECTOR [26]  VEH DET PLAN [ 2]
TYPE: G-GREEN EXTENSION/DELAY
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
26 0
EXTEND TIME... 0.0 DELAY TIME... 3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

- Place cursor in VEH DETECTOR [] position and enter "5".
- Set delay time to "0".

```

VEH DETECTOR [ 5]  VEH DET PLAN [ 2]
TYPE: S-STANDARD
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
5 5
EXTEND TIME... 0.0 DELAY TIME... 3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

- Place cursor in VEH DETECTOR [] position and enter "22".
- Set assigned phase to "0".

```

VEH DETECTOR [22]  VEH DET PLAN [ 2]
TYPE: G-GREEN EXTENSION/DELAY
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
22 0
EXTEND TIME... 0.0 DELAY TIME... 3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

- Place cursor in VEH DETECTOR [] position and enter "3".
- Set delay time to "0".

```

VEH DETECTOR [ 3]  VEH DET PLAN [ 2]
TYPE: S-STANDARD
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
3 3
EXTEND TIME... 0.0 DELAY TIME... 3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

- Place cursor in VEH DETECTOR [] position and enter "28".
- Set assigned phase to "0".

```

VEH DETECTOR [28]  VEH DET PLAN [ 2]
TYPE: G-GREEN EXTENSION/DELAY
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
28 0
EXTEND TIME... 0.0 DELAY TIME... 3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

- Place cursor in VEH DETECTOR [] position and enter "7".
- Set delay time to "0".

```

VEH DETECTOR [ 7]  VEH DET PLAN [ 2]
TYPE: S-STANDARD
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
7 7
EXTEND TIME... 0.0 DELAY TIME... 3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

- Place cursor in VEH DETECTOR [] position and enter "24".
- Set assigned phase to "0".

```

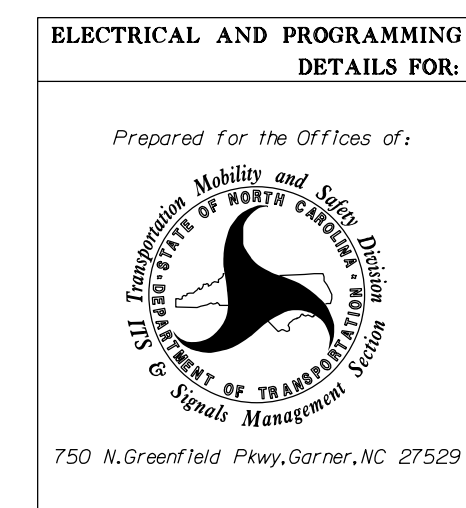
VEH DETECTOR [24]  VEH DET PLAN [ 2]
TYPE: G-GREEN EXTENSION/DELAY
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
24 0
EXTEND TIME... 0.0 DELAY TIME... 3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

END PROGRAMMING



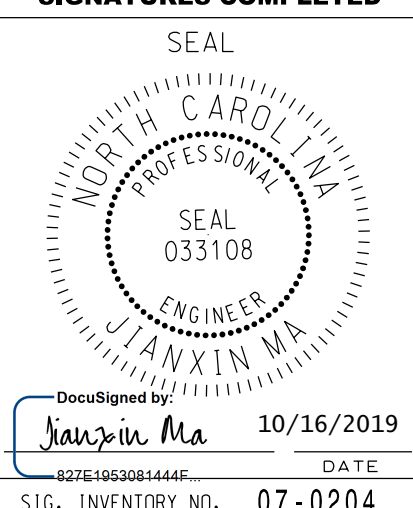
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Electrical Detail-Final Design-Sheet 3 of 4



US 70 (Burlington Road)		
at		
SR 3045/2819		
(Mt. Hope Church Road)		
Division 7	Guilford County	Greensboro
PLAN DATE: September 2019	REVIEWED BY: M.L. Stygles	
PREPARED BY: J. Ma	REVIEWED BY:	
REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0204
DESIGNED: September 2019
SEALED: 10/16/2019
REVISED: N/A

DocuSigned by: J. Ma
10/16/2019
DATE
SIG. INVENTORY NO. 07-0204