ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: ..... PPLT FYA

PROTECTED LEFT TURN....   PHASE   1
OPPOSING THROUGH.......   PHASE   2

FLASHING ARROW OUTPUT.....CH9 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 1 ← NOTICE ACTION PLAN SF BIT "1"

Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

TMG VEH OVLP...[B] TYPE: ..... PPLT FYA

PROTECTED LEFT TURN....   PHASE   3
OPPOSING THROUGH.......   PHASE   4

FLASHING ARROW OUTPUT.....CH10 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 3 ← NOTICE ACTION PLAN SF BIT "3"

Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: ..... PPLT FYA

PROTECTED LEFT TURN....   PHASE   5
OPPOSING THROUGH.......   PHASE   6

FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 5 ← NOTICE ACTION PLAN SF BIT "5"

Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

TMG VEH OVLP...[D] TYPE: ..... PPLT FYA

PROTECTED LEFT TURN....   PHASE   7
OPPOSING THROUGH.......   PHASE   8

FLASHING ARROW OUTPUT.....CH12 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 7 ← NOTICE ACTION PLAN SF BIT "7"

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:
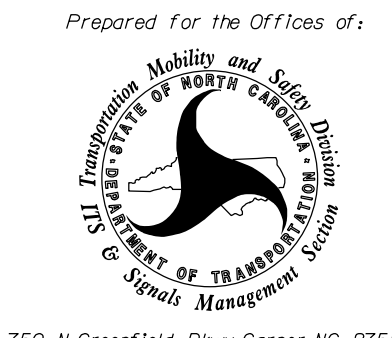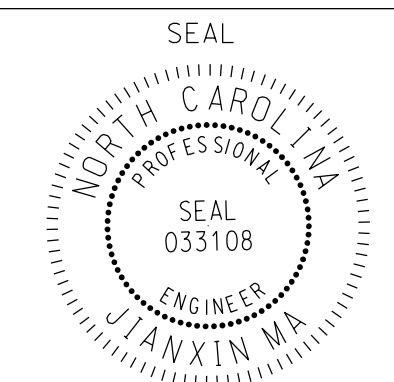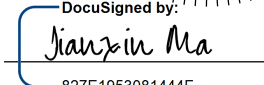
1. ON REAR OF PDA – REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.

2. ON REAR OF PDA – REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.

3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-0204
DESIGNED: September 2019
SEALED: 10/16/2019
REVISED: N/A

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DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical Detail-Final Design-Sheet 2 of 4

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of:

NC Transportation Mobility and Safety Division
STATE OF NORTH CAROLINA
DEPARTMENT OF TRANSPORTATION
Signals Management Section

750 N.Greenfield Pkwy,Garner,NC 27529

US 70 (Burlington Road)
at
SR 3045/2819
(Mt. Hope Church Road)

Division 7	Guilford County	Greensboro
PLAN DATE: September 2019	REVIEWED BY: M.L. Stygles	
PREPARED BY: J. Ma	REVIEWED BY:	

REVISIONS

	INIT.	DATE

SEAL

NORTH CAROLINA
SEAL
033108
ENGINEER
JIANXIN MA

DocuSigned by:
Jianxin Ma
10/16/2019
DATE

SIG. INVENTORY NO. 07-0204