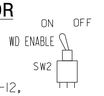
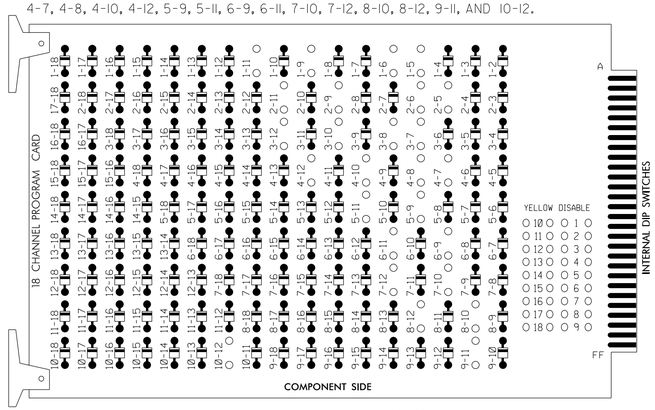
PROJECT REFERENCE NO.	SHEET NO.
U-2581BA	Sig.4.1

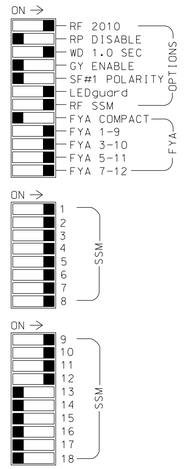
EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE DIODE JUMPERS I-5, I-6, I-9, I-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11, AND 10-12.





REMOVE JUMPERS AS SHOWN

■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Program controller to start up in phase 2 Green and 6 Green.

EQUIPMENT INFORMATION

CONTROLLER.....2070LX
CABINET.....332 W/AUX
SOFTWARE.....ECONOLITE ASC/3-2070
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11, AUX S1,AUX S2,AUX S4,AUX S5
PHASES USED.....1,2,3,4,5,6,7,8
OVERLAP "A".....*
OVERLAP "B".....*
OVERLAP "C".....*
OVERLAP "D".....*

* See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1		S2	S3	S4		S5	S6	S7		S8	S9	S10		S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1		2	13	3		4	14	5		6	15	7		8	16	9	10	17	11	12	18
PHASE	1		2	2 PED	3		4	4 PED	5		6	6 PED	7		8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	82	21,22	NU	31★	22	41,42	NU	51★	42	61,62	NU	71★	62	81,82	NU	11★	31★	NU	51★	71★	NU
RED		*	128			*	101			*	134			*	107							
YELLOW			129				102				135				108							
GREEN			130				103				136				109							
RED ARROW																	A121	A124		A114	A101	
YELLOW ARROW		126				117				132				123			A122	A125		A115	A102	
FLASHING YELLOW ARROW																	A123	A126		A116	A103	
GREEN ARROW	127	127			118	118			133	133			124	124								

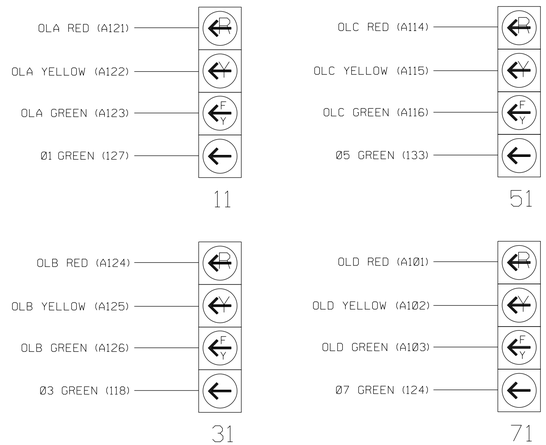
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I" UPPER	Ø1 1A	Ø1 1B	Ø2 2A	WIRED INPUT ⊗	Ø3 3A	Ø4 4A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
FILE "I" LOWER	NOT USED	NOT USED	Ø2 2B		NOT USED	NOT USED								ST DC ISOLATOR
FILE "J" UPPER	Ø5 5A	Ø5 5B	Ø6 6A	WIRED INPUT ⊗	Ø7 7A	Ø8 8A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
FILE "J" LOWER	NOT USED	NOT USED	Ø6 6B		NOT USED	NOT USED								

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
1A[1]	TB2-1,2	I1U	56	1 ★	1	YES		15		S
	-	J4U	48	26 ★	6	YES		3		G
1B	TB2-5,6	I2U	39	2	1	YES		15		S
2A	TB2-9,10	I3U	63	32	2	YES			X	N
2B	TB2-11,12	I3L	76	42	2	YES			X	N
3A[2]	TB4-5,6	I5U	58	3 ★	3	YES		15		S
	-	J8U	50	28 ★	8	YES		3		S
4A	TB4-9,10	I6U	41	4	4	YES				S
5A[3]	TB3-1,2	J1U	55	5 ★	5	YES		15		S
	-	I4U	47	22 ★	2	YES		3		G
5B	TB3-5,6	J2U	40	6	5	YES		15		S
6A	TB3-9,10	J3U	64	36	6	YES			X	N
6B	TB3-11,12	J3L	77	46	6	YES			X	N
7A[4]	TB5-5,6	J5U	57	7 ★	7	YES		15		S
	-	I8U	49	24 ★	4	YES		3		S
8A	TB5-9,10	J6U	42	8	8	YES				S

[1] Add jumper from I1-W to J4-W, on rear of input file.
[2] Add jumper from I5-W to J8-W, on rear of input file.
[3] Add jumper from J1-W to I4-W, on rear of input file.
[4] Add jumper from J5-W to I8-W, on rear of input file.

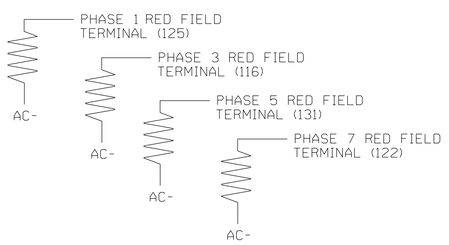
★ For the detectors to work as shown on the signal design plan, see the Vehicle Detector Setup Programming Detail for Alternate Phasing on sheet(s) x.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø7-0204
DESIGNED: September 2019
SEALED: 10/16/2019
REVISED: N/A

vhb
VHB Engineering NC, P.C. (C-3705)
940 Main Campus Drive, Suite 500
Raleigh, NC 27606
P: 919-829-0328

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical Detail-Final Design-Sheet 1 of 4

ELECTRICAL AND PROGRAMMING DETAILS FOR:

US 70 (Burlington Road)
at
SR 3045/2819
(Mt. Hope Church Road)

Division 7 | Guilford County | Greensboro

PLAN DATE: September 2019 | REVIEWED BY: M.L. Stygles
PREPARED BY: J. Ma | REVIEWED BY:

REVISIONS	INIT.	DATE

Prepared for the Offices of:
Transportation Mobility and Safety Division
State of North Carolina
Department of Transportation
Traffic Engineering Branch
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SEAL
NORTH CAROLINA
PROFESSIONAL
SEAL 033108
ENGINEER
JIANXIN MA

DocuSigned by: Jianxin Ma
827E1953081444F...
10/16/2019
DATE

SIG. INVENTORY NO. 07-0204

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