(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O

ACTION PLAN SF BIT DISABLE..... 0 Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH.... PHASE 6

FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

TMG VEH OVLP...[D] TYPE:PPLT FYA PROTECTED LEFT TURN.... PHASE OPPOSING THROUGH.... PHASE 8 FLASHING ARROW OUTPUT....CH12 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

PROJECT REFERENCE NO. U-2581BA Sig 3 2

ECONOLITE ASC/3-2070 BACKUP PROTECTION ENABLE PROGRAMMING

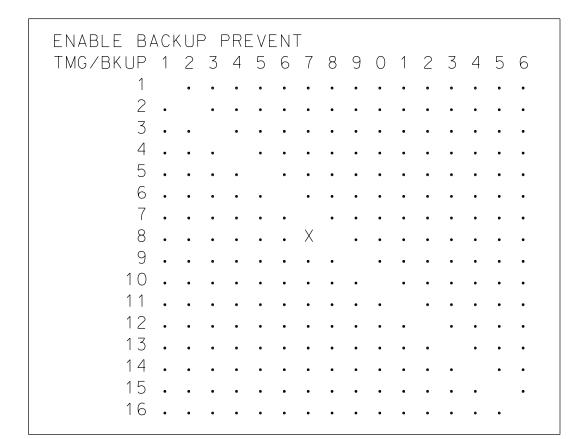
(program controller as shown)

1. From Main Menu select | 1. CONFIGURATION

2. From CONFIGURATION Submenu select | 1. CONTROLLER SEQ

3. From CONTROLLER SEQUENCE Submenu select | 3. BACKUP PREVENT PHASES

Follow programming as shown below. On the 'ENABLE BACKUP PREVENT' screen move cursor to the appropriate field and press 'YES/NO' on the controller keypad to toggle field value between 'X', 'B', 'C' and 'OFF'.



END PROGRAMMING

NOTES

1. 'X' inhibits the controller from servicing the 'BACKUP' (column) phase when the 'TIMING' (row) phase is active or next.

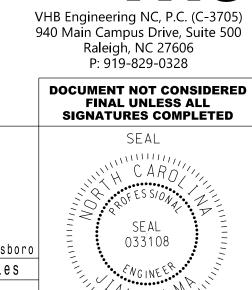
> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0204T2 DESIGNED: September 2019 SEALED: 09/09/2019 REVISED: N/A

Electrical Detail-Temporary Design 2 (TMP Phase II)-Sheet 2 of 3 ELECTRICAL AND PROGRAMMING US 70 (Burlington Road)

DETAILS FOR:

750 N.Greenfield Pkwy, Garner, NC 27529

SR 3045/2819 (Mt. Hope Church Road) Guilford County Greensboro PLAN DATE: September 2019 | REVIEWED BY: M.L. Stygles J. Ma REVIEWED BY: INIT. DATE



Jianzin Ma SIG. INVENTORY NO. 07-0204T2