

## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select **2. CONTROLLER**
- From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**

*OVERLAP A*

Select TMG VEH OVLP [A] and 'PPLT FYA'

```

TMG VEH OVLP...[A] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 1
OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH9 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
    
```

Toggle Once

*OVERLAP B*

Select TMG VEH OVLP [B] and 'PPLT FYA'

```

TMG VEH OVLP...[B] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 3
OPPOSING THROUGH..... PHASE 4

FLASHING ARROW OUTPUT....CH10 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
    
```

Toggle Once

*OVERLAP C*

Select TMG VEH OVLP [C] and 'PPLT FYA'

```

TMG VEH OVLP...[C] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 5
OPPOSING THROUGH..... PHASE 6

FLASHING ARROW OUTPUT....CH11 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
    
```

END PROGRAMMING

## FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

## ECONOLITE ASC/3-2070 BACKUP PROTECTION ENABLE PROGRAMMING

(program controller as shown)

- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **1. CONTROLLER SEQ**
- From CONTROLLER SEQUENCE Submenu select **3. BACKUP PREVENT PHASES**

Follow programming as shown below. On the 'ENABLE BACKUP PREVENT' screen move cursor to the appropriate field and press 'YES/NO' on the controller keypad to toggle field value between 'X', 'B', 'C' and 'OFF'.

```

ENABLE BACKUP PREVENT
TMG/BKUP 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
1 . . . . .
2 . . . . .
3 . . . . .
4 . X . . . . .
5 . . . . .
6 . . . . .
7 . . . . .
8 . . . . .
9 . . . . .
10 . . . . .
11 . . . . .
12 . . . . .
13 . . . . .
14 . . . . .
15 . . . . .
16 . . . . .
    
```

END PROGRAMMING

### NOTES

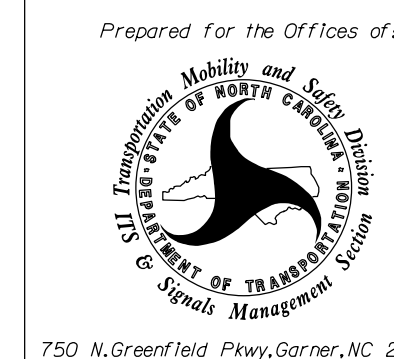
- 'X' inhibits the controller from servicing the 'BACKUP' (column) phase when the 'TIMING' (row) phase is active or next.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0204T1  
 DESIGNED: September 2019  
 SEALED: 09/09/2019  
 REVISED: N/A



Electrical Detail-Temporary Design 1 (TMP Phase I)-Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:



<b>US 70 (Burlington Road) at SR 3045/2819 (Mt. Hope Church Road)</b>		
Division 7	Guilford County	Greensboro
PLAN DATE: September 2019	REVIEWED BY: M.L. Stygles	
PREPARED BY: J. Ma	REVIEWED BY:	
REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL NORTH CAROLINA PROFESSIONAL ENGINEER J. MA 033108	DocuSign by J. Ma 9/9/2019 DATE SIG. INVENTORY NO. 07-0204T1
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