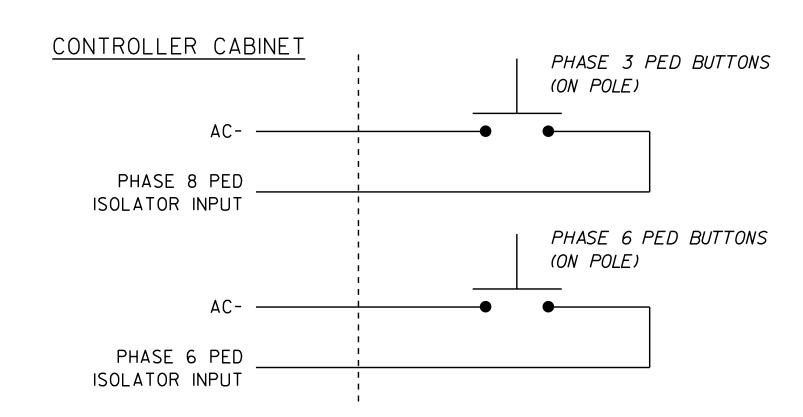
(wire push buttons as shown)

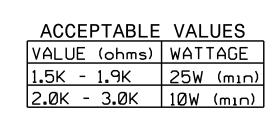


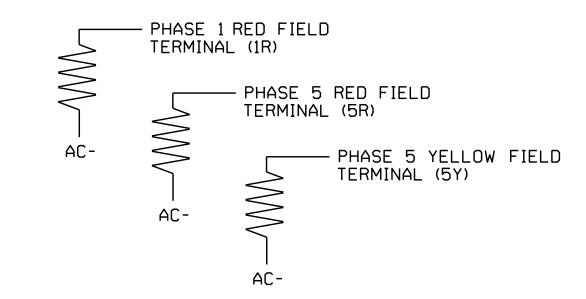
COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



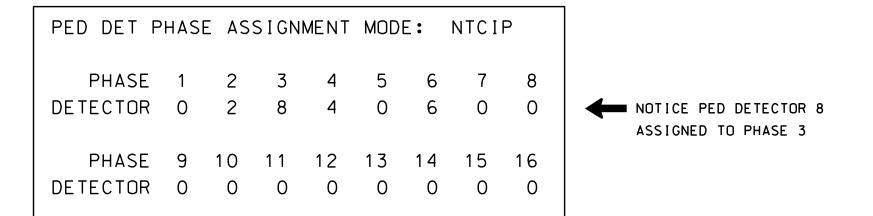


PROJECT REFERENCE NO. Sig 8 3 I-5700

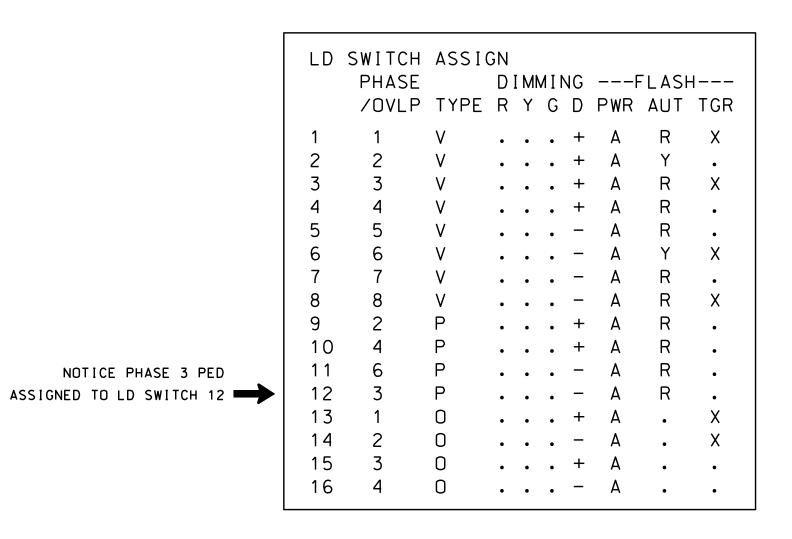
ECONOLITE ASC/3-2070 PED 3 PROGRAMMING ASSIGNMENT DETAIL

(program controller as shown)

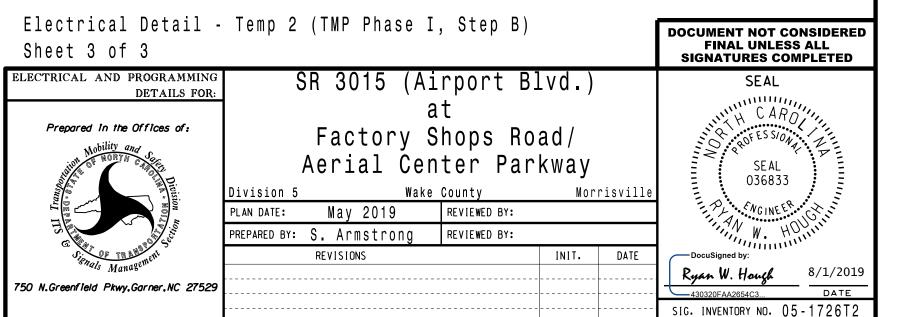
- 1. From Main Menu select 6. DETECTORS
- 2. From DETECTOR Submenu select | 3. PED DETECTOR INPUT ASSIGNMENT

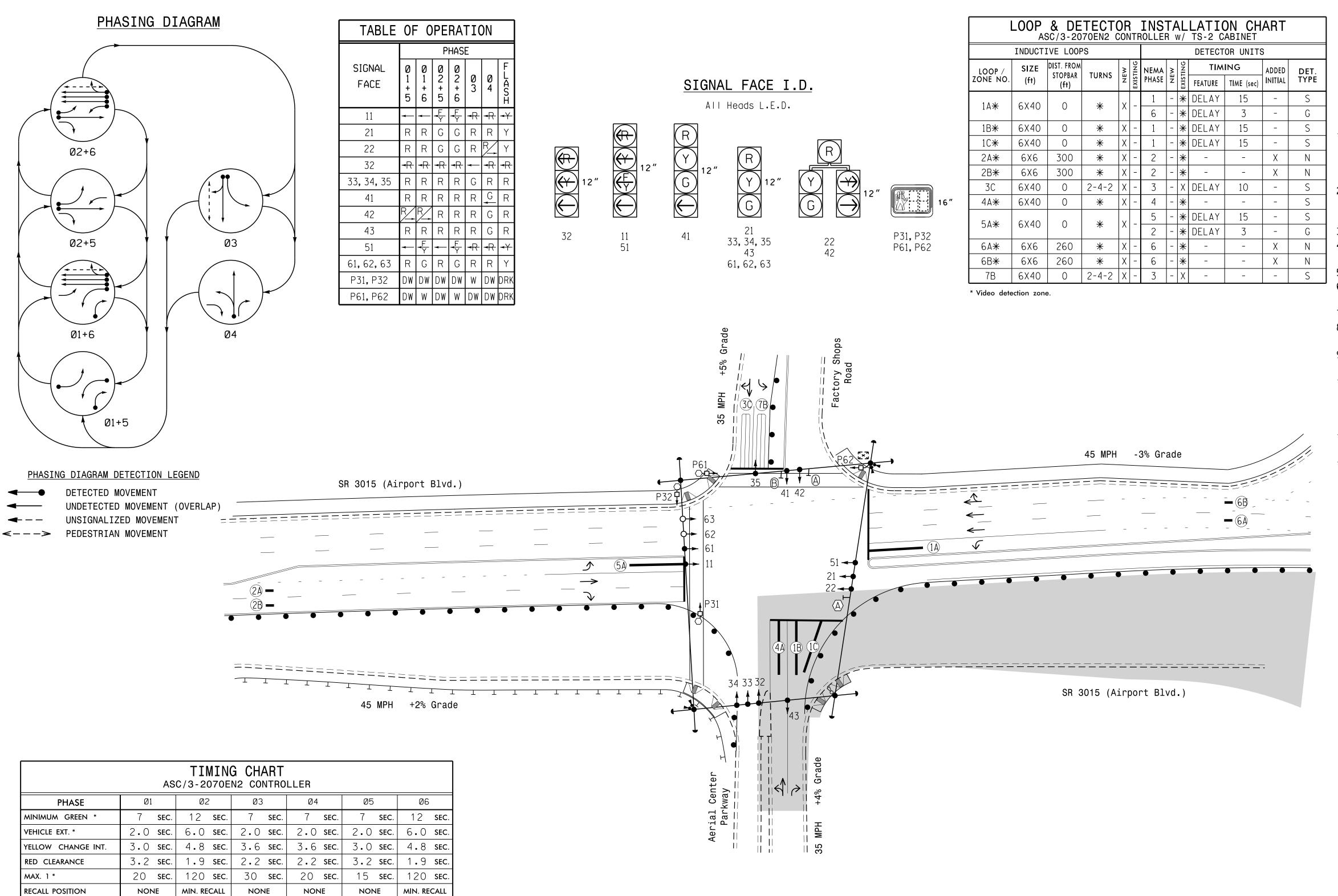


- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726T2 DESIGNED: March 2019 SEALED: 7/24/2018 REVISED: N/A





6 Phase Fully Actuated (Cary Signal System)

NOTES

Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.

PROJECT REFERENCE NO.

I-5700

Sig. 9.0

- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. The order of phase 3 and phase 4 may be reversed.
- 5. Set all detector units to presence mode.
- Reposition existing signal heads numbered 11, 21, 22, 51, and 61.
- 7. Remove existing signal head numbered 31.
- 8. Omit "WALK" and flashing "DON'T WALK" with no pedestrian calls.
- 9. Program pedestrian heads to countdown the flashing "Don't Walk" time only.
- 10. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- 11. Cary signal system data: Fiber Channel #: 26.
- 12. This intersection features a video detection system. Shown locations of detectors are conceptual only. Refer to the manufacturer's guidelines for optimal detector placement.

LEGEND

<u>PROPOSED</u>	<u>)</u>	EXISTING
\bigcirc	Traffic Signal Head	
O	Modified Signal Head	N/A
$\overline{}$	Sign	\dashv
\downarrow	Pedestrian Signal Head With Push Button & Sign	•
\bigcirc	Signal Pole with Guy	
	Signal Pole with Sidewalk Guy	
	Inductive Loop Detector	
	Controller & Cabinet	K K K
	Junction Box	
	2-in Underground Conduit	
N/A	Right of Way	
\longrightarrow	Directional Arrow	\longrightarrow
\bigcirc	Type II Signal Pedestal	
N/A	Guardrail	
N/A	Curb Ramp	
\bigotimes	Out of Pavement Detector	•
	Video Detection Area	
• •	Construction Zone Drums	•
	Construction Zone	
$\langle A \rangle$	Right Arrow "ONLY" Sign (R3-5R) (A)
B	Combined Through and Left Arrow Sign (R3-6L)	lack

Signal Upgrade - Temporary Design 3 (TMP Phase III, Step A)

SR 3015 (Airport Blvd.) Factory Shops Road/ Aerial Center Parkway

ivision 5 Wake County March 2019 REVIEWED BY:

PLAN DATE: 750 N.Greenfield Pkwy.Garner.NC 27529 PREPARED BY: J.A. Lohr REVIEWED BY: REVISIONS INIT. DATE

SEAL

SIG. INVENTORY NO. 05-1726T

DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL

SIGNATURES COMPLETED

SIMULTANEOUS GAP

LOCK DET.

PED. CLEAR

VOLUME DENSITY

MAX. INITIAL *

ACTUATION B4 ADD

SEC. PER ACTUATION 3

TIME B4 REDUCTION

TIME TO REDUCE

MINIMUM GAP

DUAL ENTRY

WALK *

— SEC.

1.5 **SEC**.

34 **SEC**.

15 **SEC**.

45 **SEC**.

3.0 **SEC**.

7 sec.

20 **SEC**.

SEC.

SEC.

SEC.

SEC.

SEC.

VEH.

SEC.

SEC.

SEC.

SEC.

SEC.

ON

SEC.

SEC.

ON

22 **SEC**

45 **SEC**

OFF

ON

— SEC.

SEC.

VEH.

SEC.

ON

- SEC. | 1.5 SEC

- SEC. 30 SEC

SEC. 15 SEC

— SEC. | 3.0 SEC

OFF

— SEC.

SEC.

SEC.

SEC.

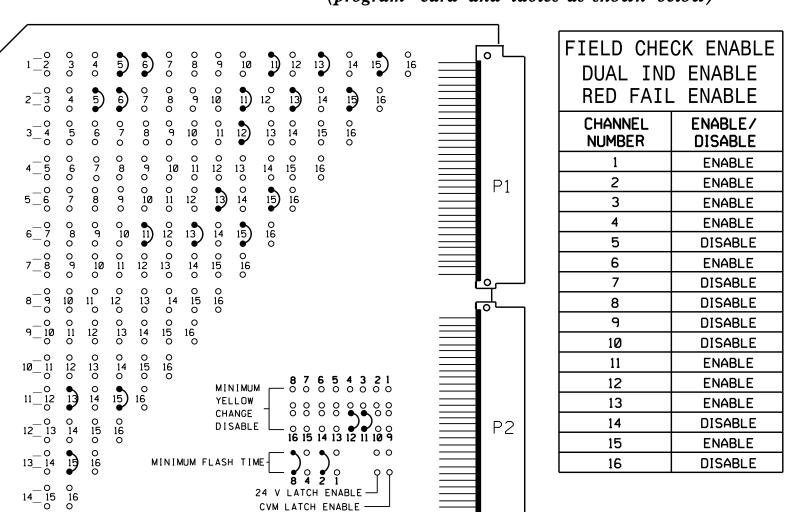
— SEC.

— SEC.

SEC.

ON

(program card and tables as shown below)



UNIT OPTIONS											
OPTION	SETTING										
RECURRENT PULSE	ON										
WALK DISABLE	OFF										
LOG CVM FAULTS	ON										
EXTERN WATCHDOG	OFF										
24V-2=12VDC	OFF										
PGM CARD MEMORY	ON										
LEDguard	ON										
FORCE TYPE 16	OFF										
TYPE12-SDLC	OFF										
VM 3x/Day Latch	ON										

FLASHING YE	LLOW ARROW							
CONFIG MODE	В							
ENABLE CHANN	NEL PAIR, FYA							
CH 1-13	ON							
CH 3-14	OFF							
CH 5-15	ON							
CH 7-16	OFF							
RED/YEL INF	PUT ENABLE							
CH 1	ON							
CH 3	OFF							
CH 5	ON							
CH 7	OFF							
FLASH RATE FAULT	ON							
FYA TRAP DETECT	ON							

MMU PROGRAMMING NOTE

ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

RACK	S L O T	сн1 L1 Ø1	S L O T	CH1 L5 NOT USED	S L O T	сн1 L9 Ø5	сн1 L 15 Ø 3	SLOT	SLOT	SLOT	S L O T		
#1	BIU	E M P T Y	сн2 L2 ø6 *	E M P T Y	сн2 L6 ø3	E M P T Y	сн2 L 1 0 ø 2 *	CH2 L16 NOT USED	E M P T Y	E M P T Y	E M P T Y	E M P T Y	

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN IN THE CHART BELOW

MMU PROGRAMMING CARD

PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

	IN IHE (.E	1AKI BELUW				
	LOOP NO.	LOOP PANEL TERMINALS		TROLLER	FUNCT	ION
ADD JUMPERS FROM: L1A TO L2A, AND	1 A	L1A,L1B		1	ø 1	
LIB TO LZB	Ι (L2A,L2B	*	2	ø 6	
	NU	L3A,L3B		3		
	NU	L4A,L4B		4		
	NU	L5A,L5B		5		
	3C	L6A,L6B		6	ø 3	
	NU	L7A,L7B		7		
	NU	L8A,L8B		8		
ADD JUMPERS FROM: L9A TO L10A, AND	5A	L9A,L9B		9	Ø 5	
L9B TO L10B	5	L10A,L10B	*	10	ø 2	
	NU	L11A,L11B		11		
	NU	L12A,L12B		12		
	NU	L13A,L13B		13		
	NU	L14A,L14B		14		
	7B	L15A,L15B		15	ø 3	
	NU	L16A,L16B		16		

CONTROLLER	FUNCTION	TIMING					
DETECTOR NO.	FUNCTION	FEATURE	TIME(SEC)				
1	Ø 1	DELAY	15				
* 2	ø 6	DELAY	3				
3							
4							
5							
6	ø 3	DELAY	10				
7							
8							
9	ø 5	DELAY	15				
* 10	ø 2	DELAY	3				
1 1							
12							
13							
14							
15	ø 3						
16							

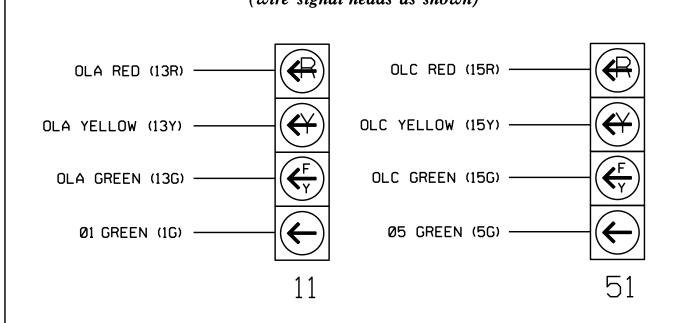
* Detector Type - G (remove delay from existing detector card)

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans for zones 1A, 1B, 1C, 2A, 2B, 4A, 5A, 6A, and 6B.

For Detection Zones 1A and 5A, the equipment placement and slots reserved for wired inputs are typical for a NCDOT installation.

FYA SIGNAL WIRING DETAIL (wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726T3 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A

LOAD SWITCH ASSIGNMENT DETAIL

(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION							
1	Ø 1							
2	ø2							
3	øЗ							
4	Ø 4							
5	ø5							
6	ø6							
7	Ø 7							
8	Ø 8							
9	Ø2 PED							
10	Ø4 PED							
11	Ø6 PED							
12	Ø3 PED							
13	OLA							
14	OLB							
15	OLC							
16	OLD							

PROJECT REFERENCE NO. Sig 9.1 I-5700

						SI	GN	AL I	HEA	D H	00k	(-UI	C C	HAR	Т					
PHASE	1		2	;	3		4		5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	11★	42	21,22	32	33 , 34 35	22	41	42,43	51 ★	61 , 62 63	NU	NU	NU	NU	P61, P62	P31, P32	11	NU	5 1	NU
RED		*	2R		3R		4R	4R	*	6R										
YELLOW			2Y		3Y		4 Y	4 Y	*	6Y										
GREEN			2G		3G		4G	4G		6G										
RED ARROW				3R													13R		15R	
YELLOW ARROW		1Y		3Y		4Y											13Y		15Y	
FLASHING YELLOW ARROW																	13G		15G	
GREEN ARROW	1G	1G		3G		4G	4G		5G											
*															11R	12R				
Ķ															11G	12G				

NU = Not Used

SOFTWAREECONOLITE ASC/3-2070

* SEE OVERLAP PROGRAMMING DETAIL ON SHEET 2

LOAD SWITCHES USED.....1,2,3,4,5,6,11,12,13,15

CONTROLLER......2070EN2

OLB.....NOT USED

OLD.....NOT USED

CABINET MOUNT.....BASE

LOADBAY POSITIONS.....16

OL A *

OLC....*

EQUIPMENT INFORMATION

- * Denotes install load resistor. See Load Resistor Installation Detail on sheet 3.
- ★ See pictorial of head wiring detail this sheet.

NOTE: Signal head 31 has been disconnected and bagged but still hangs on the span.

NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 7,8,9,10,14, and 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (LS AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phase 2 Green and 6 Walk.
- 4. Set power-up flash time to 10 seconds and implement on the Malfunction Management Unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6 for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

Electrical Detail - Temp 3 (TMP Phase III, Step A) Sheet 1 of 3 ELECTRICAL AND PROGRAMMING SR 3015 (Airport Blvd.)

DETAILS FOR: Prepared in the Offices of:

Factory Shops Road/ Aerial Center Parkway

ivision 5 PLAN DATE: REVIEWED BY: May 2019 PREPARED BY: S. Armstrong Reviewed BY: REVISIONS INIT. DATE

SIGNATURES COMPLETED 036833

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

SIG. INVENTORY NO. 05-1726T3

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH13 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

OVERLAP C

Toggle Twice

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE:PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH15 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

PROJECT REFERENCE NO. Sig 9.2 I-5700

ECONOLITE ASC/3-2070 SPECIAL MMU PROGRAMMING

(program controller as shown)

- 1. From Main Menu select | 1. CONFIGURATION |
- 2. From CONFIGURATION Submenu select 4. PORT 1 (SDLC)
- 3. From PORT 1 (SDLC) Submenu select 2. MMU PROGRAM

CAUTION!

Set intersection to Flash before attempting to enter or change any MMU programming data.

This programming and that of the MMU programming card must match exactly. If they do not, the intersection will be placed into Flash.

MMU	MU PROGRAM [MANUAL]											
	СН	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	
	1	•	X	•	X	•	X	•	•	•	•	X	X	•	•	•	
	2	•	Χ	•	Χ	•	Χ	•	•	•	•	Χ	Χ	•	•		
	3	•	•	•	•	Χ	•	•	•	•	•	•	•	•			
	4	•	•	•	•	•	•	•	•	•	•	•	•				
	5	•	Χ	•	Χ	•	•	•	•	•	•	•					
	6	•	Χ	•	Χ	•	Χ	•	•	•	•						
	7	•	•	•	•	•	•	•	•	•							
	8	•	•	•	•	•	•	•	•								
	9	•	•	•	•	•	•	•									
	10	•	•	•	•	•	•										
	11	•	Χ	•	Χ	•											
	12	•	•	•	•												
	13	•	Χ	•													
	14	•	•														
	15	•															

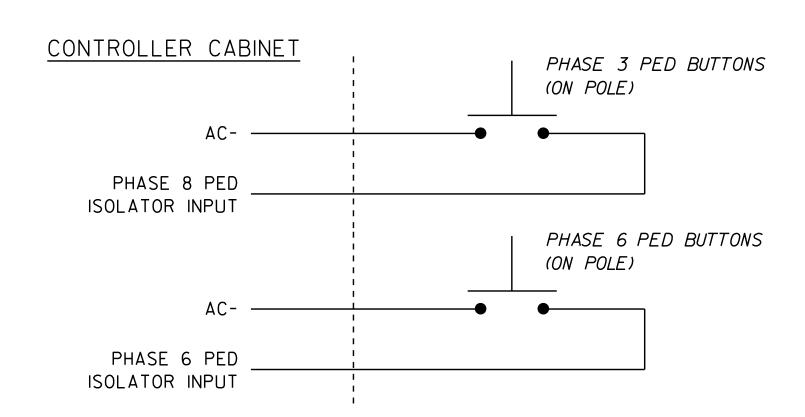
END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726T3 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A

Electrical Detail - Temp 3 (TMP Phase III, Step A) DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED Sheet 2 of 3 ELECTRICAL AND PROGRAMMING SR 3015 (Airport Blvd.) DETAILS FOR: Factory Shops Road/ Aerial Center Parkway Division 5 Wake County May 2019 REVIEWED BY: PLAN DATE: PREPARED BY: S. Armstrong Reviewed BY: REVISIONS INIT. DATE 750 N.Greenfield Pkwy, Garner, NC 27529

SIG. INVENTORY NO. 05-1726T3

(wire push buttons as shown)

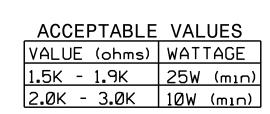


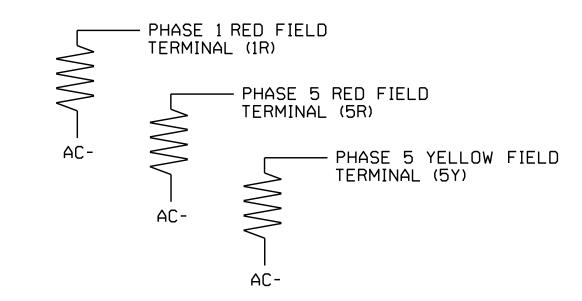
COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



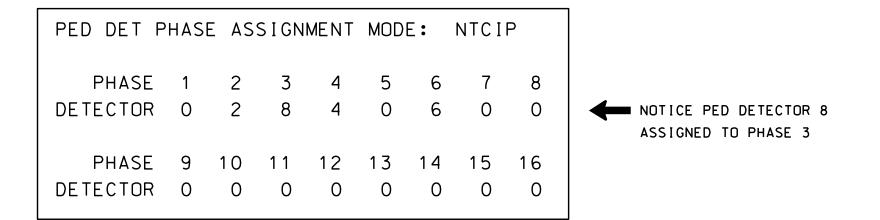


PROJECT REFERENCE NO. Sig 9 3 I-5700

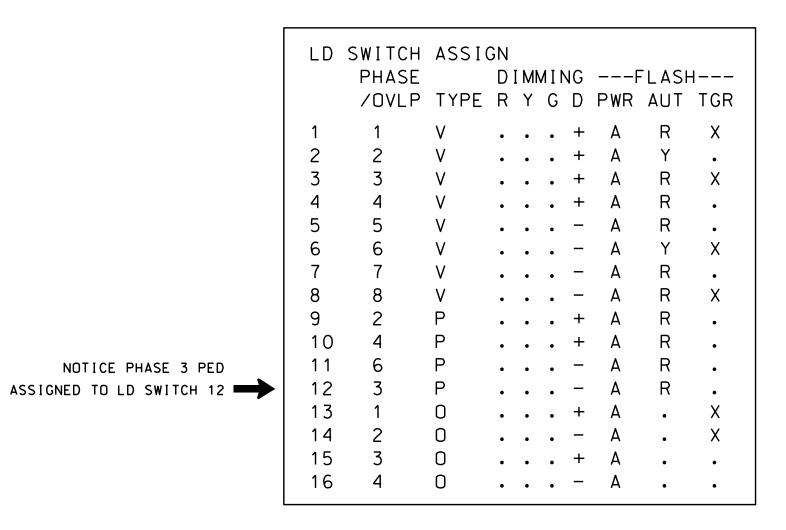
ECONOLITE ASC/3-2070 PED 3 PROGRAMMING ASSIGNMENT DETAIL

(program controller as shown)

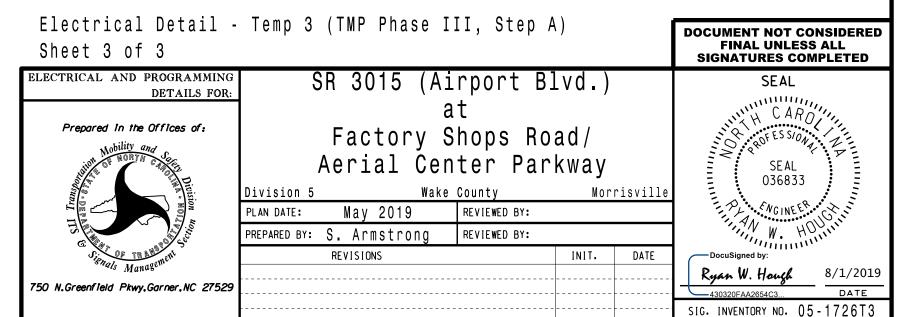
- 1. From Main Menu select 6. DETECTORS
- 2. From DETECTOR Submenu select | 3. PED DETECTOR INPUT ASSIGNMENT

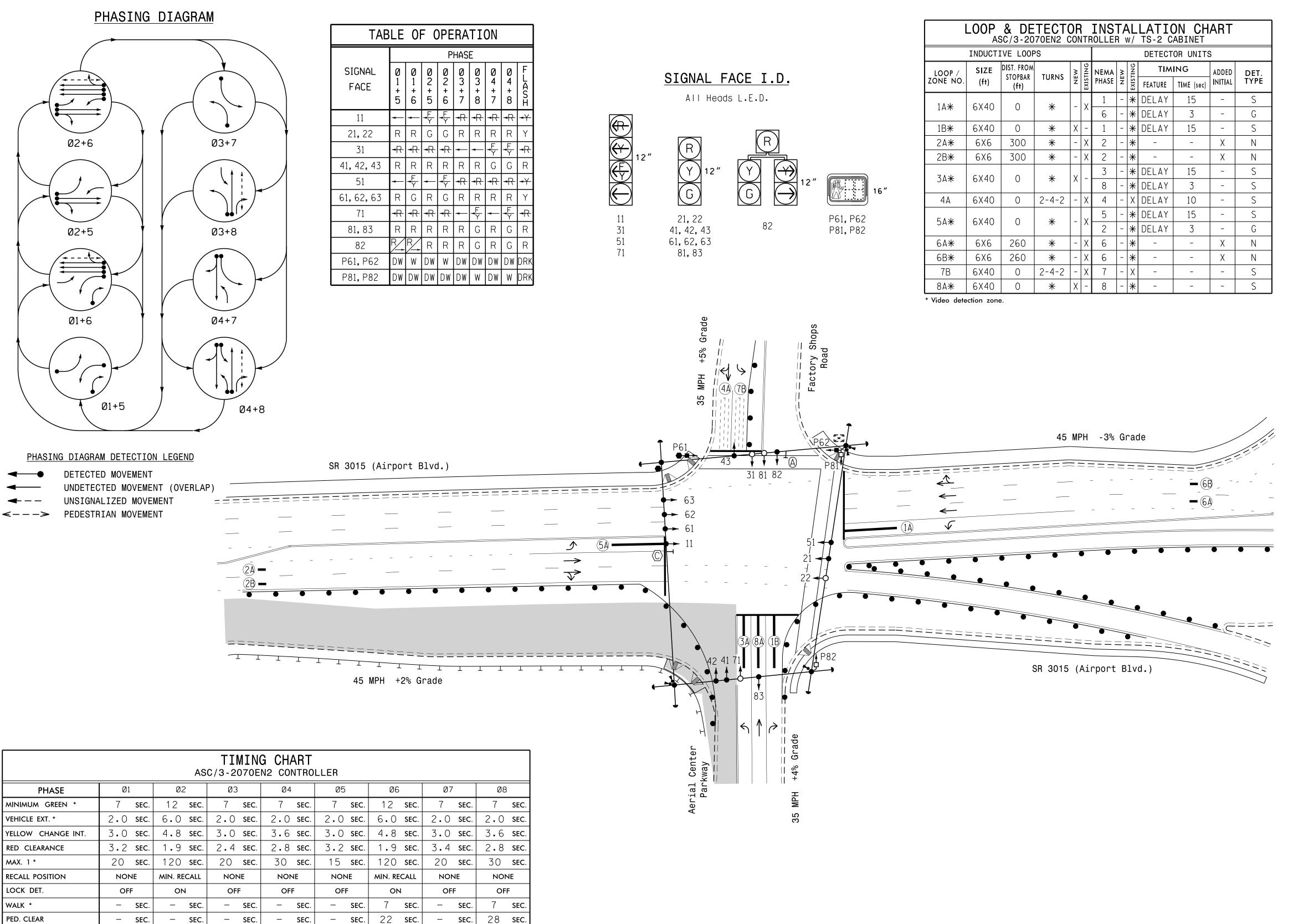


- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726T3 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A





8 Phase Fully Actuated (Cary Signal System)

NOTES

1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.

PROJECT REFERENCE NO.

I-5700

Sig. 10.0

- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 and/or phase 5 may be lagged.
- 4. Phase 3 and/or phase 7 may be lagged.
- 5. Renumber existing loop numbered 3C to 4A.
 - Set all detector units to presence mode.
- 7. Renumber existing signal heads numbered 33, 34, 35, 42, and 43 to 41, 42, 43,
- 82, and 83, respectively. 8. Reposition existing signal heads numbered
- 9. Omit "WALK" and flashing "DON'T WALK" with no pedestrian calls.
- 10. Program pedestrian heads to countdown the flashing "Don't Walk" time only.
- 11. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- 12. Cary signal system data: Fiber Channel #: 26.

21 and 83.

13. This intersection features a video detection system. Shown locations of detectors are conceptual only. Refer to the manufacturer's guidelines for optimal detector placement.

LEGEND <u>EXISTING</u> **PROPOSED** Traffic Signal Head Modified Signal Head N/A Pedestrian Signal Head With Push Button & Sign Signal Pole with Guy Signal Pole with Sidewalk Guy Inductive Loop Detector \boxtimes Controller & Cabinet Junction Box 2-in Underground Conduit N/A Right of Way \longrightarrow Directional Arrow Guardrail Type II Signal Pedestal Curb Ramp Out of Pavement Detector Video Detection Area Construction Zone Drums Construction Zone Right Arrow "ONLY" Sign (R3-5R) "U-TURN YIELD TO RIGHT TURN" Sign (R10-16)

FINAL UNLESS ALL

SEAL

CARN

026486

SIG. INVENTORY NO. 05-1726T4

DOCUMENT NOT CONSIDERED Signal Upgrade - Temporary Design 4 (TMP Phase III, Step B) SIGNATURES COMPLETED SR 3015 (Airport Blvd.) Factory Shops Road/ Aerial Center Parkway Division 5 Wake County March 2019 REVIEWED BY: 750 N.Greenfleid Pkwy.Garner.NC 27529 PREPARED BY: J.A. Lohr REVIEWED BY: REVISIONS INIT. DATE

1"=40'

OFF

SEC.

SEC.

ON

1.5 **SEC**.

34 **SEC**.

15 **SEC**.

45 **SEC**.

3.0 **SEC**.

OFF

SEC.

SEC.

SEC.

SEC.

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all

OFF

SEC.

SEC.

SEC.

ON

SEC.

OFF

SEC.

- SEC.

- SEC.

OFF

ON

1.5 **SEC**.

30 **SEC**.

15 **SEC**.

45 **SEC**.

3.0 **SEC**.

OFF

OFF

SEC.

SEC.

— SEC.

— SEC.

— SEC.

ON

ON

OFF

SEC.

SEC.

VOLUME DENSITY

MAX. INITIAL *

ACTUATION B4 ADD

SEC. PER ACTUATION

TIME B4 REDUCTION

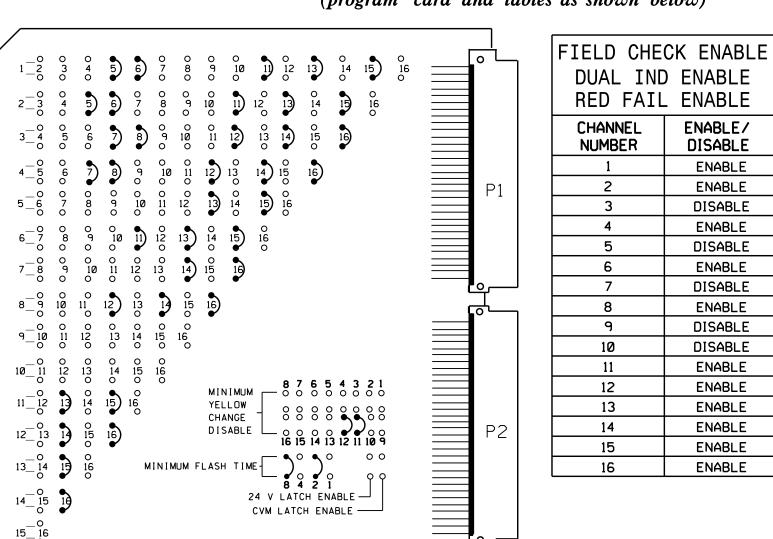
TIME TO REDUCE '

SIMULTANEOUS GAP

MINIMUM GAP

DUAL ENTRY

(program card and tables as shown below)



UNIT OPTIONS											
OPTION	SETTING										
RECURRENT PULSE	ON										
WALK DISABLE	OFF										
LOG CVM FAULTS	ON										
EXTERN WATCHDOG	OFF										
24V-2=12VDC	OFF										
PGM CARD MEMORY	ON										
LEDguard	ON										
FORCE TYPE 16	OFF										
TYPE12-SDLC	OFF										
VM 3x/Day Latch	ON										

FLASHING YELLOW ARROW											
CONFIG MODE	В										
ENABLE CHANN	NEL PAIR, FYA										
CH 1-13	ON										
CH 3-14	ON										
CH 5-15	ON										
CH 7-16	ON										
RED/YEL INF	PUT ENABLE										
CH 1	ON										
CH 3	ON										
CH 5	ON										
CH 7	ON										
LASH RATE FAULT	ON										
FYA TRAP DETECT	ON										

MMU PROGRAMMING NOTE

ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

RACK	S L O T	сн1 L1 Ø 1	сн1 L7 Ø3	CH1 L5 NOT USED	S L O T	сн1 L9 Ø 5	сн1 L 15 Ø 3	SLOT	SLOT	S L O T	S L O T		
#1	BIU	E M P T Y	сн2 L2 Ø6 *	сн2 L8 Ø8	сн2 L6 ø 4	E M P T Y	сн2 L 1 0 ø 2 *	CH2 L16 NOT USED	EMPTY	EMPTY	E M P T Y	E M P T Y	

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN IN THE CHART BEL

MMU PROGRAMMING CARD

	IN THE CE	HART BELUW
	LOOP NO.	LOOP PANEL TERMINALS
ADD JUMPERS FROM: L1A TO L2A, AND	1 A	L1A,L1B
LIB TO L2B	τ .	L2A,L2B
	NU	L3A,L3B
	NU	L4A,L4B
	NU	L5A,L5B
	4 A	L6A,L6B
ADD JUMPERS FROM: L7A TO L8A, AND	3 A	L7A,L7B
L7B TO L8B	Σ(L8A,L8B
ADD JUMPERS FROM: L9A TO L10A, AND	5A	L9A,L9B
L9B TO L10B	3(L10A,L10B
	NU	L11A,L11B
	NU	L12A,L12B
	NU	L13A,L13B
	NU	L14A,L14B
	7B	L15A,L15B
	NU	L16A,L16B

ROGRAM	CON	ITRO	LLER	DE	TEC	TOF	RS
ACCORD	ING	TO	THE	SC	HED	ULE	
SHOWN	ΙN	THE	CHAI	RT	BEL	.OW	

SHU) LOW	WIN		SHO	WN	ΙN	THE	CHART E	BELOW			
NEL		CONTROLI		ER	ELIN	CTION	TIMING				
S		DE TE	ECTOR	NO.	r UN	CIION	FEATURE	TIME(SEC)			
1 B			1		Ø	1	DELAY	15			
2B		*	2		Ø	6	DELAY	3			
3B			3								
4B			4								
5B			5								
6B			6		Ø	4	DELAY	10			
7B			7		Ø	3	DELAY	15			
3B			8		Ø	8	DELAY	3			
9B			9		Ø	5	DELAY	15			
1 OB		*	10		Ø	2	DELAY	3			
1 1 B			11								
12B			12								
13B			13								
14B			14								
15B			15		ø	7					
16B			16								

* Detector Type - G (remove delay from existing detector card)

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans for zones 1A, 1B, 2A, 2B, 3A, 5A, 6A, 6B, and 8A.

For Detection Zones 1A, 3A, and 5A, the equipment placement and slots reserved for wired inputs are typical for a NCDOT installation.

NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 9 and 10 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (LS AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phase 2 Green and 6 Walk.
- 4. Set power-up flash time to 10 seconds and implement on the Malfunction Management Unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6 for volume density operation.
- 10. Program phases 4 and 8 for dual entry.
- 11. The cabinet and controller are a part of the Cary Signal System.

	PROJECT REFERENCE NO.	SHEET NO.
	I-5700	Sig 10.1

				SIG	iNAL	. HI	EAD	HC	OK-	UP	СН	4RT					
PHASE	1		2	3	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	11*	82	21,22	31	41,42, 43	5 1	61 , 62 63	71 ★	81 , 82 , 83	NU	NU	P61, P62	P81, P82	11	31	★ 51	71 [★]
RED		*	2R	*	4R	*	6R	*	8R								
YELLOW			2Y	*	4 Y	*	6Y	*	8Y								
GREEN			2G		4G		6G		8G								
RED ARROW														13R	14R	15R	16R
YELLOW ARROW		1Y												13Y	14Y	15Y	16Y
FLASHING YELLOW ARROW														13G	14G	15G	16G
GREEN ARROW	1G	1G		3G		5G		7G									
₩												11R	12R				
×												11G	12G				

NU = Not Used

- * Denotes install load resistor. See Load Resistor Installation Detail on sheet 3.
- ★ See pictorial of head wiring detail this sheet.

EQUIPMENT INFORMATION

CONTROLLER2070EN2
CABINETNC-8 TS-2
SOFTWAREECONOLITE ASC/3-2070
CABINET MOUNTBASE
LOADBAY POSITIONS16
LOAD SWITCHES USED1,2,3,4,5,6,7,8,11,12,13,14,15,16
PHASES USED1,2,3,4,5,6,6PED,7,8,8PED
OL A*
OLB*
OLC*
OLD*

* SEE OVERLAP PROGRAMMING DETAIL ON SHEET 2

LOAD SWITCH

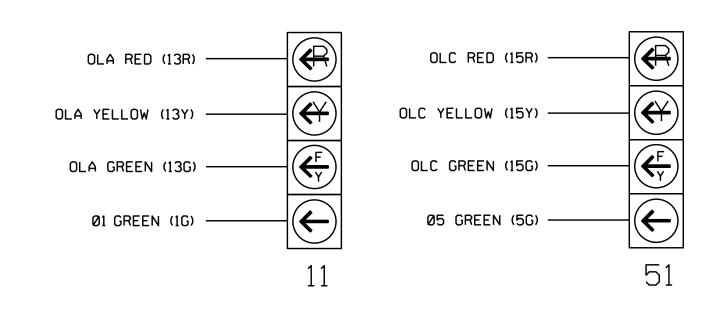
(program controller according to schedule in chart below)

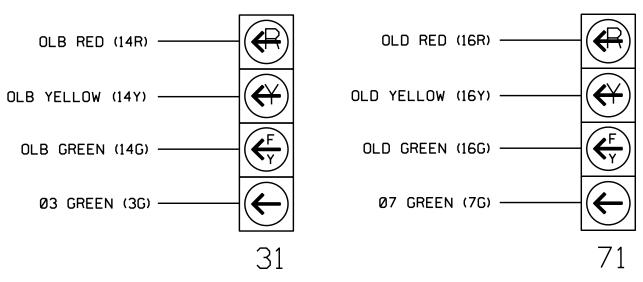
ASSIGNMENT DETAIL

LOAD SWITCH NUMBER	FUNCTION
1	Ø 1
2	ø 2
3	ø 3
4	ø 4
5	ø 5
6	ø 6
7	ø 7
8	ø 8
9	Ø2 PED
10	Ø4 PED
11	Ø6 PED
12	Ø8 PED
13	OLA
14	OLB
15	OLC
16	OLD

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)





Electrical Detail - Temp 4 (TMP Phase III, Step B) Sheet 1 of 3

DETAILS FOR: Prepared in the Offices of:

750 N.Greenfield Pkwy, Garner, NC 27529

ELECTRICAL AND PROGRAMMING

SR 3015 (Airport Blvd.)

Factory Shops Road/ Aerial Center Parkway

ivision 5 REVIEWED BY: PLAN DATE: May 2019 PREPARED BY: S. Armstrong Reviewed BY: REVISIONS INIT. DATE

SIGNATURES COMPLETED 036833

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

SIG. INVENTORY NO. 05-1726T4

SEALED: 7/24/2019 REVISED: N/A

DESIGNED: March 2019

THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: 05-1726T4

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA' TMG VEH OVLP...[A] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH13 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0 Toggle Once OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

TMG VEH OVLP...[B] TYPE:PPLT FYA PROTECTED LEFT TURN.... PHASE 3 OPPOSING THROUGH..... PHASE 4 FLASHING ARROW OUTPUT....CH14 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0 Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA' TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH15 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0 Toggle Once

Select TMG VEH OVLP [D] and 'PPLT FYA'

OVERLAP D

TMG VEH OVLP...[D] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 7 OPPOSING THROUGH..... PHASE 8 FLASHING ARROW OUTPUT....CH16 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

ECONOLITE ASC/3-2070 SPECIAL MMU PROGRAMMING

(program controller as shown)

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 4. PORT 1 (SDLC)
- 3. From PORT 1 (SDLC) Submenu select 2. MMU PROGRAM

CAUTION!

Set intersection to Flash before attempting to enter or change any MMU programming data.

This programming and that of the MMU programming card must match exactly. If they do not, the intersection will be placed into Flash.

MMU PROGRAM [MANUAL] CH 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 . X . X . X X X . . . 2 . X . X . X X X . . 3 X . X . X . . . X X . . . 4 X . X . X . . . X X . . 5 . X . X 6 . X . X . X 7 X . X 8 X . X . X . . . 9 10 11 . X . X . 12 X . X . 13 . X . 14 X . 15 .

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726T4 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A

Electrical Detail - Temp 4 (TMP Phase III, Step B) Sheet 2 of 3 ELECTRICAL AND PROGRAMMING

DETAILS FOR: Prepared in the Offices of:

750 N.Greenfield Pkwy, Garner, NC 27529

SR 3015 (Airport Blvd.)

Factory Shops Road/ Aerial Center Parkway

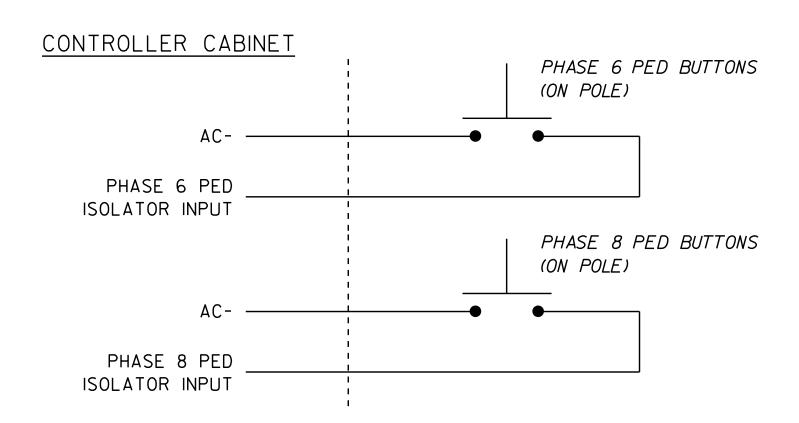
Division 5 May 2019 REVIEWED BY: PLAN DATE: PREPARED BY: S. Armstrong Reviewed BY: REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 05-1726T4

036833

(wire push buttons as shown)

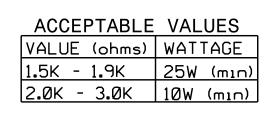


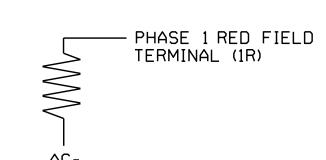
COUNTDOWN PEDESTRIAN SIGNAL OPERATION

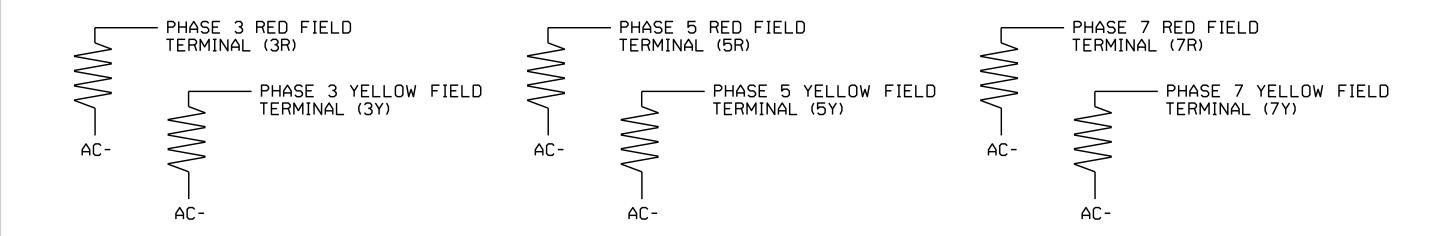
Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)





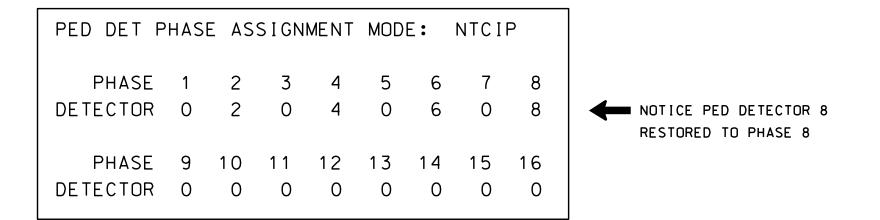


PROJECT REFERENCE NO. I-5700 Sig 10.3

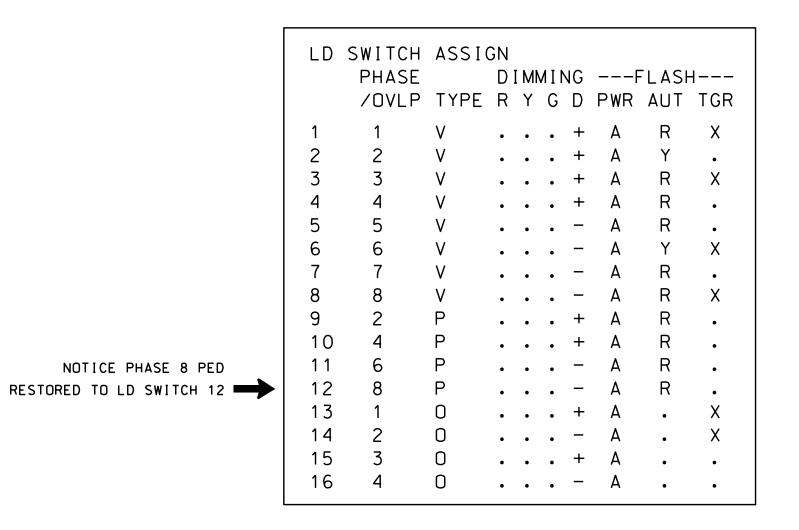
ECONOLITE ASC/3-2070 RESTORE PED 8 PROGRAMMING ASSIGNMENT DETAIL

(program controller as shown)

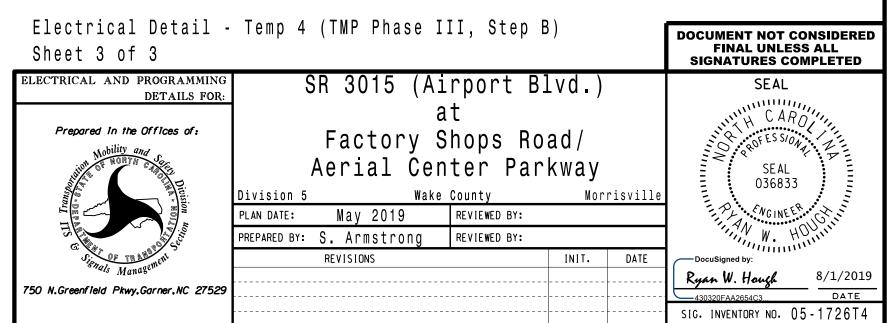
- 1. From Main Menu select | 6. DETECTORS
- 2. From DETECTOR Submenu select | 3. PED DETECTOR INPUT ASSIGNMENT

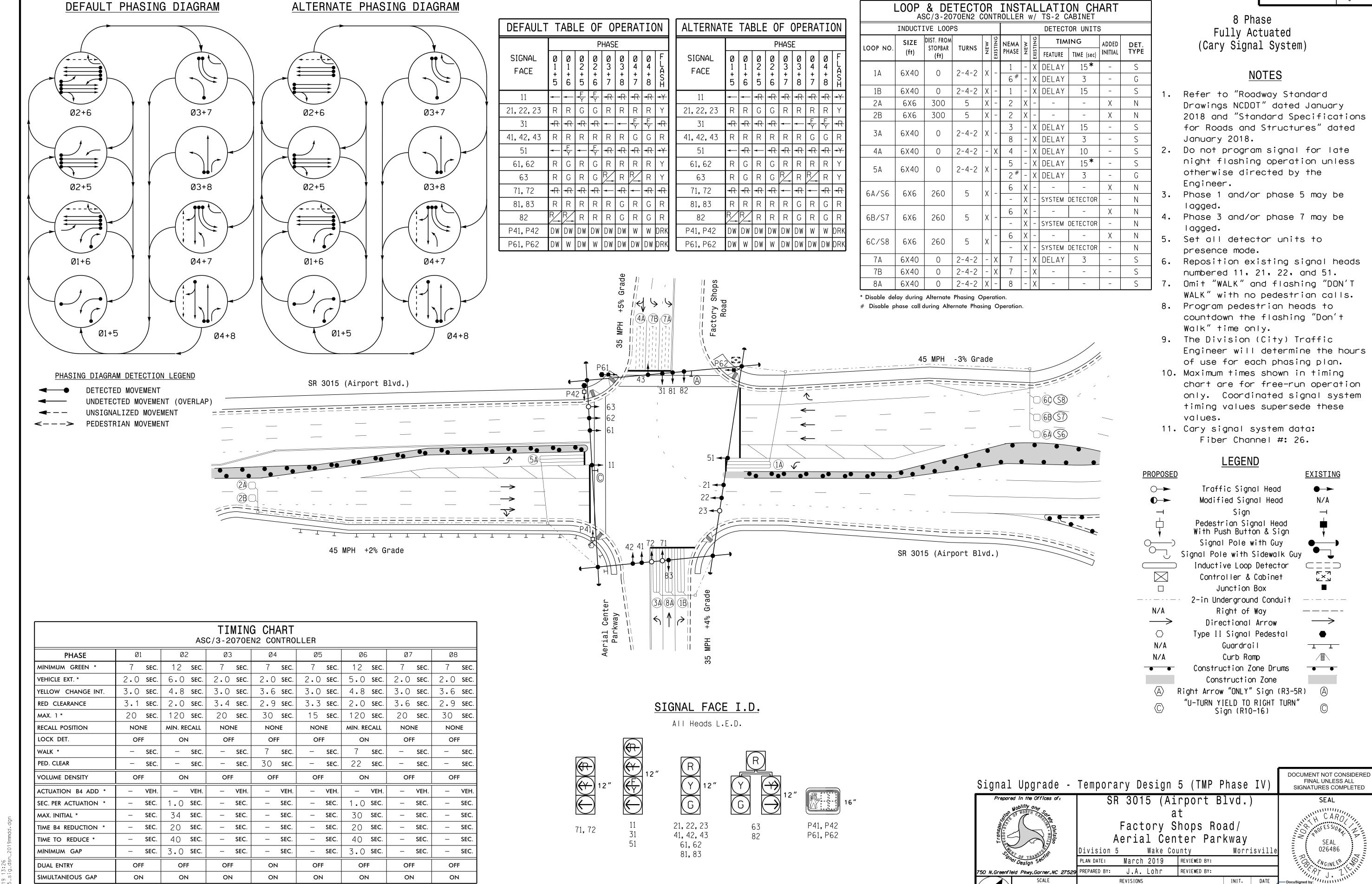


- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select | 3. LOAD SW ASSIGN



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726T4 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A





PROJECT REFERENCE NO.

I-5700

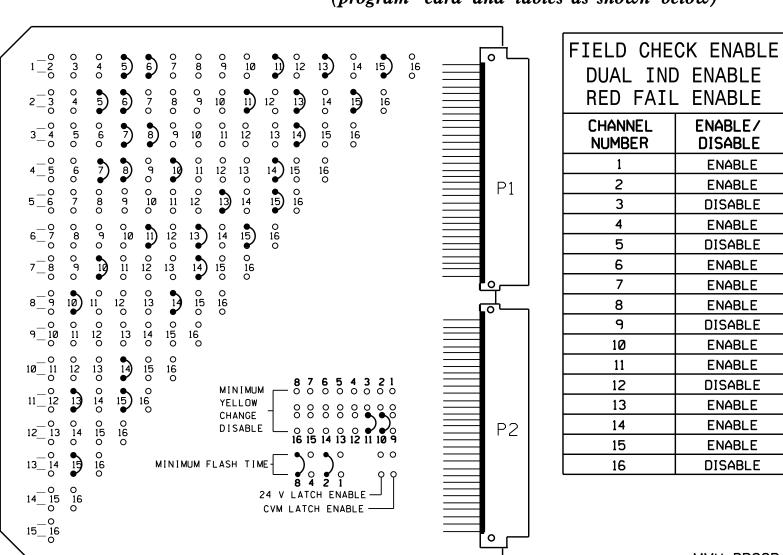
SIG. INVENTORY NO. 05-1726T

1"=40'

Sig. 11.0

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all

(program card and tables as shown below)



OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3×/Day Latch	ON

FLASHING YE	ELLOW ARROW
CONFIG MODE	В
ENABLE CHANN	NEL PAIR, FYA
CH 1-13	ON
CH 3-14	ON
CH 5-15	ON
CH 7-16	OFF
RED/YEL INF	PUT ENABLE
CH 1	ON
CH 3	ON
CH 5	ON
CH 7	OFF
FLASH RATE FAULT	ON
FYA TRAP DETECT	ON

MMU PROGRAMMING NOTE

ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1	CH1	CH1	CH1	CH1	CH1	CH1			
		L3	L 1	L7	L5	L11	L9	L15	L13	S	S	S
		ø 1	ø 1	Ø 3	Ø 2	ø6	ø 5	ø 7	Ø 6	L	L	L
RACK	BIU				**	** SYS6			** SYS8	O T	O T	O T
# 1		CH2	CH2	CH2	CH2	CH2	CH2	CH2	CH2	Ε	Ε	E
		L4	L2	L8	L6	L12	L10	L16	L14	M	M	M
		ø 2	ø 6	ø 8	ø 4	ø6	ø 2	ø 8	ø 7	T	T .	T
		**	*			** SYS7	*			Y	Y	Y

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN IN THE CHART BELOW

MMU PROGRAMMING CARD

	LOOP NO.	TERMINALS
ADD JUMPERS FROM: L1A TO L2A, AND	1 A	L1A,L1B
LIB TO L2B	14	L2A,L2B
	1B	L3A,L3B
	2A	L4A,L4B
	2B	L5A,L5B
	4 A	L6A,L6B
ADD JUMPERS FROM: L7A TO L8A, AND	3A	L7A,L7B
L7A TO L8A, AND	34	L8A,L8B
ADD JUMPERS FROM: L9A TO L10A, AND	5A	L9A,L9B
CAH IO CIGH! HIND]	

PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

	IIV I 🗆 Г . Г	IAKI DELUW							
		LOOP PANEL	1	CON	TROLLER	CUNCT LON	TIMING		
	LOOP NO.	TERMINALS		DETE	CTOR NO.	FUNCTION	FEATURE	TIME (SEC	
OD JUMPERS FROM: L1A TO L2A, AND	1 A	L1A,L1B	*		1	ø 1	DELAY	15	
LIB TO L2B	1 4	L2A,L2B	*	*	2	ø 6	DELAY	3	
	1B	L3A,L3B			3	ø 1	DELAY	15	
	2A	L4A,L4B		**	4	ø 2			
	2B	L5A,L5B		**	5	ø 2			
	4 A	L6A,L6B			6	ø 4	DELAY	10	
DD JUMPERS FROM: L7A TO L8A, AND	3A	L7A,L7B			7	ø 3	DELAY	15	
L7B TO L8B		L8A,L8B	*		8	ø 8	DELAY	3	
OD JUMPERS FROM: L9A TO L10A, AND	5A	L9A,L9B		*		9	ø 5	DELAY	15
L9B TO L10B		L10A,L10B		*	10	ø 2	DELAY	3	
	6A/S6 L11A,L11B *			**	11	ø 6	SYSTEM		
	6B/S7	L12A,L12B		**	12	ø 6	SYSTEM		
	6C/S8	L13A,L13B		**	13	ø 6	SYSTEM		
	7 A	L14A,L14B			14	ø 7	DELAY	3	
	7B	L15A,L15B			15	ø 7			
	8.8	L16A,L16B			16	ø 8			
							·	·	

- * Detector Type G (remove delay from existing detector card)
- ** Detector Type N
- ★ See the Vehicle Detector Setup Programming Detail on sheet 4 for Alternate Phasing.

NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 9, 12, and 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (LS AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phase 2 Green and 6 Walk.
- 4. Set power-up flash time to 10 seconds and implement on the Malfunction Management Unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6 for volume density operation.
- 10. Program phase 4 for dual entry.
- 11. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO.	SHEET NO.
I - 5700	Sig 11.1

SIGNAL HEAD HOOK-UP CHART																		
PHASE	1		2	3	4	5	6		7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	11*	82	21 , 22 23	31	41,42, 43	5 1	61 , 62 63	63	71,72	81 , 82 , 83	NU	P41, P42	P61. P62	NU	11★	3 1	★ 51	NU
RED		*	2R	*	4R	*	6R			8R								
YELLOW			2Y	*	4 Y	*	6Y			8Y								
GREEN			2G		4G		6G			8G								
RED ARROW									7R						13R	14R	15R	
YELLOW ARROW		1Y						7Y	7Y						13Y	14Y	15Y	
FLASHING YELLOW ARROW															13G	14G	15G	
GREEN ARROW	1G	1G		3G		5G		7G	7G									
₩												10R	11R					
Ķ												1ØG	11G					

NU = Not Used

- * Denotes install load resistor. See Load Resistor Installation Detail on sheet 3.
- ★ See pictorial of head wiring detail this sheet.

EQUIPMENT INFORMATION

CONTROLLER......2070EN2

SOFTWAREECONOLITE ASC/3-2070

CABINET MOUNT.....BASE LOADBAY POSITIONS.....16

LOAD SWITCHES USED.....1,2,3,4,5,6,7,8,10,11,13,14,15

OL A * OLB....* OLC....* OLD....*

THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: 05-1726T5

DESIGNED: March 2019

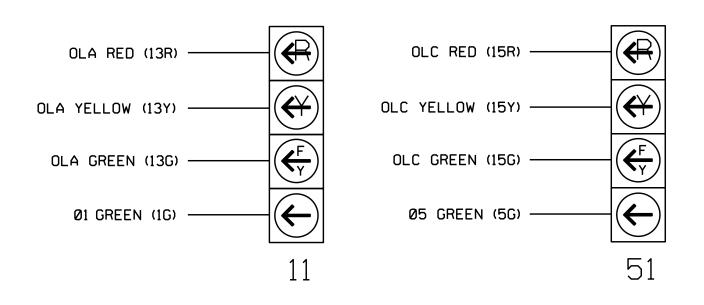
SEALED: 7/24/2019

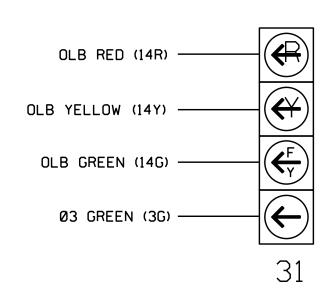
REVISED: N/A

* SEE OVERLAP PROGRAMMING DETAIL ON SHEET 2

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)





LOAD SWITCH ASSIGNMENT DETAIL

(program controller according to schedule in chart below)

LOAD SWITCH FUNCTION

NUMBER	FUNCTION
1	ø 1
2	ø 2
3	ø 3
4	ø 4
5	ø 5
6	ø 6
7	ø 7
8	ø 8
9	Ø2 PED
10	Ø4 PED
11	ø6 PED
12	Ø8 PED
13	OLA
14	OLB
15	OLC
16	OLD

Electrical Detail - Temp 5 (TMP Phase IV) Sheet 1 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of:

SR 3015 (Airport Blvd.)

Factory Shops Road/ Aerial Center Parkway ivision 5 REVIEWED BY: PLAN DATE: May 2019

PREPARED BY: S. Armstrong Reviewed BY: REVISIONS INIT. DATE

036833 Ryan W. Hough

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

SIGNATURES COMPLETED

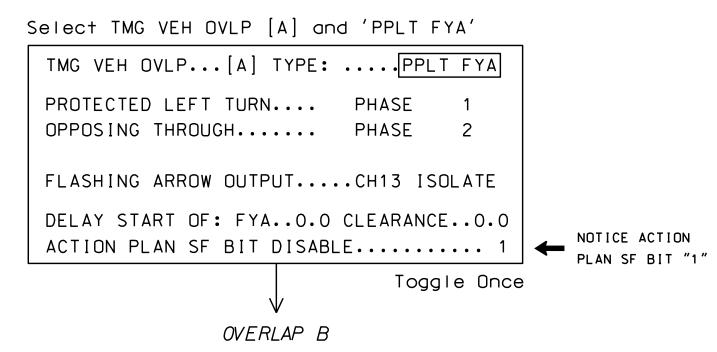
750 N.Greenfield Pkwy.Garner.NC 27529 SIG. INVENTORY NO. 05-1726T5

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

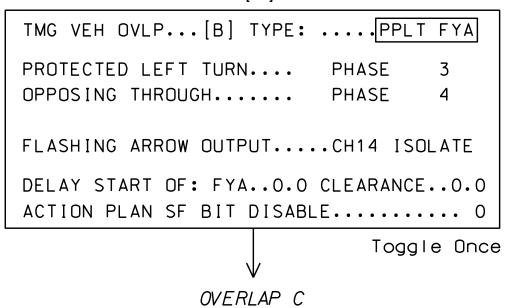
(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

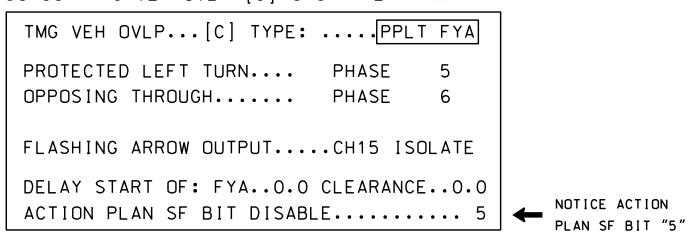
OVERLAP A



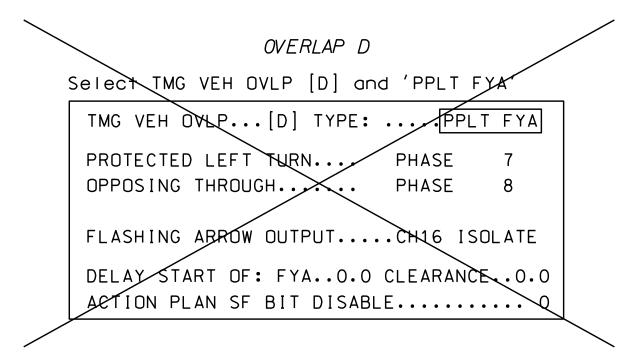
Select TMG VEH OVLP [B] and 'PPLT FYA'



Select TMG VEH OVLP [C] and 'PPLT FYA'



END PROGRAMMING



DELETE THIS PROGRAMMING

ECONOLITE ASC/3-2070 SPECIAL MMU PROGRAMMING

(program controller as shown)

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select | 4. PORT 1 (SDLC)
- 3. From PORT 1 (SDLC) Submenu select 2. MMU PROGRAM

CAUTION!

Set intersection to Flash before attempting to enter or change any MMU programming data.

This programming and that of the MMU programming card must match exactly. If they do not, the intersection will be placed into Flash.

MMU PR	OGRA	М	[MA	١٨٤	JAL	_]								
	СН	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2
	1	•	Χ	•	Χ	•	Χ	•	•	•	•	Χ	Χ	•	•	•
	2	•	Χ	•	Χ	•	Χ	•	•	•	•	Χ	Χ	•	•	
	3	•	•	Χ	•	•	•	•	•	Χ	Χ	•	•	•		
	4	•	•	Χ	•	•	•	Χ	•	Χ	Χ	•	•			
	5	•	Χ	•	Χ	•	•	•	•	•	•	•				
	6	•	Χ	•	Χ	•	Χ	•	•	•	•					
	7	•	•	Χ	•	•	•	Χ	•	•						
	8	•	•	Χ	•	•	•	Χ	•							
	9	•	•	•	•	•	•	•								
	10	•	•	Χ	•	•	•									
	11	•	Χ	•	Χ	•										
	12	•	•	•	•											
	13	•	X	•												
	14	•	•													
	15	•														

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726T5 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A

Electrical Detail - Temp 5 (TMP Phase IV) Sheet 2 of 5 ELECTRICAL AND PROGRAMMING SR 3015 (Airport Blvd.) DETAILS FOR:

750 N.Greenfield Pkwy.Garner.NC 27529

Factory Shops Road/ Aerial Center Parkway

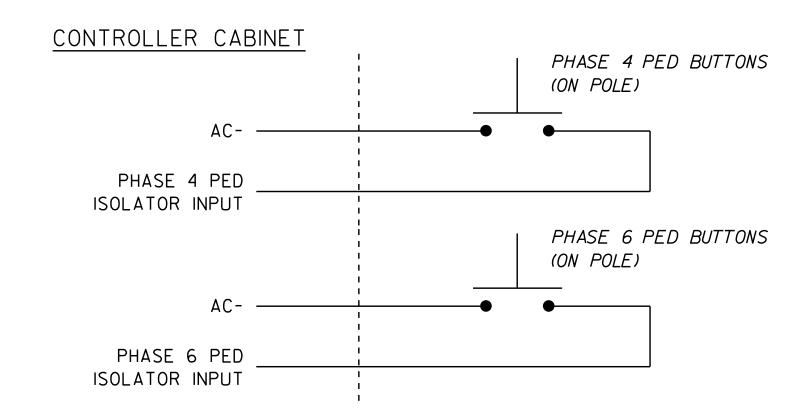
Division 5 May 2019 REVIEWED BY: PLAN DATE: PREPARED BY: S. Armstrong Reviewed BY: REVISIONS INIT. DATE

036833

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SIG. INVENTORY NO. 05-1726T5

(wire push buttons as shown)

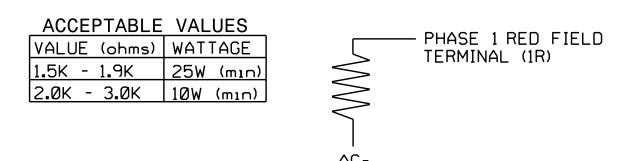


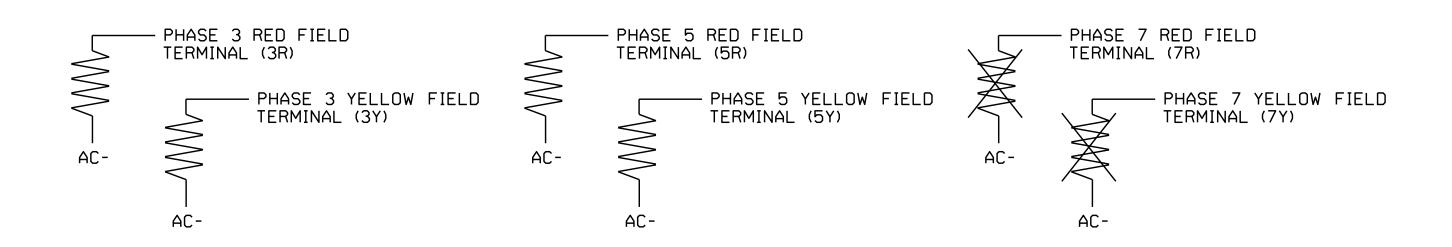
COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)





IMPORTANT! Remove phase 7 load resistors as shown.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726T5 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A

Electrical Detail - Temp 5 (TMP Phase IV) Sheet 3 of 5 ELECTRICAL AND PROGRAMMING SR 3015 (Airport Blvd.) DETAILS FOR:

750 N.Greenfield Pkwy, Garner, NC 27529

Factory Shops Road/ Aerial Center Parkway Wake County

Division 5 Morrisville PLAN DATE: May 2019 REVIEWED BY: PREPARED BY: S. Armstrong REVIEWED BY: REVISIONS INIT. DATE

036833

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 05-1726T5

ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A AND 5A

(program controller as shown)

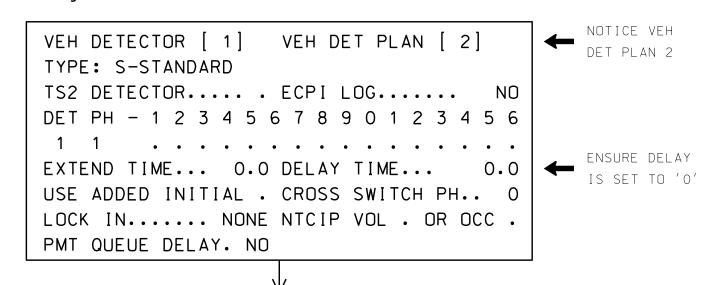
IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

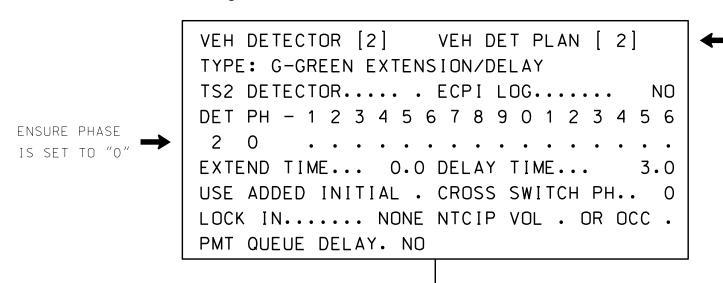
- 1. From Main Menu select | 8. UTILITIES
- 2. From UTILITIES Submenu select | 1. COPY/CLEAR
- 3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

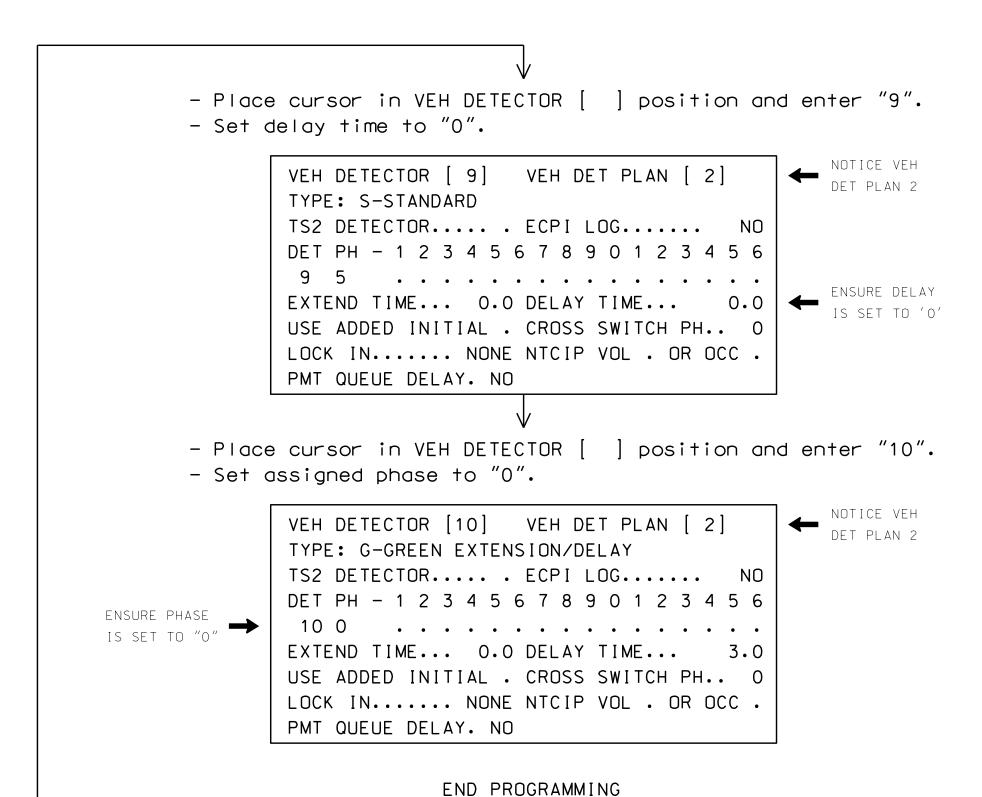
COPY / CLEAR UTILITY FROM TO PHASE TIMING.... > PHASE TIMING.... TIMING PLAN..... > TIMING PLAN..... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select | 6. DETECTORS
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [] position and enter "2".
 - Place cursor in VEH DETECTOR [] position and enter "1".
 - Set delay time to "0".

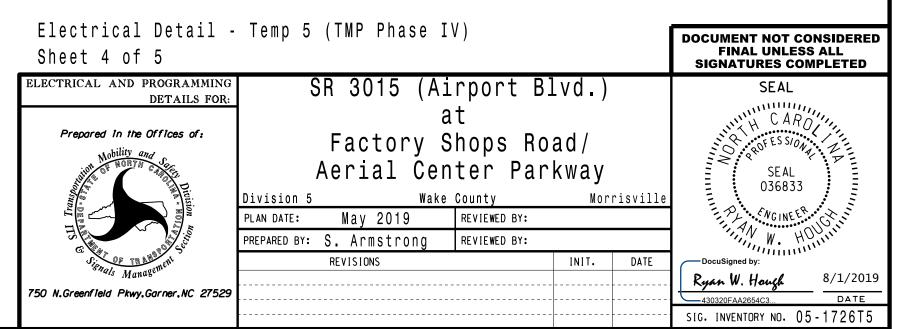


- Place cursor in VEH DETECTOR [] position and enter "2".
- Set assigned phase to "0".





THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726T5 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A



ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN.

SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED

TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED

TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

NONE	
1, 5	

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1.5: Modifies overlap parent phases for heads 11 and 51 to run

protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1

and reduces delay time for phase call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select 5. TIME BASE

2. From TIME BASE Submenu select 2. ACTION PLAN

	2. From TI	ML	BAS	sE S	subi	mer	nu s	ele	ec†	2	• A	CII	UN	PLA	N I		
	ACTION PLA PATTERN TIMING PLA VEH DETECT	AN. TOR	··· ··· PL	A AN.	UTO • 0 • 2		SEQ DET	UEN LC	CE.	• • •	• • •	NON	O IE				
		VEH DET DIAG PLN O							RED REST NO PED DET DIAG PLN0								
									TY								
	PED PR RE						QUE										
	PMT COND I						QUL	UL	DLL	A · •	• • •	• '					
		1					6	7	8	9	0	1	2	3	4	5	6
	PED RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	WALK 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	VEX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	VEH RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	MAX RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	MAX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
	MAX 3	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	CS INH	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
NOTICE	OMIT	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
SPC FCT BITS	SPC FCT	Χ	•	•	•	X	•	•	•	(1	-8)						
DIIS	AUX FCT	•	•	•		-3 : -		_			_			_		_	
		1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
	LP 1-15	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 16-30	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 31-45	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 46-60 LP 61-75	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 76-90	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 91-100	•	•	•	•	•	• -	•	•	•	•	•	•	•	•	•	
		•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1726T5
DESIGNED: March 2019
SEALED: 7/24/2019
REVISED: N/A

Electrical Detail - Temp 5 (TMP Phase IV)

Sheet 5 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:

Division Plan

750 N.Greenfield Pkwy, Garner, NC 27529

SR 3015 (Airport Blvd.)
at

Factory Shops Road/ Aerial Center Parkway

Morrisvi

Division 5 Wake County Morrisville
PLAN DATE: May 2019 REVIEWED BY:
PREPARED BY: S. Armstrong REVIEWED BY:
REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

SEAL

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O36833

Docusigned by:

Ryan W. Hough

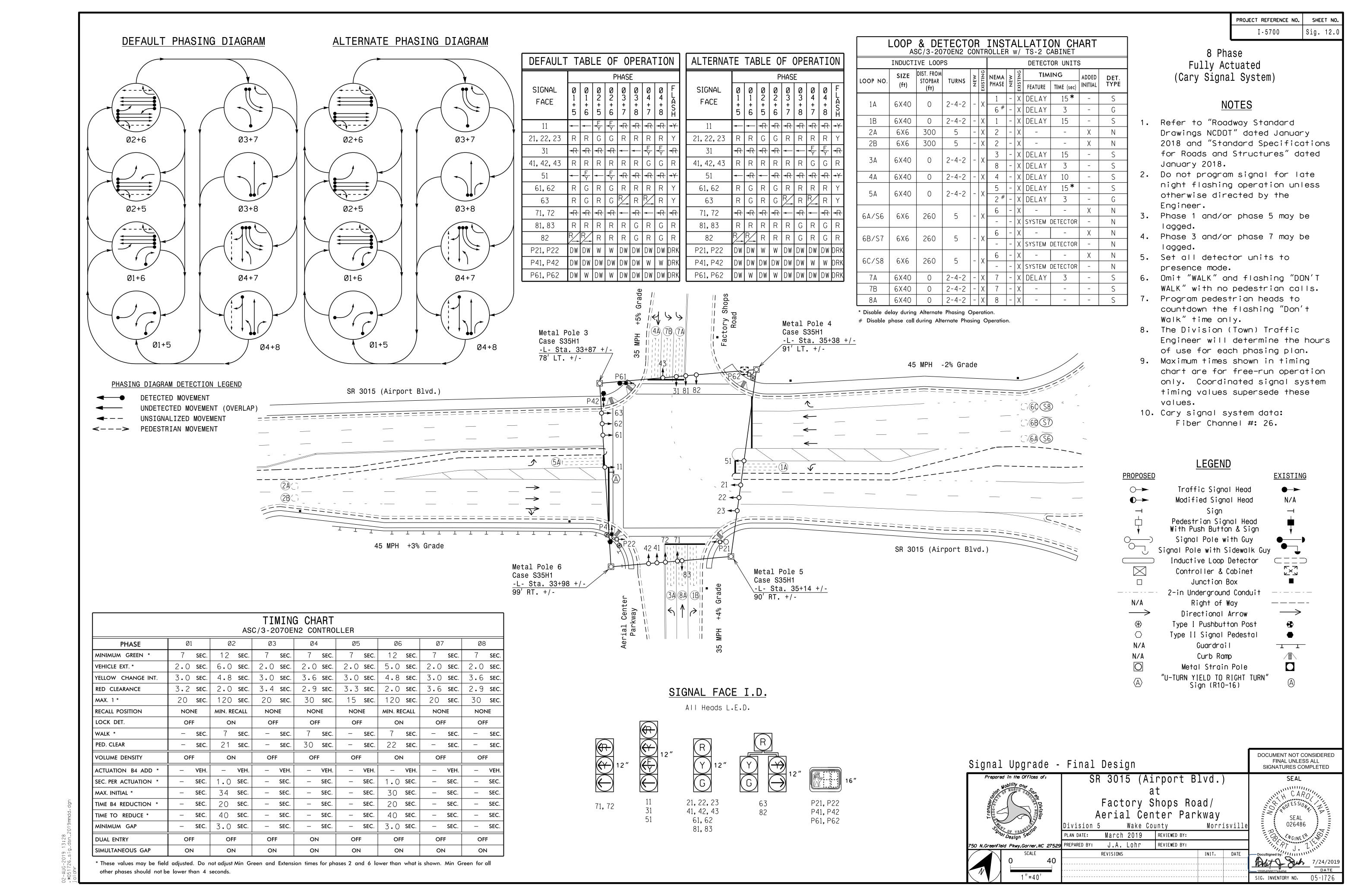
8/1/2019

Ryan W. Hough

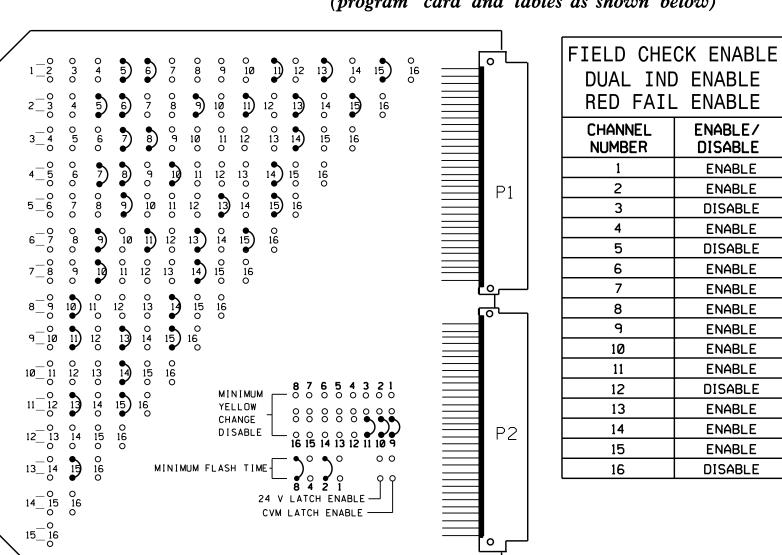
A30320EAA2654C3

DATE

SIG. INVENTORY NO. 05-1726T5



(program card and tables as shown below)



OPTION	SETTING				
RECURRENT PULSE	ON				
WALK DISABLE	OFF				
LOG CVM FAULTS	ON				
EXTERN WATCHDOG	OFF				
24V-2=12VDC	OFF				
PGM CARD MEMORY	ON				
LEDguard	ON				
FORCE TYPE 16	OFF				
TYPE12-SDLC	OFF				
VM 3x/Day Latch	ON				

FLASHING YE	LLOW ARROW							
CONFIG MODE B								
ENABLE CHANNEL PAIR, FYA								
CH 1-13	ON							
CH 3-14	ON							
CH 5-15	ON							
CH 7-16	OFF							
RED/YEL INF	PUT ENABLE							
CH 1	ON							
CH 3	ON							
CH 5	ON							
CH 7	OFF							
FLASH RATE FAULT	ON							
FYA TRAP DETECT	ON							

MMU PROGRAMMING NOTE

ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1	CH1	CH1	CH1	CH1	CH1	CH1			
		L3	∟1	L 7	L5	L11	L9	L15	L13	S	S	S
		ø 1	ø 1	ø 3	Ø 2	ø6	ø 5	Ø 7	ø6	L	L	
RACK	BIU				**	** SYS6			** SYS8	O T	O T	O T
# 1		CH2	CH2	CH2	CH2	CH2	CH2	CH2	CH2	Ε	Ε	E
		L4	L2	L8	L6	L12	L10	L16	L14	M	M	M
		ø 2	ø 6	ø 8	ø 4	ø6	ø 2	ø 8	ø 7	T	T .	Т
		**	*			** SYS7	*			Y	Y	Y

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN IN THE CHART BELOW

MMU PROGRAMMING CARD

	LOOP NO.	TERMINALS
ADD JUMPERS FROM: L1A TO L2A.AND	1 A	L1A,L1B
LIB TO L2B	1.0	L2A,L2B
	1B	L3A,L3B
	2A	L4A,L4B
	2B	L5A,L5B
	4 A	L6A,L6B
ADD JUMPERS FROM: L7A TO L8A, AND	3A	L7A,L7B
L7B TO L8B	74	L8A,L8B
ADD JUMPERS FROM: L9A TO L10A.AND	5A	L9A,L9B
LAH IO LIMH HIND	l JA	

PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

•			1	CUN.	TROLLER		TIMING		
	LOOP NO.	LOOP PANEL TERMINALS			CTOR NO.	FUNCTION	FEATURE	TIME (SEC	
JUMPERS FROM: 1A TO L2A, AND	1 A	L1A,L1B	*		1	ø 1	DELAY	15	
1B TO L2B	1.0	L2A,L2B	*	*	2	ø 6	DELAY	3	
	1B	L3A,L3B			3	ø 1	DELAY	15	
	2A	L4A,L4B		**	4	ø 2			
	2B	L5A,L5B		**	5	ø 2			
	4 A	L6A,L6B			6	ø 4	DELAY	10	
) JUMPERS FROM: 7A TO L8A, AND	3 A	L7A,L7B			7	ø 3	DELAY	15	
7B TO L8B	56	L8A,L8B			8	ø 8	DELAY	3	
) JUMPERS FROM: 9A TO L10A, AND	5A	L9A,L9B	*		9	ø 5	DELAY	15	
9B TO L10B	3 £	L10A,L10B	*	*	10	ø 2	DELAY	3	
	6A/S6	L11A,L11B		**	11	ø6	SYSTEM		
	6B/S7	L12A,L12B		**	12	ø 6	SYSTEM		
	6C/S8	L13A,L13B		**	13	ø6	SYSTEM		
	7 A	L14A,L14B			14	ø 7	DELAY	3	
	7B	L15A,L15B			15	ø 7			
	8.8	L16A,L16B			16	ø 8			

- * Detector Type G (remove delay from existing detector card)
- ** Detector Type N
- ★ See the Vehicle Detector Setup Programming Detail on sheet 4 for Alternate Phasing.

NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 12, and 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (LS AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phase 2 Walk and 6 Walk.
- 4. Set power-up flash time to 10 seconds and implement on the Malfunction Management Unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6 for volume density operation.
- 10. Program phase 4 for dual entry.
- 11. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO.	SHEET
I-5700	Sig 12

	SIGNAL HEAD HOOK-UP CHART																	
PHASE	1		2	3	4	5	6		7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	11*	82	21 , 22 23	31	41,42, 43	★	61 . 62 63	63	71,72	81 , 82 , 83	P21, P22	P41, P42	P61, P62	NU	11	31 ★	★	NU
RED		*	2R	*	4R	*	6R			8R								
YELLOW			2Y	*	4Y	*	6Y			8Y								
GREEN			2G		4G		6G			8G								
RED ARROW									7R						13R	14R	15R	
YELLOW ARROW		1Y						7Y	7Y						13Y	14Y	15Y	
FLASHING YELLOW ARROW															13G	14G	15G	
GREEN ARROW	1G	1G		3G		5G		7G	7G									
₩											9R	10R	11R					
*											9G	10G	11G					

NU = Not Used

- * Denotes install load resistor. See Load Resistor Installation Detail on sheet 3.
- ★ See pictorial of head wiring detail this sheet.

EQUIPMENT INFORMATION

CONTROLLER......2070EN2

SOFTWAREECONOLITE ASC/3-2070

CABINET MOUNT.....BASE LOADBAY POSITIONS.....16

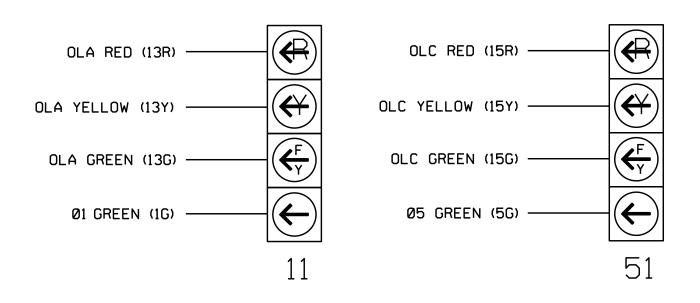
LOAD SWITCHES USED.....1,2,3,4,5,6,7,8,9,10,11,13,14,15 PHASES USED......1,2,2PED,3,4,4PED,5,6,6PED,7,8

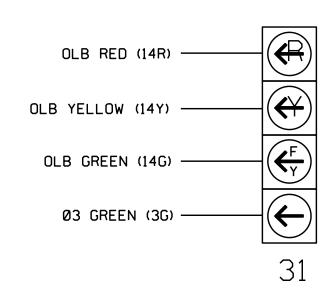
OL A * OLB....* OLC....* OLD....*

* SEE OVERLAP PROGRAMMING DETAIL ON SHEET 2

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

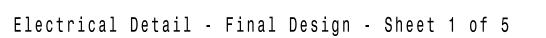




LOAD SWITCH ASSIGNMENT DETAIL

(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION						
1	ø 1						
2	ø 2						
3	ø 3						
4	Ø 4						
5	ø 5						
6	ø 6						
7	Ø 7						
8	ø 8						
9	Ø2 PED						
10	Ø4 PED						
11	Ø6 PED						
12	Ø8 PED						
13	OLA						
14	OLB						
15	OLC						
16	OLD						



ELECTRICAL AND PROGRAMMING DETAILS FOR Prepared in the Offices of:

750 N.Greenfield Pkwy, Garner, NC 27529

SR 3015 (Airport Blvd.)

Factory Shops Road/ Aerial Center Parkway

REVIEWED BY: PLAN DATE: May 2019 PREPARED BY: S. Armstrong Reviewed BY: REVISIONS INIT. DATE

036833

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

SIGNATURES COMPLETED

SIG. INVENTORY NO. 05-1726

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA' TMG VEH OVLP...[A] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH13 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE.................................. 1 ← NOTICE ACTION PLAN SF BIT "1" Toggle Once OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA' TMG VEH OVLP...[B] TYPE:PPLT FYA

PROTECTED LEFT TURN.... PHASE 3 OPPOSING THROUGH..... PHASE 4

FLASHING ARROW OUTPUT....CH14 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0 Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE:PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH15 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O

ACTION PLAN SF BIT DISABLE..... 5 ← NOTICE ACTION PLAN SF BIT "5"

END PROGRAMMING

ECONOLITE ASC/3-2070 SPECIAL MMU PROGRAMMING

(program controller as shown)

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select | 4. PORT 1 (SDLC)
- 3. From PORT 1 (SDLC) Submenu select 2. MMU PROGRAM

CAUTION!

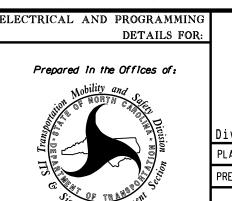
Set intersection to Flash before attempting to enter or change any MMU programming data.

This programming and that of the MMU programming card must match exactly. If they do not, the intersection will be placed into Flash.

MMU PROGRAM [MANUAL] CH 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 . X . X . X X X . . . 2 . X . X . X . . . X X . . 3 . . X X X . . . 4 . . X . . . X . X X . . 5 . X . X . . . X . . . 6 . X . X . X . X . . 7 . . X . . . X . . 8 . . X . . . X . 9 . X . X . X . 10 . . X . . . 11 . X . X . 13 . X . 14 . . 15 .

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A



750 N.Greenfield Pkwy.Garner.NC 27529

Electrical Detail - Final Design - Sheet 2 of 5 SR 3015 (Airport Blvd.)

Factory Shops Road/ Aerial Center Parkway

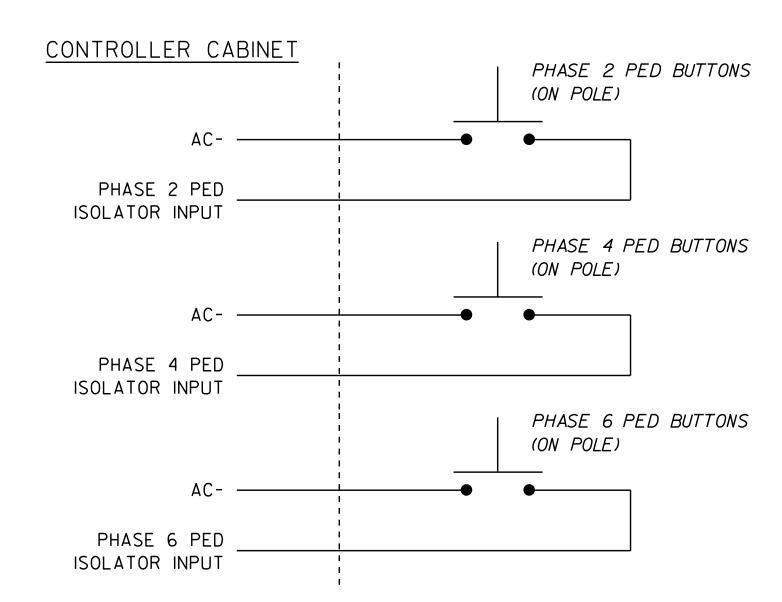
Division 5 May 2019 REVIEWED BY: PLAN DATE: PREPARED BY: S. Armstrong Reviewed BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 05-1726

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

036833

(wire push buttons as shown)



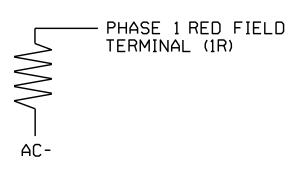
COUNTDOWN PEDESTRIAN SIGNAL OPERATION

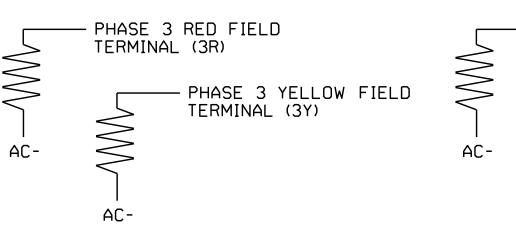
Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

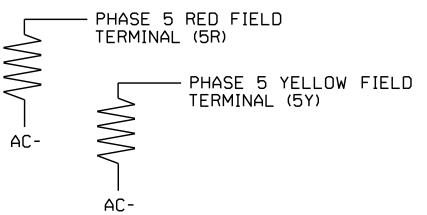
LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES VALUE (ohms) WATTAGE 1.5K - 1.9K 25W (min) 2.0K - 3.0K 10W (min)







THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A

Electrical Detail - Final Design - Sheet 3 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR:

SR 3015 (Airport Blvd.)

Factory Shops Road/ Aerial Center Parkway

Wake County Division 5 May 2019 REVIEWED BY: PLAN DATE: PREPARED BY: S. Armstrong REVIEWED BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 05-1726

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A AND 5A

(program controller as shown)

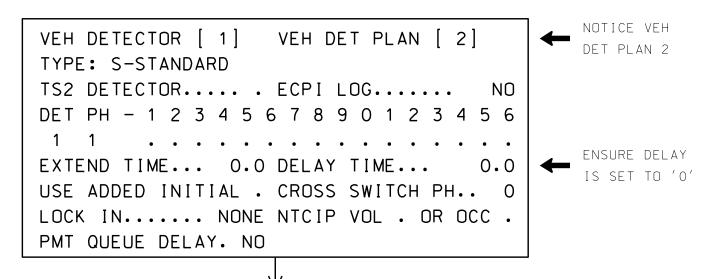
IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

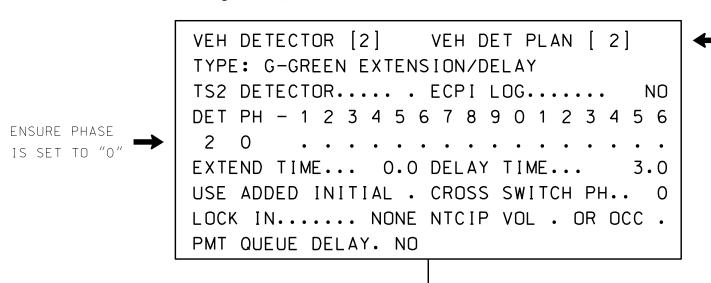
- 1. From Main Menu select | 8. UTILITIES
- 2. From UTILITIES Submenu select | 1. COPY/CLEAR
- 3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

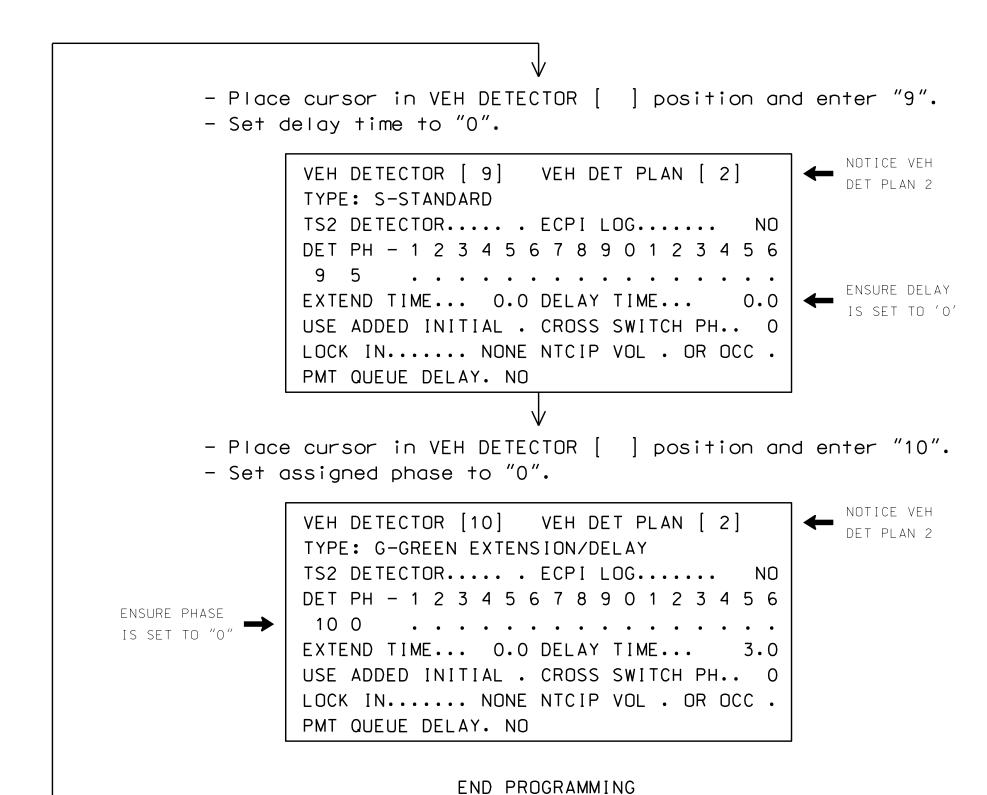
COPY / CLEAR UTILITY FROM TO PHASE TIMING.... > PHASE TIMING.... TIMING PLAN..... > TIMING PLAN..... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select | 6. DETECTORS
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [] position and enter "2".
 - Place cursor in VEH DETECTOR [] position and enter "1".
 - Set delay time to "0".



- Place cursor in VEH DETECTOR [] position and enter "2".
- Set assigned phase to "0".

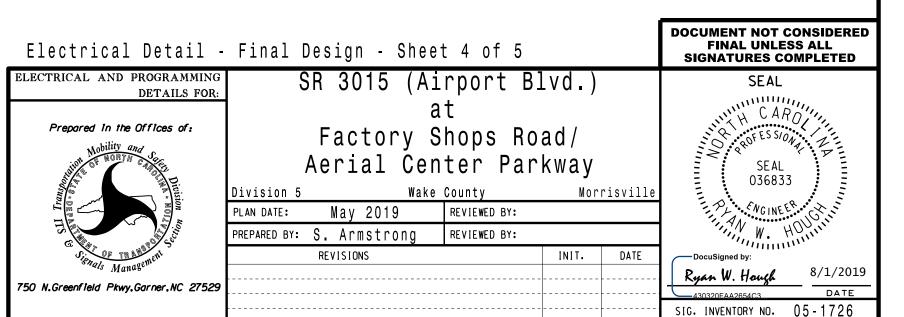




THIS ELECTRICAL DETAIL IS FOR

DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A

THE SIGNAL DESIGN: 05-1726



ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

<u>PHASING</u>	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN DEFAULT PHASING	1	NONE
ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	1, 5

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1,5: Modifies overlap parent phases for heads 11 and 51 to run

protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1

call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select 5. TIME BASE

2. From TIME BASE Submenu select | 2. ACTION PLAN |

	2. From III	VI <u>C</u>	DAS	E :	Subi	ner	iu S	ете	ЭСТ		• A	C 1 1	UN	PLA	411		
	ACTION PLA	٩N.	[1	1												
	PATTERN		_		_		SYS	OV	ERR	IDE		. N	10				
	TIMING PLA																
	VEH DETEC	ΓOR	PL	AN.	. 2		DET	LC	G			NON	ΙE				
	FLASH			•			RED	RE	ST.			. N	10				
	VEH DET DIAG PLN O PED DET DIAG PLNO																
	DIMMING ENABLE NO PRIORITY RETURN. NO																
	PED PR RETURN NO QUEUE DELAY NO PMT COND DELAY NO																
	PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
	PED RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	WALK 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	VEX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	VEH RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	MAX RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	MAX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
	MAX 3	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
	CS INH	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
NOTICE	OMIT	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
SPC FCT -	SPC FCT	X	•	•	•	Χ	•	•	•	(1	-8)						
BITS	AUX FCT	•	•	•		-3 X											
		1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
	LP 1-15	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 16-30	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 31-45	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 46-60	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 61-75	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 76-90	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	LP 91-100	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1726 DESIGNED: March 2019 SEALED: 7/24/2019 REVISED: N/A

Electrical Detail - Final Design - Sheet 5 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR:

750 N.Greenfield Pkwy, Garner, NC 27529

SR 3015 (Airport Blvd.) Factory Shops Road/

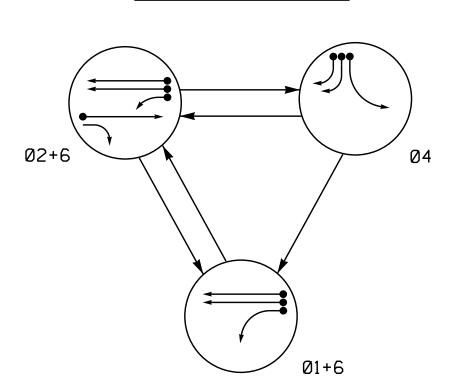
Aerial Center Parkway Wake County

Division 5 May 2019 REVIEWED BY: PLAN DATE: PREPARED BY: S. Armstrong REVIEWED BY: REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED 036833

SIG. INVENTORY NO. 05-1726

PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

UNSIGNALIZED MOVEMENT

UNDETECTED MOVEMENT (OVERLAP)

DETECTED MOVEMENT

TABLE OF	0PE	ERA [®]	TIO	N
		PHA	SE	
SIGNAL FACE	01+6	∞ N+6	04	止」位のエ
11	\	□	#	*
21, 22	R	G	R	Υ
41	#	#	\	#
42, 43	R	R	†	R
44	R	R	G	R
61, 62	1	1	R	Υ

SIGNAL FACE I.D. All Heads L.E.D. R Y 12" 21**,** 22 44 42,43 6I**,** 62

		l	l					
4B *	6X40	0	*	Х	ı	4	*	_
4C *	6X40	0	*	Х	ı	4	*	_
6A *	6X6	300	*	Х	ı	6	*	-
6B ∦	6X6	300	*	Х	١	6	*	-

6X40

INDUCTIVE LOOPS

6X6 | 300 |

STOPBAR (ft)

LOOP & DETECTOR INSTALLATION CHART ASC/3-2070EN2 CONTROLLER W/ TS-2 CABINET

DETECTOR UNITS

ADDED DET.
INITIAL TYPE

X N

X N

TIMING

- DELAY | 15

DELAY

|*****|-|DELAY|

*	Video	detection	zone.

ZONE NO.

2A*

3 Phase Fully Actuated (Cary Signal System)

NOTES

- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 1 may be lagged.
- 4. Set all detector units to presence mode.
- 5. Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
- 6. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- 7. Cary signal system data: Fiber channel # 26.
- 8. This intersection features a video detection system. Shown locations of detectors are conceptual only. Refer to the manufacturer's guidelines for optimal detector placement.

<−−> PEDESTRIAN MOVEMENT -L- Sta. 38+34 +/-101' LT. +/--L- Sta. 39+93 +/-67' LT. +/-SR 3015 (Airport Boulevard) 45 MPH -2% Grade SR 3015 (Airport Boulevard) -L- Sta. 40+25 +/-65' RT. +/--L- Sta. 38+16 +/ 88' RT. +/-

TIMING CHART ASC/3-2070EN2 CONTROLLER											
PHASE	Ø1		02	2	04		Ø6				
MINIMUM GREEN *	7	SEC.	12	SEC.	7	SEC.	12	SEC.			
VEHICLE EXT. *	2.0	SEC.	6.0	SEC.	2.0	SEC.	6.0	SEC.			
YELLOW CHANGE INT.	3.0	SEC.	4.7	SEC.	3.0	SEC.	4.7	SEC.			
RED CLEARANCE	1.8	SEC.	1.9	SEC.	1.9	SEC.	1.9	SEC.			
MAX. 1 *	25	SEC.	120	SEC.	25	SEC.	120	SEC.			
RECALL POSITION	NON	٧E	MIN. RE	CALL	ИОИ	1E	MIN. RECALL				
LOCK DET.	OFF		10	1	OFI	=	0	1			
WALK *	_	SEC.	_	SEC.	_	SEC.	_	SEC.			
PED. CLEAR	-	SEC.	_	SEC.	1	SEC.	_	SEC.			
VOLUME DENSITY	OF	F	10	1	OFI	=	ON				
ACTUATION B4 ADD *	_	VEH.	_	VEH.	_	VEH.	_	VEH.			
SEC. PER ACTUATION *	_	SEC.	2.5	SEC.	_	SEC.	1.5	SEC.			
MAX. INITIAL *	_	SEC.	34	SEC.	_	SEC.	34	SEC.			
TIME B4 REDUCTION *	_	SEC.	15	SEC.	_	SEC.	15	SEC.			
TIME TO REDUCE *	_	SEC.	45	SEC.	_	SEC.	45	SEC.			
MINIMUM GAP	_	SEC.	3.0	SEC.	_	SEC.	3.0	SEC.			
DUAL ENTRY	OF	F	OF	F	OFI	=	OFF				
SIMULTANEOUS GAP	40	1	10	1	01		01	1			

* These values may be field adjusted. Do not adjust in Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than

<u>PROPOSED</u>		EXISTING
\bigcirc	Traffic Signal Head	
O	Modified Signal Head	N/A
\rightarrow	Sign	\dashv
\downarrow	Pedestrian Signal Head	•
	Signal Pole with Guy Signal Pole with Sidewalk Guy	
	Inductive Loop Detector	
	Controller & Cabinet	K_N K_N
	Junction Box	
	2-in Underground Conduit	
N/A	Right of Way	
\longrightarrow	Directional Arrow	\longrightarrow
N/A	Curb Ramp	
N/A	Guardrail	1 1
•	Construction Zone Drums	•
	Construction Zone	·
∞	Out of Pavement Detector	•
	Video Detection Area	
⟨∆⟩ R	ight Arrow "ONLY" Sign (R3-5R	() (A)
B	No Right Turn Sign (R3-1)	B
© No	U-Turn/No Left Turn Sign (R3-	_

LEGEND

Signal Upgrade - Temporary Design 1 (TMP Phase I)



This plan supersedes the plan

signed and sealed on 7/24/19.

SR 3015 (Airport Boulevard) I-40 EB Ramps

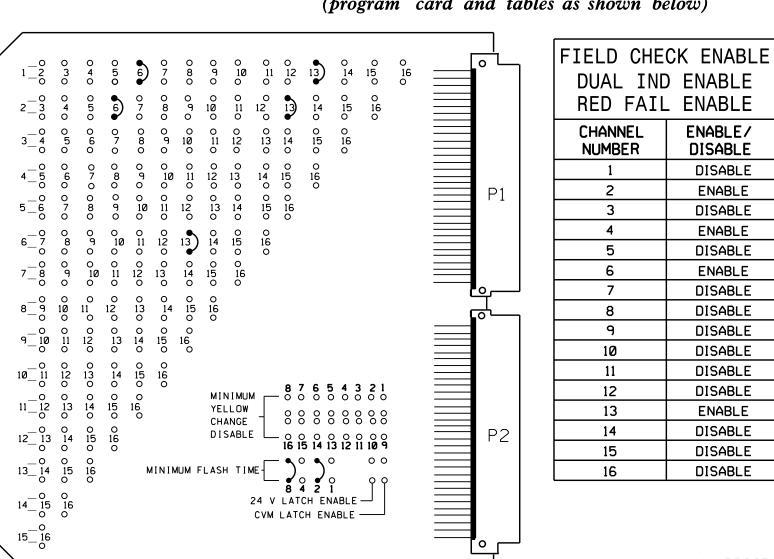
Morrisville Division 5 Wake County PLAN DATE: September 2019 REVIEWED BY:

750 N.Greenfield Pkwy.Garner.NC 27529 PREPARED BY: J.A. Lohr REVIEWED BY:

026486 SIG. INVENTORY NO. 05-0947T

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

(program card and tables as shown below)



MMU PROGRAMMING CARD

UNIT OPTIONS									
OPTION	SETTING								
RECURRENT PULSE	ON								
WALK DISABLE	OFF								
LOG CVM FAULTS	ON								
EXTERN WATCHDOG	OFF								
24V-2=12VDC	OFF								
PGM CARD MEMORY	ON								
LEDguard	ON								
FORCE TYPE 16	OFF								
TYPE12-SDLC	OFF								
VM 3×/Day Latch	ON								

FLASHING YE	ELLOW ARROW						
CONFIG MODE	В						
ENABLE CHANN	NEL PAIR, FYA						
CH 1-13	ON						
CH 3-14	OFF						
CH 5-15	OFF						
CH 7-16	OFF						
RED/YEL INF	PUT ENABLE						
CH 1	ON						
CH 3	OFF						
CH 5	OFF						
CH 7	OFF						
ASH RATE FAULT	ON						
YA TRAP DETECT	ON						

MMU PROGRAMMING NOTE

ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

RACK	DILL	SLOF	SLOT	S L O T	S L O T	SLOT	S L O T	S L O T	SLOT	S L O T	SLOT	S L O T
#1	BIU	$\mathbb{H} \Sigma \cap \vdash \succ$	$\mathbb{E} \mathbb{M} \mathbb{P} \vdash Y$	ЕМРТҮ	E M P T Y	$\mathbb{E} \Delta P \vdash \lambda$	ЕМРТҮ	ЕМРТҮ	$\mathbb{H} \Sigma \cap \vdash \succ$	E M P T Y	ЕМРТҮ	E M P T Y

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN

	HART BELOW
LOOP NO.	LOOP PANEL TERMINALS
NU	L1A,L1B
NU	L2A,L2B
NU	L3A,L3B
NU	L4A,L4B
NU	L5A,L5B
NU	L6A,L6B
NU	L7A,L7B
NU	L8A,L8B
NU	L9A,L9B
NU	L10A,L10B
NU	L11A,L11B
NU	L12A,L12B
NU	L13A,L13B
NU	L14A,L14B
NU	L15A,L15B
NU	L16A,L16B

PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

3		_						
CONTROLLER	FUNCTION	TIMING						
DETECTOR NO.	FUNCTION	FEATURE	TIME(SEC)					
1								
2								
3								
4								
5								
6								
7								
8								
9								
10								
1 1								
12								
13								
14								
15								
16								

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 3,5,7,8,9,10,11,12,14,15, and 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (LS AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phase 2 Green and 6 Green.
- 4. Set power-up flash time to 10 seconds and implement on the Malfunction Management Unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6 for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO.	SHEET N
I - 5700	Sig 13

					SIG	NAL	. HI	EAD	НО	OK-	UP	CHA	4RT					
PHASE	1	2	3		4		5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	11	21,22	NU	41	42,43	44	NU	61,62	NU	NU	NU	NU	NU	NU	11*	NU	NU	NU
RED	*	2R			4R	4R		6R										
YELLOW	*	2Y				4 Y		6Y										
GREEN		2G				4G												
RED ARROW				4R											13R			
YELLOW ARROW				4Y	4Y										13Y			
FLASHING YELLOW ARROW															13G			
GREEN ARROW	1G			4G	4G			6G										
*																		
Ķ																		

NU = Not Used

- * Denotes install load resistor. See Load Resistor Installation Detail on sheet 2.
- ★ See pictorial of head wiring detail this sheet.

EQUIPMENT INFORMATION

CONTROLLER......2070EN2 SOFTWAREECONOLITE ASC/3-2070 CABINET MOUNT.....BASE

LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....1,2,4,6,13

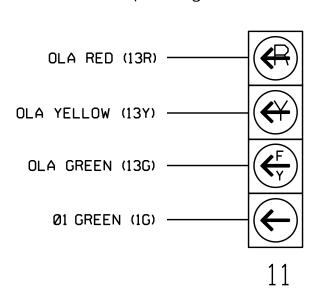
OL A * OLB.....NOT USED OLC.....NOT USED

OLD.....NOT USED

* SEE OVERLAP PROGRAMMING DETAIL ON SHEET 2

FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



LOAD SWITCH ASSIGNMENT DETAIL

(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION
1	ø 1
2	ø 2
3	ø 3
4	ø 4
5	ø 5
6	ø 6
7	ø 7
8	ø 8
9	ø2 PED
10	Ø4 PED
11	ø6 PED
12	Ø8 PED
13	OLA
14	OLB
15	OLC
16	OLD

THIS ELECTRICAL DETAIL SUPERSEDES THE DETAIL SEALED ON 8/1/2019.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-0947T1 DESIGNED: September 2019 SEALED: 10/2/2019 REVISED: N/A

Electrical Detail - Temp Design 1 (TMP Phase I)

Sheet 1 of 2 ELECTRICAL AND PROGRAMMING DETAILS FOR Prepared in the Offices of:

SR 3015 (Airport Boulevard) I-40 EB Ramps

ivision 5 Morrisville PLAN DATE: October 2019 REVIEWED BY: PREPARED BY: S. Armstrong Reviewed BY: REVISIONS INIT. DATE

036833

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SIG. INVENTORY NO. 05-0947T1

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

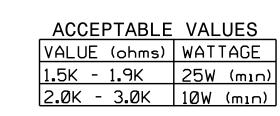
Select TMG VEH OVLP [A] and 'PPLT FYA'

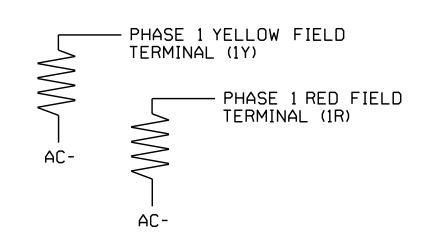
TMG VEH OVLP[A] TYPE:	····PPLT FYA
PROTECTED LEFT TURN	PHASE 1
OPPOSING THROUGH	PHASE 2
FLASHING ARROW OUTPUT	.CH13 ISOLATE
DELAY START OF: FYAO.O ACTION PLAN SF BIT DISABL	

END PROGRAMMING

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)





ECONOLITE ASC/3-2070 SPECIAL MMU PROGRAMMING

(program controller as shown)

1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 4. PORT 1 (SDLC)

3. From PORT 1 (SDLC) Submenu select 2. MMU PROGRAM

CAUTION.

Set intersection to Flash before attempting to enter or change any MMU programming data.

This programming and that of the MMU programming card must match exactly. If they do not, the intersection will be placed into Flash.

MMU	PROGRA	М	[MANUAL]											
	СН	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2
	1	•	•	•	Χ	•	•	•	•	•	•	Χ	•	•	•	•
	2	•	•	•	Χ	•	•	•	•	•	•	Χ	•	•	•	
	3	•	•	•	•	•	•	•	•	•	•	•	•	•		
	4	•	•	•	•	•	•	•	•	•	•	•	•			
	5	•	•	•	•	•	•	•	•	•	•	•				
	6	•	•	•	Χ	•	•	•	•	•	•					
	7	•	•	•	•	•	•	•	•	•						
	8	•	•	•	•	•	•	•	•							
	9	•	•	•	•	•	•	•								
	10	•	•	•	•	•	•									
	11	•	•	•	•	•										
	12	•	•	•	•											
	13	•	•	•												
	14	•	•													
	15	•														

END PROGRAMMING

THIS ELECTRICAL DETAIL SUPERSEDES THE DETAIL SEALED ON 8/1/2019.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-0947T1 DESIGNED: September 2019 SEALED: 10/2/2019 REVISED: N/A

Electrical Detail - Temp Design 1 (TMP Phase I)
Sheet 2 of 2

Prepared in the Offices of:

Prepared in the Offices of:

Nobility and Company of the Company of

SR 3015 (Airport Boulevard) at I-40 EB Ramps

Division 5 Wake County Morrisville

PLAN DATE: October 2019 REVIEWED BY:

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by:

Ryan W. Hough 10/8/2019

-- 430320FAA2654C3... DATE

SIG. INVENTORY NO. 05-0947T1

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