ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL
(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: .....PPLT FYA

PROTECTED LEFT TURN....    PHASE   5
OPPOSING THROUGH.......    PHASE   6

FLASHING ARROW OUTPUT.....CH15 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```

END PROGRAMMING

OVERLAP F

Select TMG VEH OVLP [F] and 'NORMAL'

```
TMG VEH OVLP...[F] TYPE: ......NORMAL
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . . . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

OVERLAP E

Select TMG VEH OVLP [E] and 'NORMAL'

```
TMG VEH OVLP...[E] TYPE: ......NORMAL
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . . X . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```
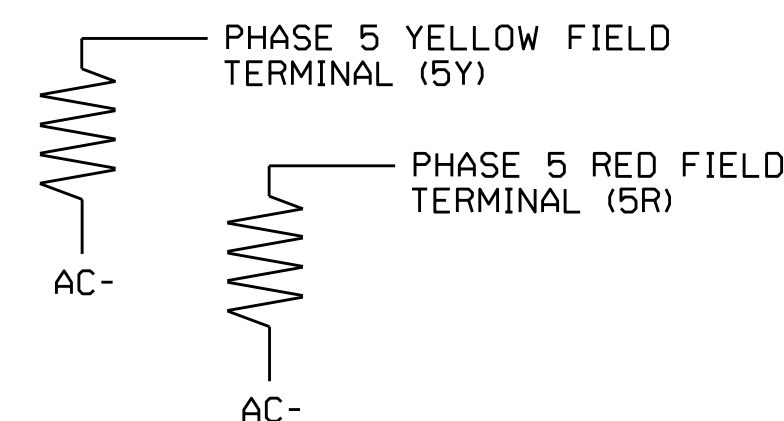
DELETE OVERLAP 'E' AND OVERLAP 'F' PROGRAMMING

LOAD RESISTOR INSTALLATION DETAIL
(install resistors as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

PHASE 5 YELLOW FIELD TERMINAL (5Y)

PHASE 5 RED FIELD TERMINAL (5R)

AC-

AC-

ECONOLITE ASC/3-2070 SPECIAL MMU PROGRAMMING
(program controller as shown)

1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 4. PORT 1 (SDLC)

3. From PORT 1 (SDLC) Submenu select 2. MMU PROGRAM

CAUTION!

Set intersection to Flash before attempting
to enter or change any MMU programming data.

This programming and that of the MMU programming
card must match exactly. If they do not, the
intersection will be placed into Flash.

```
MMU PROGRAM [     MANUAL]

    CH 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2
     1 . . . . . . . . . . . . . . .
     2 . X . . . . . . . X X . .
     3 . . . . . . . . . . . . .
     4 . . . . . . . . . . . .
     5 . X . . . . . . . . . .
     6 . X . . . . . . . . .
     7 . . . . . . . . . .
     8 . . . . . . . . .
     9 . . . . . . . .
    10 . . . . . . .
    11 . . . . . .
    12 . . . . .
    13 . . .
    14 . .
    15 .
```

END PROGRAMMING

THIS ELECTRICAL DETAIL SUPERSEDES
THE DETAIL SEALED ON 8/1/2019.

```
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1168T3
DESIGNED: September 2019
SEALED: 10/2/2019
REVISED: N/A
```

Electrical Detail - Temp Design 3 (TMP Phase III, Step B)
Sheet 2 of 3

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

SEAL
NORTH CAROLINA
PROFESSIONAL
SEAL
036833
ENGINEER
RYAN W. HOUGH

DocuSigned by:
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10/8/2019
DATE

03-OCT-2019 07:44
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sarmstrong