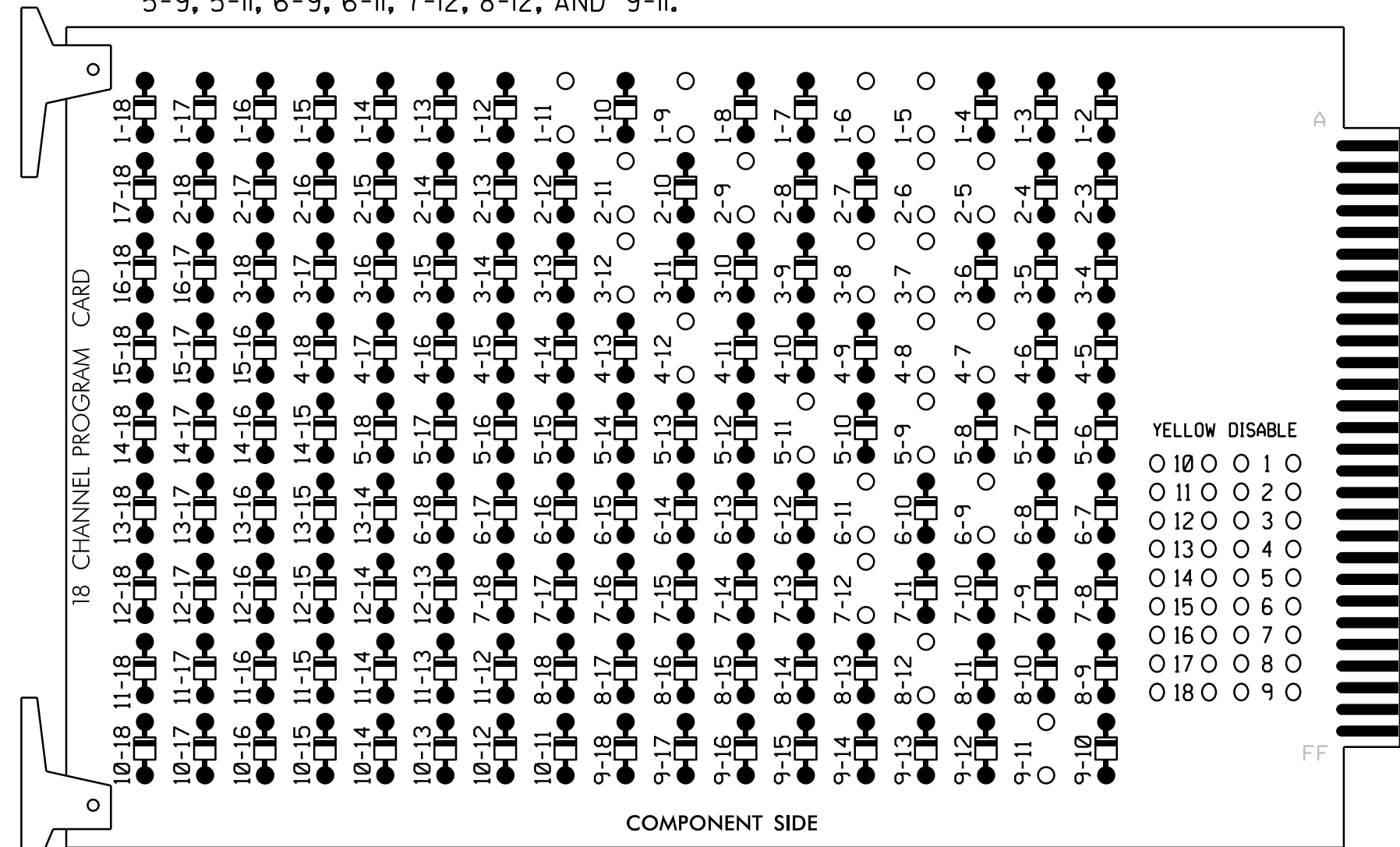


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

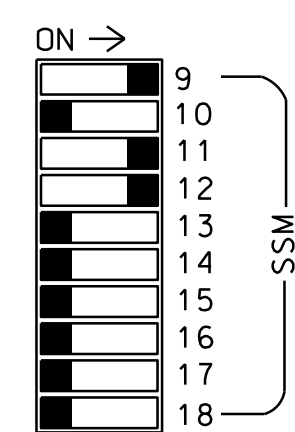
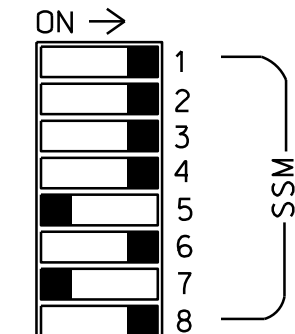
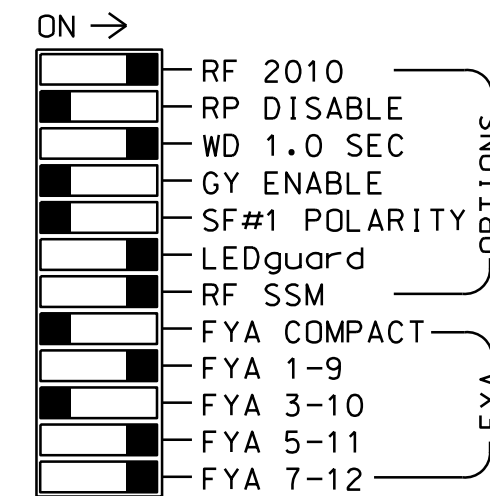
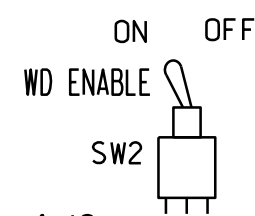
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-12, 4-7, 4-8, 4-12, 5-9, 5-11, 6-9, 6-11, 7-12, 8-12, AND 9-11.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phase 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash and overlap 1 as Wag Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,
 AUX S1,AUX S4,AUX S5
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....1+2
 OVERLAP "B".....NONE
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	
SIGNAL HEAD NO.	11	82	21,22	NU	31,32	41,42	NU	51	61,62	NU	71	81,82	NU	11	NU	NU	51	71	NU
RED	*	128			101			134				107							
YELLOW		129			102		*	135		*	108								
GREEN		130			103			136			109								
RED ARROW				116									A121				A114	A101	
YELLOW ARROW		126			117								A122				A115	A102	
FLASHING YELLOW ARROW													A123				A116	A103	
GREEN ARROW	127	127			118			133			124								

NU = Not Used

* See pictorial of head wiring in detail this sheet.
 * Denotes install load resistor. See load resistor installation detail this sheet.

INPUT FILE POSITION LAYOUT

(from view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2	∅ 3	∅ 3	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4	∅ 4
L	1A	1B	2A	3A	3B	4A	4B	4C	4D	4E	4F	4G	4H	4I
U	NOT USED	NOT USED	∅ 2	NOT USED	NOT USED	∅ 4	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED
L	5A	6A	7A	8A	9A	10A	11A	12A	13A	14A	15A	16A	17A	18A
U	NOT USED	∅ 6	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED
L	6B	7B	8B	9B	10B	11B	12B	13B	14B	15B	16B	17B	18B	19B

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

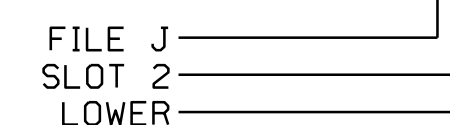
⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
2B	TB2-11,12	I3L	76	38	42	2	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			
3B	TB4-9,10	I6U	41	3	4	3	Y	Y			
4A	TB6-1,2	I7U	65	27	34	4	Y	Y			
4B	TB6-3,4	I7L	78	40	44	4	Y	Y			10
5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
7A ³	TB5-5,6	J5U	57	19	7	7	Y	Y			15
	-	I8U	49	11	24	4	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.
- Add jumper from J5-W to I8-W, on rear of input file.

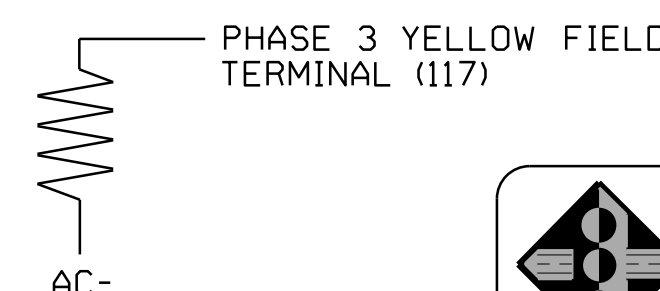
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR REMOVAL DETAIL

(remove resistor as shown below)

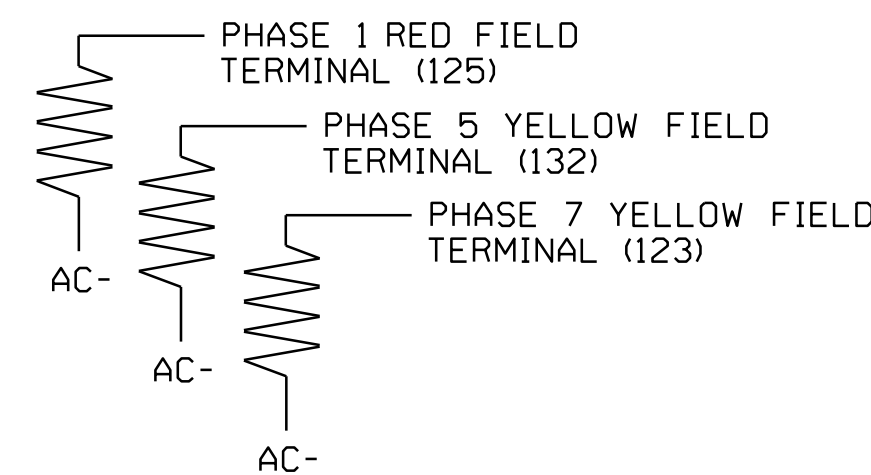
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



LOAD RESISTOR INSTALLATION DETAIL

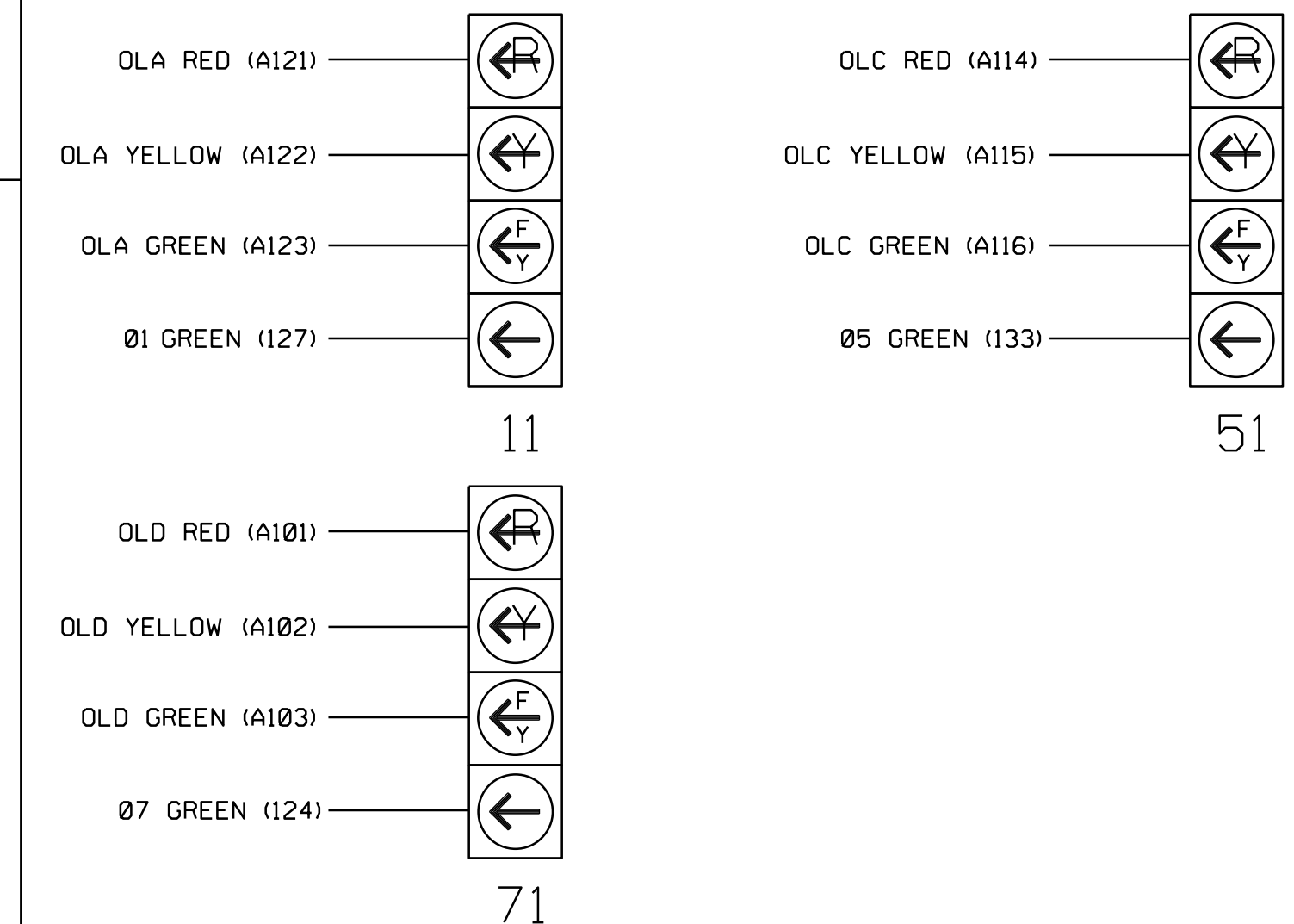
(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal heads 11, 51, and 71 requires special programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 14-0195
 DESIGNED: Sep 2019
 SEALED: 9/9/2019
 REVISED: N/A

Electrical Detail - Sheet 1 of 2
 Final Design

ELECTRICAL AND PROGRAMMING DETAILS FOR: 	US 64 at NC 69		SEAL NORTH CAROLINA PROFESSIONAL ENGINEER NICHOLAS E. BURNS SEAL 046300 9/9/2019
	Division 14 PLAN DATE: September 2019 PREPARED BY: TS Popelka	Clay County REVIEWED BY: NE Burns RKA PROJ. NO: 15226 (040)	

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 14-0195