#### PROJECT REFERENCE NO. SHEET NO A-0011C Sig-4.2

GREEN

FLASH

# TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

#### (program controller as shown below)

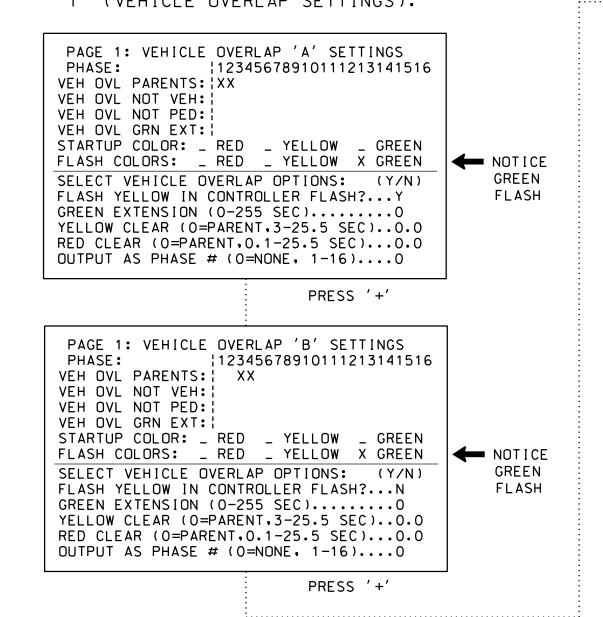
- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11 AND 12.

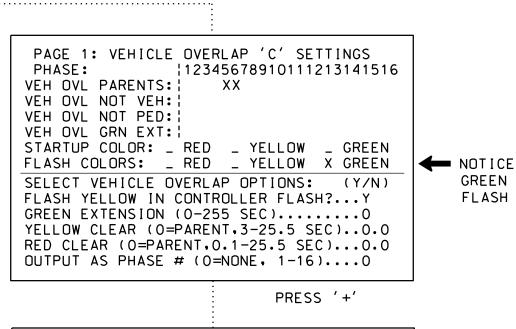


### OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).





PAGE 1: VEHICLE OVERLAP 'D' SETTINGS

VEH OVL PARENTS: |

VEH OVL NOT VEH:

VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN **←** NOTICE GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

12345678910111213141516

XX

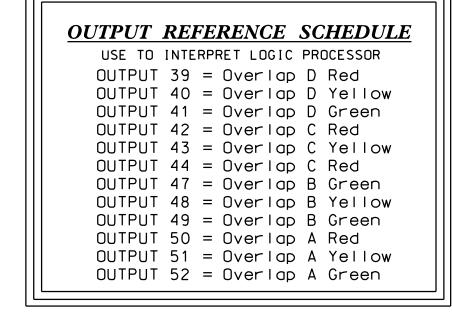
OVERLAP PROGRAMMING COMPLETE

## FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROUACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

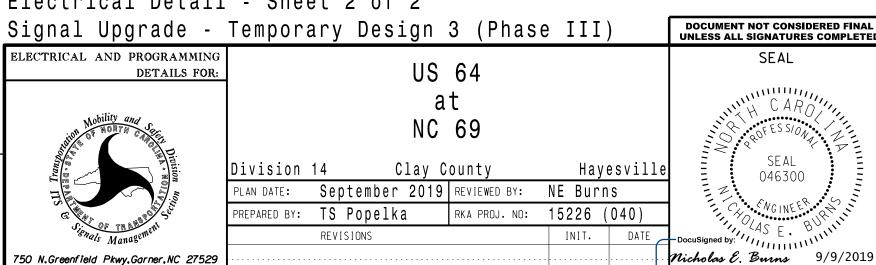
THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 14-0195T3 DESIGNED: Sep 2019 SEALED: 9/9/2019 REVISED: N/A

SIG. INVENTORY NO. 14-0195T3

Electrical Detail - Sheet 2 of 2



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