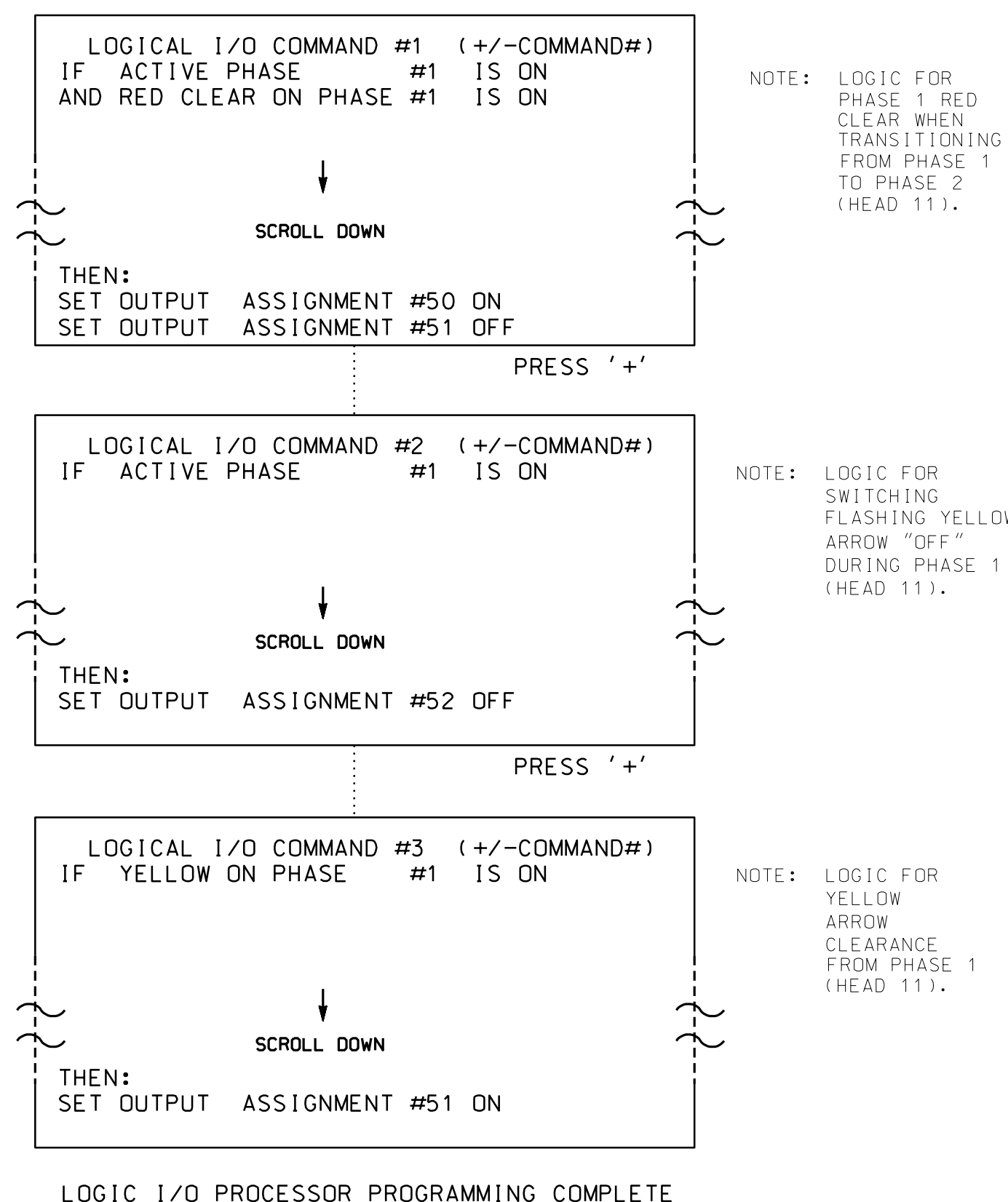


### LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



**OUTPUT REFERENCE SCHEDULE**

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 50 = Overlap A Red  
 OUTPUT 51 = Overlap A Yellow  
 OUTPUT 52 = Overlap A Green

### OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0.0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE


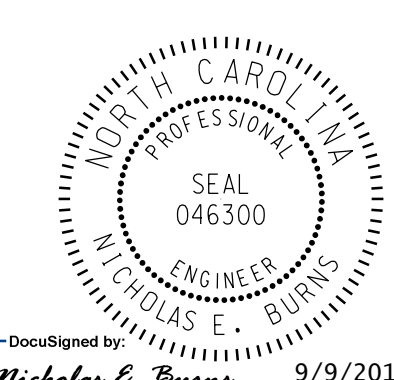
THIS ELECTRICAL DETAIL IS FOR  
 THE SIGNAL DESIGN: 14-0195T1  
 DESIGNED: Sep 2019  
 SEALED: 9/9/2019  
 REVISED: N/A

Electrical Detail - Sheet 2 of 2  
 Signal Upgrade - Temporary Design 1 (Phase I)

Prepared in the offices of:



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ELECTRICAL AND PROGRAMMING DETAILS FOR:  750 N. Greenfield Pkwy, Corner, NC 27529	US 64 at NC 69 Division 14 Clay County Haynesville	SEAL 
PLAN DATE: September 2019 REVIEWED BY: NE Burns PREPARED BY: TS Popelka RKA PROJ. NO: 15226 (040)		
REVISIONS	INIT.	DATE
Documented by: <u>Nicholas E. Burns</u> 9/9/2019 DATE		
SIG. INVENTORY NO. 14-0195T1		

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED