PROJECT REFERENCE NO. SHEET NO. U-5818 Sig 4.2

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

#### PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: | 12345678910111213141516

VEH OVL PARENTS: | XX

VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN
FLASH COLORS: \_ RED \_ YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)......0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0611T3

DESIGNED: FEBRUARY 2019

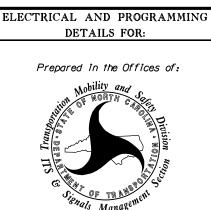
SEALED: 02/05/2019

REVISED: N/A

# Temporary Design 3 - TMP Phase III Electrical Detail - Sheet 2 of 2



Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672



750 N.Greenfield Pkwy, Garner, NC 27529

### SR 1001 (Sugar Hill Road) at I-40 Eastbound Ramps

Division 13 McDowell County Marion
PLAN DATE: January 2019 REVIEWED BY: E D Harris
PREPARED BY: G B Spell REVIEWED BY: R M Muncey
REVISIONS INIT. DATE

SEAL
43239

NO INE ENDOUSIGNED BY:

RAILA M. MUMALIA

DOCUSIGNED BY:

RAILA M. MUMALIA

OTREE SIGNATURE

DATE

SIG. INVENTORY NO. 13-0611T

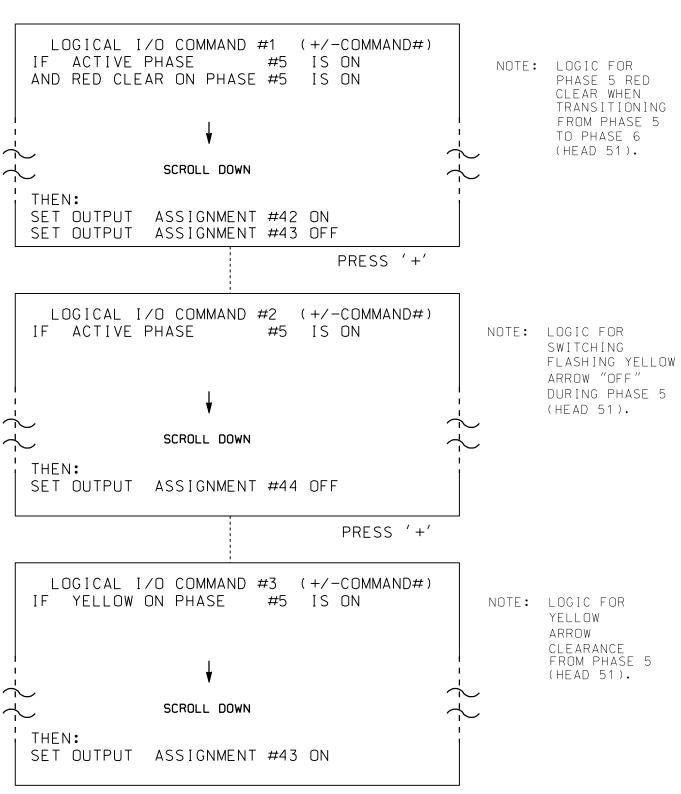
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

# OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green

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