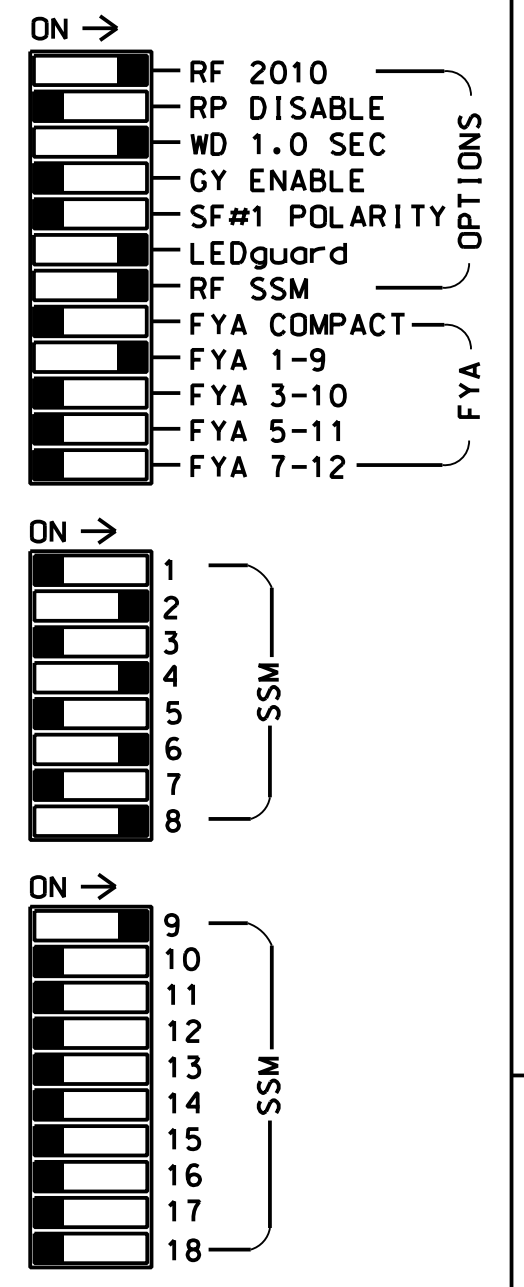
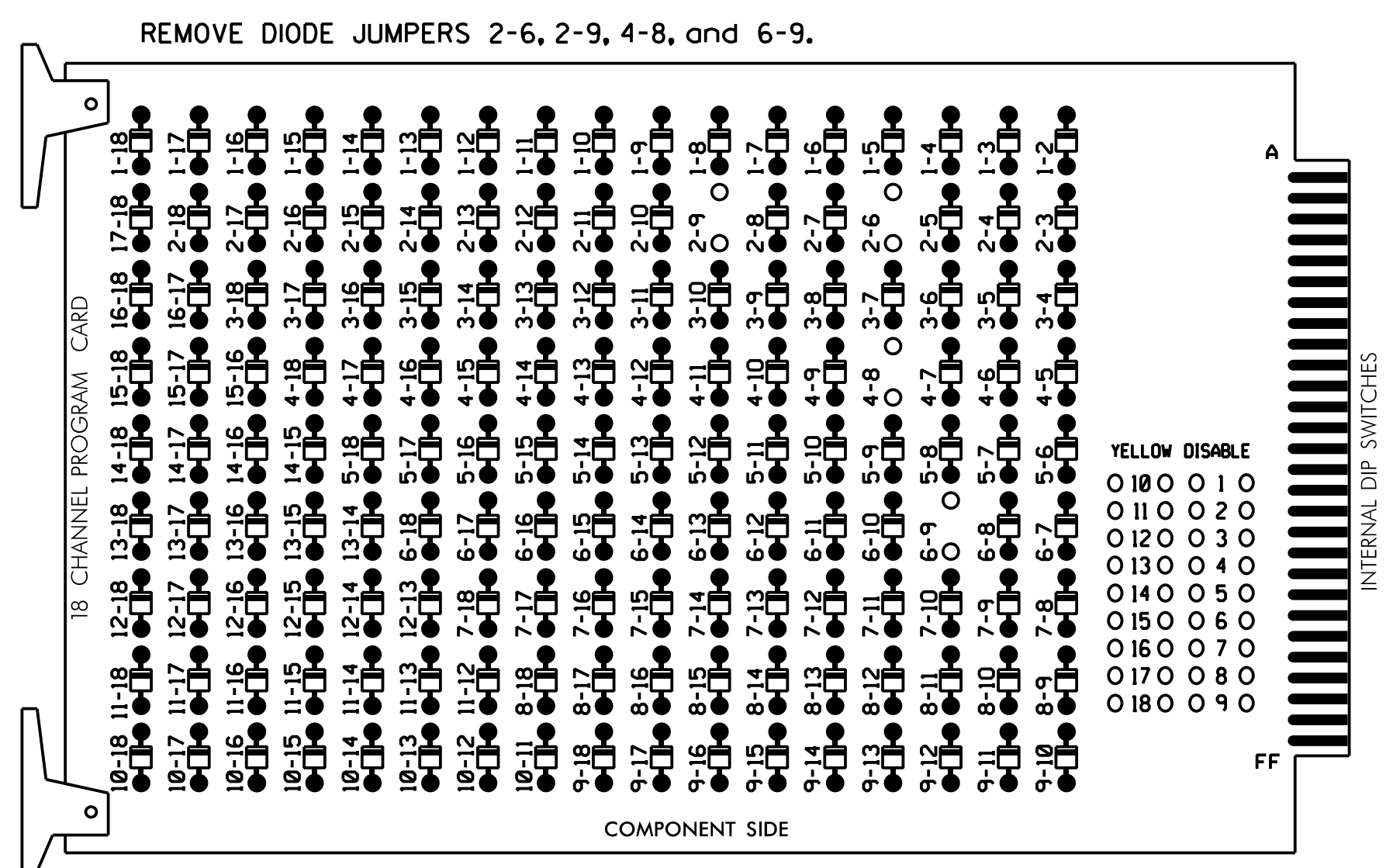


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 4 and 8 for Dual Entry.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Havelock US 70 Business Closed Loop Signal System.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22,23,24	NU	NU	41,42	NU	NU	62,63,65,66	NU	NU	81,82	NU	61,64	NU	NU	NU	NU	NU
RED		128			101			134			107							
YELLOW		129			102			135			108							
GREEN		130			103			136			109							
RED ARROW													A121					
YELLOW ARROW														A122				
FLASHING YELLOW ARROW															A123			
GREEN ARROW																		

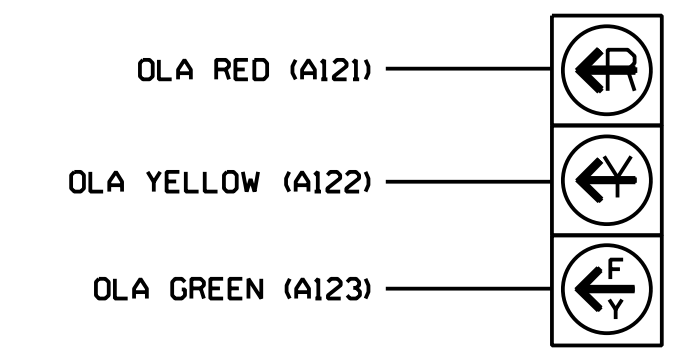
NU = Not Used
★ See pictorial of head wiring in detail this sheet.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
CABINET.....332 W/AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S5,S8,S11,AUX S1
PHASES USED.....2,4,6,8
OVERLAP "A".....2
OVERLAP "B".....NOT USED
OVERLAP "C".....NOT USED
OVERLAP "D".....NOT USED

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



61,64

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	S	∅ 2	∅ 2	S	S	∅ 4	S	S	SYS	S	S	S	S	FS
L	2A	2C	2B	2D	4A	4B	S28	S29	DC ISOLATOR	ST	DC ISOLATOR			
U	∅ 6	∅ 6	∅ 6	∅ 6	S	∅ 8	S	S	S	S	S	S	S	S
L	6A	6B	6D	6F	8A	8B								
U	NOT USED	∅ 6	∅ 6	NOT USED										
L	6C	6E												

EX. : 1A, 2A, ETC. = LOOP NO.'S

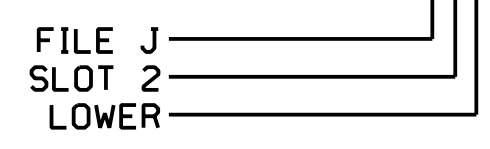
FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
2C	TB2-9,10	I3U	63	25	32	2	Y	Y			
2D	TB2-11,12	I3L	76	38	42	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			
*S28	TB6-9,10	I9U	60	22	11	SYS					
*S29	TB6-11,12	I9L	62	24	13	SYS					
6A	TB3-1,2	J1U	55	17	5	6	Y	Y			
6B	TB3-5,6	J2U	40	2	6	6	Y	Y			
6C	TB3-7,8	J2L	44	6	16	6	Y	Y			
6D	TB3-9,10	J3U	64	26	36	6	Y	Y			
6E	TB3-11,12	J3L	77	39	46	6	Y	Y			
6F	TB5-1,2	J4U	48	10	26	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			15

* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 02-0189
DESIGNED: March 2018
SEALED: 12-7-18
REVISED: N/A

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: :12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

Electrical Detail Signal Upgrade

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared for: **US 70 Business (E Main Street) at SR 1737 (Roosevelt Boulevard)/Woodhaven Drive**

Division 02 Craven Co. Havelock

PLAN DATE: March 2018 REVIEWED BY: A.D. Klinksiek

PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons

REVISIONS: _____ INITI. _____ DATE _____

DocuSigned by: **Matasha R. Simmons** 12/7/2018

750 N. Greenfield Pkwy, Corner, NC 27529

SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 031464 MATASHA R. SIMMONS

SIG. INVENTORY NO. 02-0189