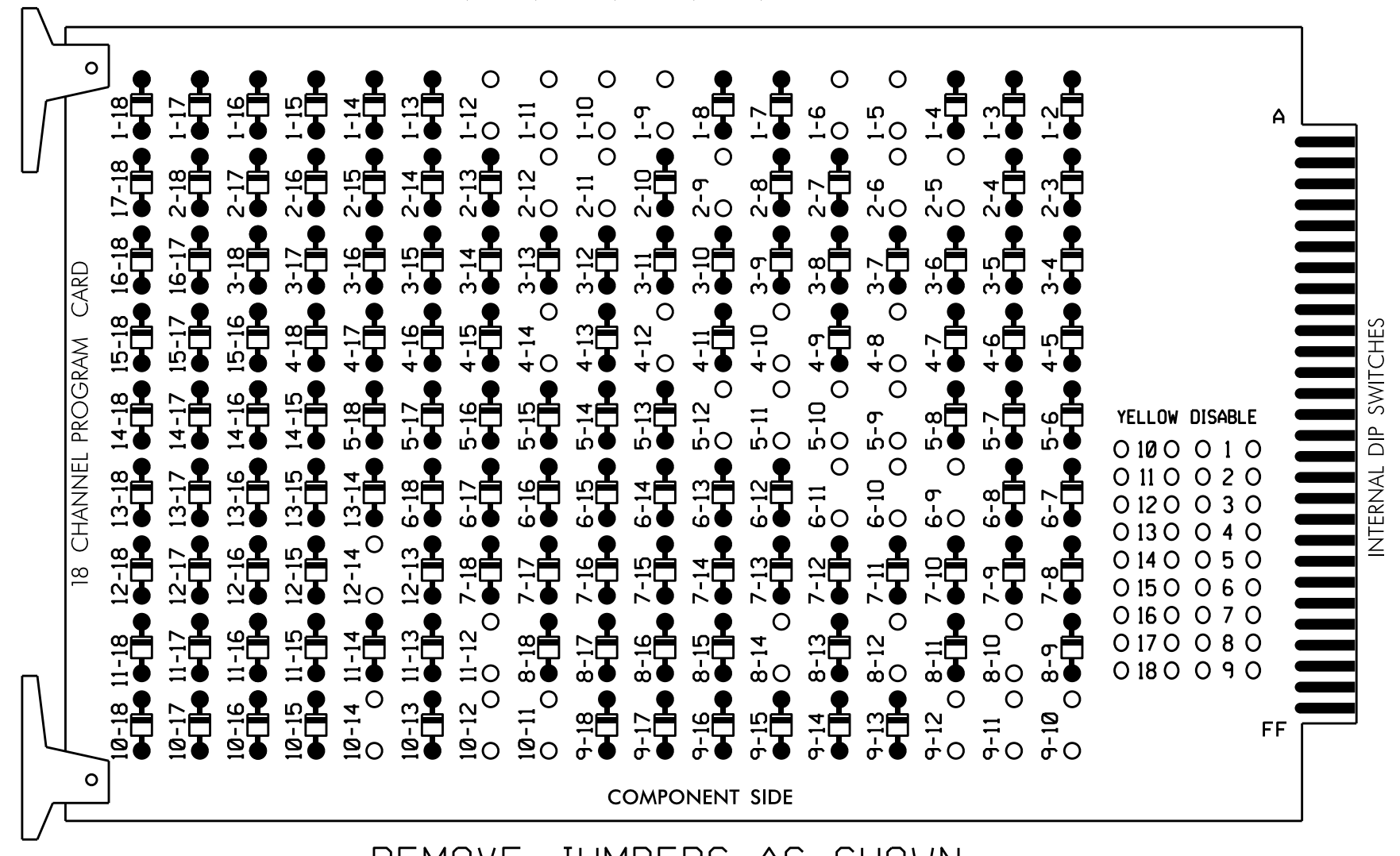


EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-10, 1-11, 1-12, 2-5, 2-6, 2-9, 2-11, 2-12, 4-8, 4-10, 4-12, 4-14, 5-9, 5-10, 5-11, 5-12, 6-9, 6-10, 6-11, 8-10, 8-12, 8-14, 9-10, 9-11, 9-12, 10-11, 10-12, 10-14, 11-12, and 12-14.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program Phase 4 for Startup Ped Call.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the Wilmington Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S5,S6,S7,S8,S11,AUX S1,AUX S2
 AUX S4,AUX S5
 PHASES USED.....1,2,4,4 PED,5,6,8
 OVERLAP "A".....1+2
 OVERLAP "B".....1+8
 OVERLAP "C".....5+6
 OVERLAP "D".....4+5

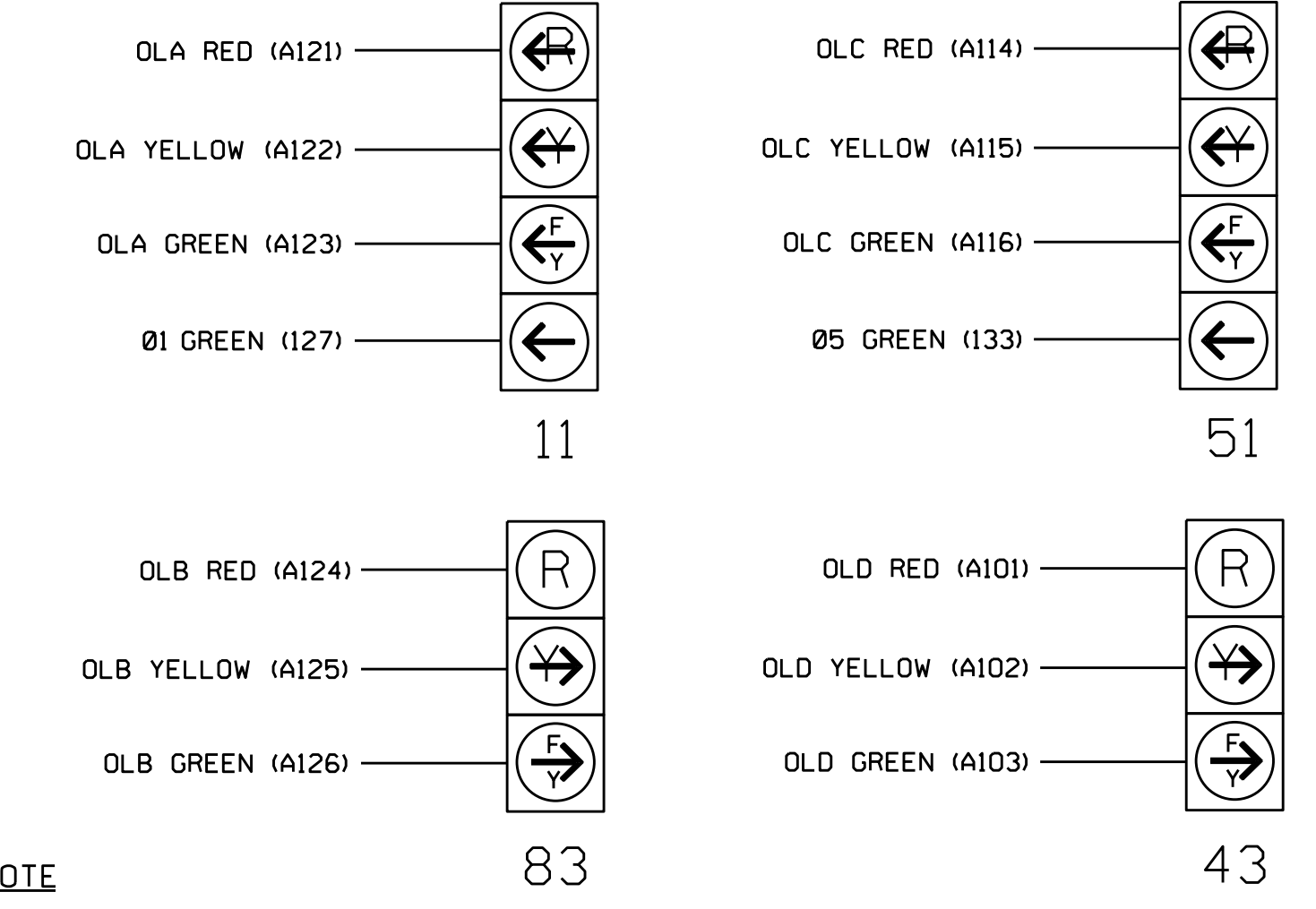
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22	NU	NU	41,42	P41, P42	51	61,62	NU	NU	81,82	NU	11	83	NU	51	43	NU	
RED		128			101			134			107			A124				A101	
YELLOW	*	129			102		*	135			108								
GREEN		130			103			136			109								
RED ARROW														A121				A114	
YELLOW ARROW														A122	A125			A115	A102
FLASHING YELLOW ARROW														A123	A126			A116	A103
GREEN ARROW	127							133											
Hand								104											
Person								106											

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-1027
 DESIGNED: February 2018
 SEALED: 8-1-18
 REVISED: 8/26/2020

INPUT FILE CONNECTION & PROGRAMMING CHART

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2	∅ 4	∅ 5	∅ 6	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	∅ 15
L	1A	1B	2A	4A	5A	6A	8A	9A	10A	11A	12A	13A	14A	15A
U	NOT USED	NOT USED	∅ 2	NOT USED	∅ 5	∅ 6	NOT USED	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	∅ 15
L	NOT USED	NOT USED	2B	NOT USED	5B	6B	NOT USED	9B	10B	11B	12B	13B	14B	15B

EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 ⊗ Wired Input - Do not populate slot with detector card

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			10
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			3
	1B	TB2-5,6	I2U	39	1	2	1	Y	Y		15
2A	TB2-7,8	I3U	63	25	32	2	Y	Y			
	2B	TB2-9,10	I3L	76	38	42	2	Y	Y		
	4A	TB4-9,10	I6U	41	3	4	4	Y	Y		
	5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y		
-		I4U	47	9★	22	2	Y	Y	Y		3
-		J1U	55	17★	55	5	Y	Y			3
5B		TB3-5,6	J2U	40	2	6	5	Y	Y		15
6A	TB3-7,8	J3U	64	26	36	6	Y	Y			
	6B	TB3-9,10	J3L	77	39	46	6	Y	Y		
	8A	TB5-9,10	J6U	42	4	8	8	Y	Y		
	PED PUSH BUTTONS										
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOT I12.

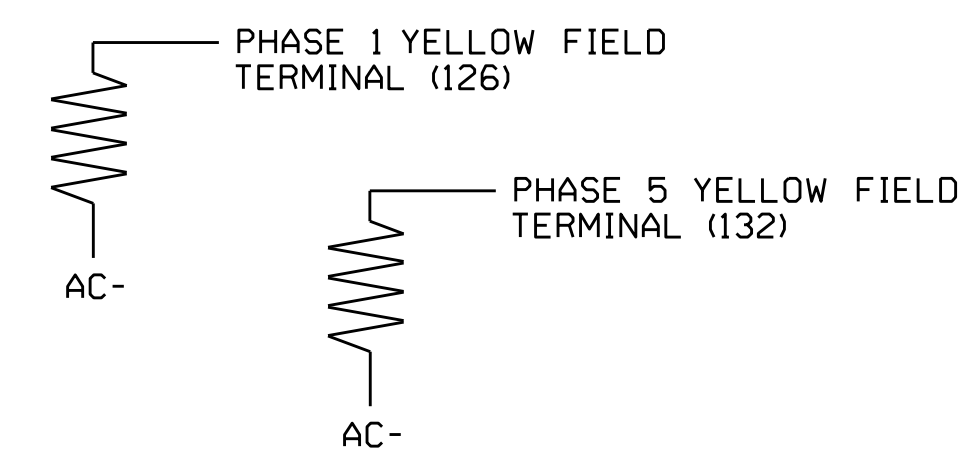
- Add jumper from I1-W to J4-W, on rear of input file.
 - Add jumper from J1-W to I4-W, on rear of input file.
- ★ See Input Page Assignment programming details on sheets 3 and 4.

LOAD RESISTOR INSTALLATION DETAIL

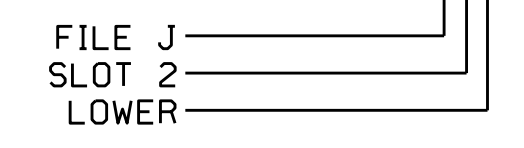
(install resistors as shown)

ACCEPTABLE VALUES

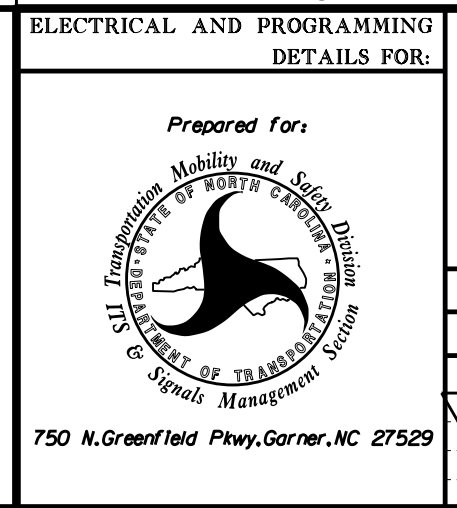
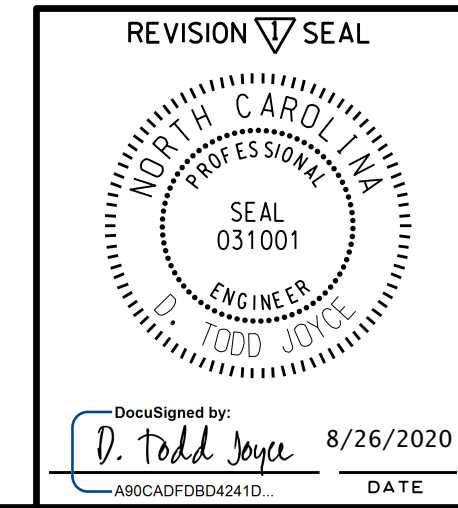
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



INPUT FILE POSITION LEGEND: J2L



Electrical Detail - Sheet 1 of 5
 Signal Upgrade
 Final Design



Prepared for:
 US 17 (Market Street) at
 SR 2734 (Marsh Oaks Drive)/
 SR 2290 (Mendenhall Drive)
 Division 03 New Hanover Co. Wilmington
 PLAN DATE: February 2018 REVIEWED BY: A.D. Klinskies
 PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons
 REVISIONS: [Table with 2 columns: No., Description, Date]
 1. Remove all phases from 'Startup Ped Callis'. CES. [Signature] 8/26/20

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
 Not a certified document. This document originally issued and sealed by Natasha M. Rodevick, PE, #031464 on 08/01/2018. This document shall not be considered a certified document.
 SIGNATURE: [Blank] DATE: [Blank]
 SIG. INVENTORY NO. 03-1027