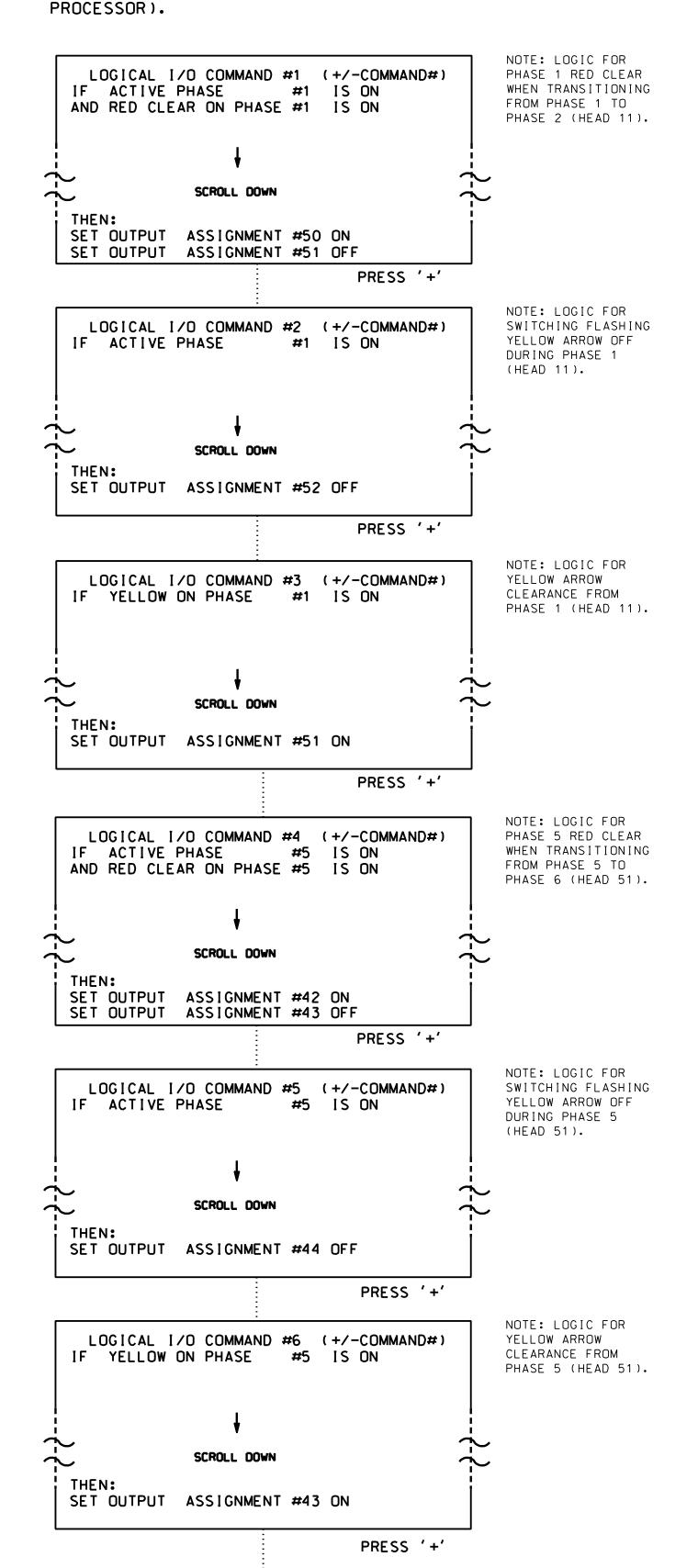
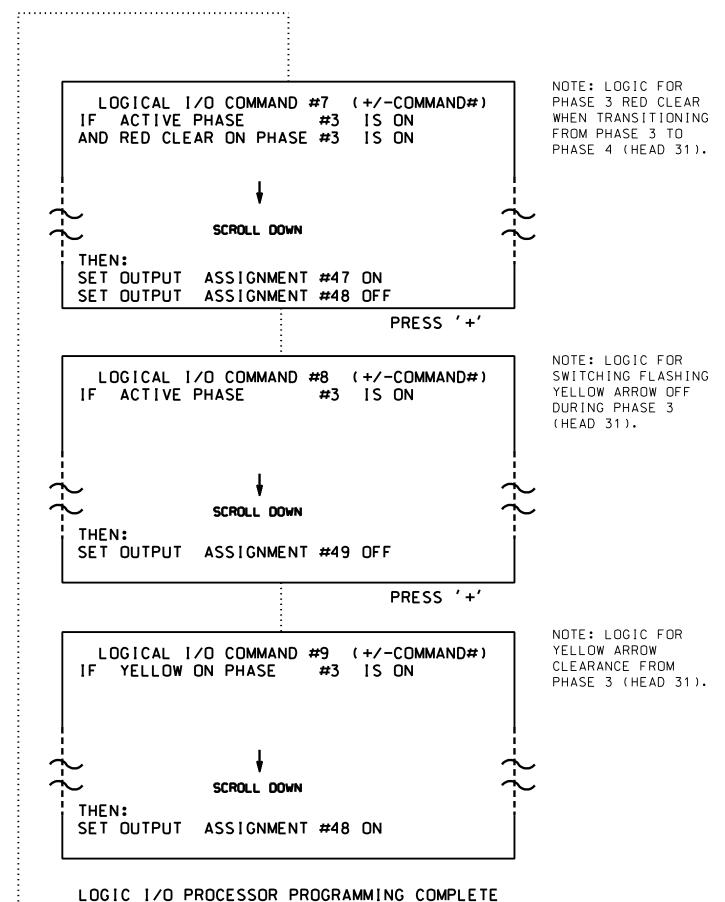
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, and 9.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS). THEN '3' (LOGICAL I/O



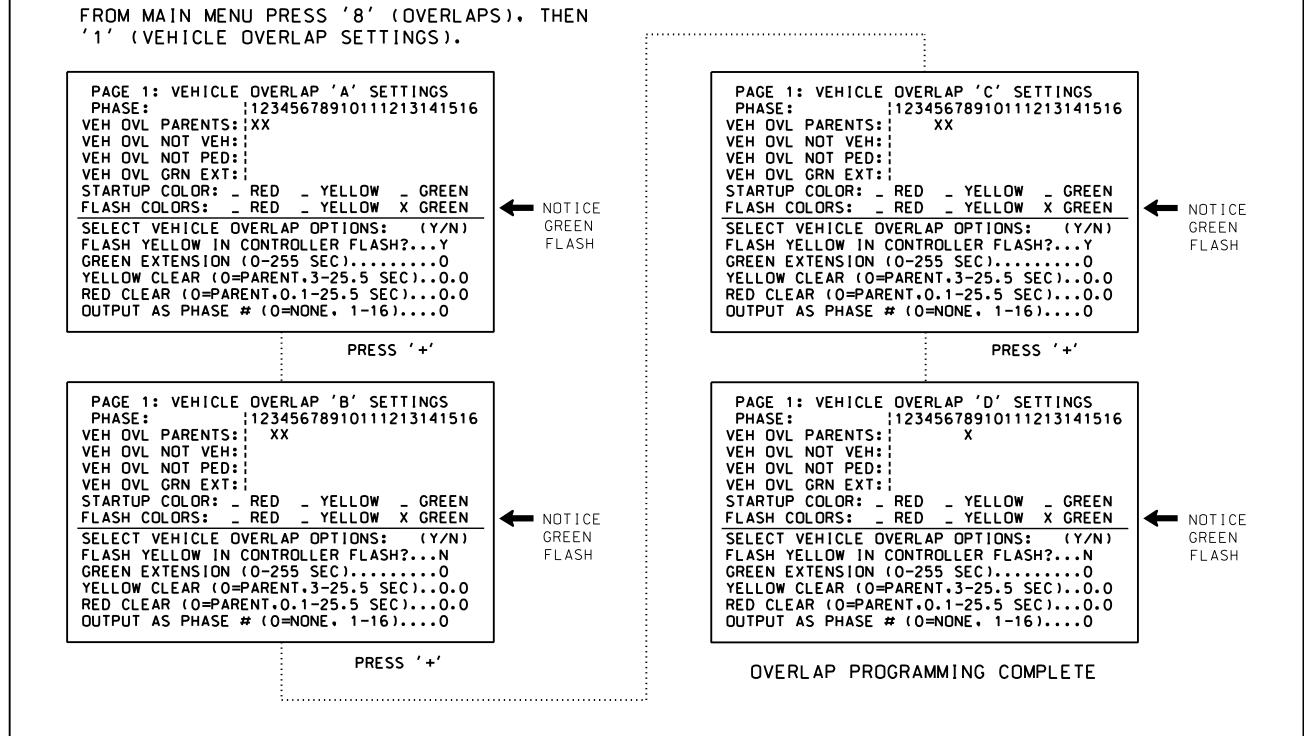


OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 47 = Overlap B Red OUTPUT 48 = Overlop B Yellow OUTPUT 49 = Overlap B Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

FOR DEFAULT PHASING

(program controller as shown below)

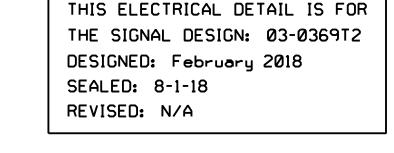


FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.



Electrical Detail - Sheet 2 of 6

Signal Upgrade

Temporary Design 2

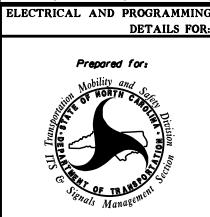
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PROJECT REFERENCE NO.

U-4902D

SHEET NO.

Sig. 5



US 17 (Market Street) at SR 1363 (Bayshore Drive)/ SR 2717 (Torchwood Boulevard)

Division 03 New Hanover Co. February 2018 REVIEWED BY: A.D. Klinksiek PLAN DATE: PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons

REVISIONS

031464

SIG. INVENTORY NO. 03-0369

TH CAROL

OFESSION.

INIT. DATE

HNTB NORTH CAROLINA, P.C.
343 E. Six Forks Road, Suite 200
Raleigh, North Carolina 27609
NC License No: C-1554
(919) 546-8997

750 N.Greenfield Pkwy.Garner.NC 27529