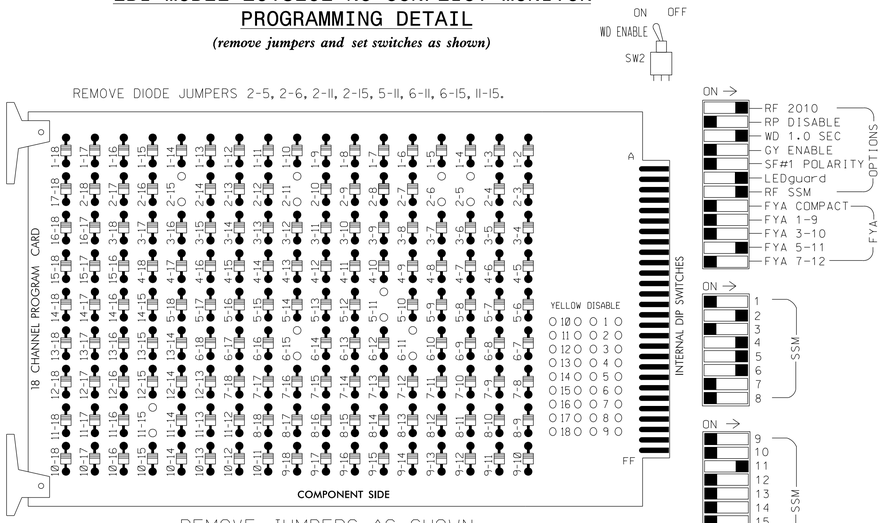
PROJECT REFERENCE NO.	SHEET NO.
U-5833	Sig.2.1

EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phase 6 for "STARTUP PED CALL".
6. Program phases 2 and 6 for Yellow Flash.

EQUIPMENT INFORMATION

CONTROLLER.....2070
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S5,S7,S8,S9,AUX S4
PHASES USED.....2,4,5,6,6PED
OVERLAP "A".....NOT USED
OVERLAP "B".....NOT USED
OVERLAP "C".....5+6
OVERLAP "D".....NOT USED

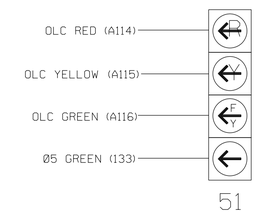
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7		S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5		6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5		6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21.22	NU	NU	41.42	NU	51★	42	61.62	P61, P62	NU	NU	NU	NU	NU	NU	51★	NU	NU
RED		128			101			✳	134										
YELLOW		129			102				135										
GREEN		130			103				136										
RED ARROW																	A114		
YELLOW ARROW								132									A115		
FLASHING YELLOW ARROW																	A116		
GREEN ARROW							133	133											
✋										119									
🚶										121									

NU = Not Used
✳ Denotes install load resistor. See Load Resistor Installation Detail this sheet.
★ See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for this signal requires special logic programming. See sheet 2 of 4 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	SLOT EMPTY	Ø2 2A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	Ø4 4A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø6PED DC ISOLATOR	FS DC ISOLATOR
	L		NOT USED				NOT USED							NOT USED	ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø5 5B	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	Ø6 6A												

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
5A[1]	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9★	22	2	Y	Y	Y		3
	-	J1U	55	17★	55	5	Y	Y			3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			15
6A	TB3-7,8	J2L	44	6	16	6	Y	Y			
PED PUSH BUTTONS											
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					

NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOT I13.

[1] Add jumper from J1-W to I4-W, on rear of input file.
★ See Input Page Assignment programming details on sheet 3.

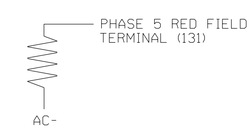
INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



NC Dept of Transportation
Division of Highways
Final Drawing Date: 1/12/2018
DocuSigned by: R. N. Zinser F138897347224 8F...
ITS & Signals Unit

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 13-1241
DESIGNED: Dec. 2017
SEALED: 12/15/2017
REVISED: N/A

vhb
940 Main Campus Drive, Suite 500
Raleigh, NC 27606
NC License No. C-3705
919.829.0328

Electrical Detail-Final Design-Sheet 1 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of: ITS Transportation Mobility and Safety Division, State of North Carolina, Department of Transportation, Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SR 2159 (Piney Ridge Road) at SR 2241 (Oak Street Extension)

Division 13 Rutherford County Forest City

PLAN DATE:	Dec. 2017	REVIEWED BY:	J.L. Lewis
PREPARED BY:	J. Ma	VHB PROJECT NO.:	38536.09

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 033108 ENGINEER JIANXIN MA

Jianxin Ma 2017.12.15 10:14:25 -05'00'
SIGNATURE DATE
SIG. INVENTORY NO. 13-1241