PROJECT REFERENCE NO. OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING LOGICAL I/O PROCESSOR PROGRAMMING DETAIL U-4902D Sig. 12. (program controller as shown below) TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE FROM MAIN MENU PRESS '8' (OVERLAPS). (program controller as shown below) THEN '1' (VEHICLE OVERLAP SETTINGS). 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND PAGE 1: VEHICLE OVERLAP 'A' SETTINGS PAGE 1: VEHICLE OVERLAP 'C' SETTINGS ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, and 6. 12345678910111213141516 112345678910111213141516 VEH OVL PARENTS: :XX VEH OVL PARENTS: | XX VEH OVL NOT VEH: ! VEH OVL NOT VEH: : 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O VEH OVL NOT PED: VEH OVL NOT PED: PROCESSOR). VEH OVL GRN EXT: VEH OVL GRN EXT: : STARTUP COLOR: _ RED _ YELLOW _ GREEN STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE NOTE: LOGIC FOR GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN LOGICAL I/O COMMAND #1 (+/-COMMAND#) PHASE 1 RED CLEAR FLASH YELLOW IN CONTROLLER FLASH?...Y FLASH FLASH YELLOW IN CONTROLLER FLASH?...Y FLASH WHEN TRANSITIONING IF ACTIVE PHASE #1 IS ON GREEN EXTENSION (0-255 SEC)..... GREEN EXTENSION (0-255 SEC)...... FROM PHASE 1 TO AND RED CLEAR ON PHASE #1 IS ON YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 PHASE 2 (HEAD 11). RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 PRESS '+' PRESS '+' SCROLL DOWN PAGE 1: VEHICLE OVERLAP 'D' SETTINGS PAGE 1: VEHICLE OVERLAP 'B' SETTINGS SET OUTPUT ASSIGNMENT #50 ON 112345678910111213141516 112345678910111213141516 SET OUTPUT ASSIGNMENT #51 OFF VEH OVL PARENTS: | XX VEH OVL PARENTS: X VEH OVL NOT VEH: | VEH OVL NOT VEH: PRESS '+' VEH OVL NOT PED: VEH OVL NOT PED: VEH OVL GRN EXT: | VEH OVL GRN EXT: NOTE: LOGIC FOR STARTUP COLOR: _ RED _ YELLOW _ GREEN STARTUP COLOR: _ RED _ YELLOW _ GREEN LOGICAL I/O COMMAND #2 (+/-COMMAND#) SWITCHING FLASHING FLASH COLORS: _ RED _ YELLOW X GREEN FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE **←** NOTICE YELLOW ARROW OFF IF ACTIVE PHASE #1 IS ON DURING PHASE 1 GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) (HEAD 11). FLASH YELLOW IN CONTROLLER FLASH?...N FLASH FLASH YELLOW IN CONTROLLER FLASH?...N FLASH GREEN EXTENSION (0-255 SEC)..... GREEN EXTENSION (0-255 SEC)...... YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 SCROLL DOWN PRESS '+' OVERLAP PROGRAMMING COMPLETE SET OUTPUT ASSIGNMENT #52 OFF PRESS '+' OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING NOTE: LOGIC FOR LOGICAL I/O COMMAND #3 (+/-COMMAND#) YELLOW ARROW (program controller as shown below) CLEARANCE FROM IF YELLOW ON PHASE #1 IS ON PHASE 1 (HEAD 11). FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2. SCROLL DOWN PAGE 2: VEHICLE OVERLAP 'A' SETTINGS NOTICE -NOTICE -PAGE 2: VEHICLE OVERLAP 'C' SETTINGS 112345678910111213141516 112345678910111213141516 PAGE 2 PAGE 2 PHASE: SET OUTPUT ASSIGNMENT #51 ON VEH OVL PARENTS: | X VEH OVL PARENTS: X VEH OVL NOT VEH: VEH OVL NOT VEH: ; VEH OVL NOT PED: ! VEH OVL NOT PED: PRESS '+' VEH OVL GRN EXT: : VEH OVL GRN EXT: : STARTUP COLOR: _ RED _ YELLOW _ GREEN STARTUP COLOR: _ RED _ YELLOW _ GREEN NOTE: LOGIC FOR FLASH COLORS: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW _ GREEN LOGICAL I/O COMMAND #4 (+/-COMMAND#) PHASE 5 RED CLEAR SELECT VEHICLE OVERLAP OPTIONS: (Y/N) SELECT VEHICLE OVERLAP OPTIONS: (Y/N) WHEN TRANSITIONING IF ACTIVE PHASE #5 IS ON FLASH YELLOW IN CONTROLLER FLASH?...Y FLASH YELLOW IN CONTROLLER FLASH?...Y FROM PHASE 5 TO AND RED CLEAR ON PHASE #5 IS ON GREEN EXTENSION (0-255 SEC)..... GREEN EXTENSION (0-255 SEC)..... PHASE 6 (HEAD 51). YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 SCROLL DOWN PRESS '+' PRESS '+' SET OUTPUT ASSIGNMENT #42 ON PAGE 2: VEHICLE OVERLAP 'D' SETTINGS PAGE 2: VEHICLE OVERLAP 'B' SETTINGS NOTICE -NOTICE -SET OUTPUT ASSIGNMENT #43 OFF PAGE 2 12345678910111213141516 112345678910111213141516 PAGE 2 PRESS '+' VEH OVL PARENTS: |X VEH OVL PARENTS: | XX VEH OVL NOT VEH: | VEH OVL NOT VEH: ; VEH OVL NOT PED: VEH OVL NOT PED: NOTE: LOGIC FOR VEH OVL GRN EXT: | VEH OVL GRN EXT: | LOGICAL I/O COMMAND #5 (+/-COMMAND#) SWITCHING FLASHING STARTUP COLOR: _ RED _ YELLOW _ GREEN STARTUP COLOR: _ RED _ YELLOW _ GREEN YELLOW ARROW OFF IF ACTIVE PHASE #5 IS ON FLASH COLORS: _ RED _ YELLOW X GREEN FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE **←** NOTICE DURING PHASE 5 (HEAD 51). SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN FLASH YELLOW IN CONTROLLER FLASH?...N FLASH FLASH YELLOW IN CONTROLLER FLASH?...N FLASH GREEN EXTENSION (0-255 SEC)..... GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 SCROLL DOWN OUTPUT AS PHASE # (0=NONE, 1-16)....0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 SET OUTPUT ASSIGNMENT #44 OFF PRESS '+' OVERLAP PROGRAMMING COMPLETE PRESS '+' Electrical Detail - Sheet 2 of 5 NOTE: LOGIC FOR LOGICAL I/O COMMAND #6 (+/-COMMAND#) YELLOW ARROW Signal Upgrade CLEARANCE FROM IF YELLOW ON PHASE #5 IS ON **DOCUMENT NOT CONSIDERED FINAL** Temporary Design 3 PHASE 5 (HEAD 51). **UNLESS ALL SIGNATURES COMPLETED** OUTPUT REFERENCE SCHEDULE ELECTRICAL AND PROGRAMMING USE TO INTERPRET LOGIC PROCESSOR US 17 (Market Street) at THIS ELECTRICAL DETAIL IS FOR TH CAROL SR 2734 (Marsh Oaks Drive)/ THE SIGNAL DESIGN: 03-1027T3 OUTPUT 42 = Overlap C Red Prepared for: COFESSION. SCROLL DOWN OUTPUT 43 = Overlap C Yellow SR 2290 (Mendenhall Drive) DESIGNED: February 2018 OUTPUT 44 = Overlap C Green SEALED: 8-1-18 031464 OUTPUT 50 = Overlap A Red SET OUTPUT ASSIGNMENT #43 ON Division 03 New Hanover Co. REVISED: N/A OUTPUT 51 = Overlap A Yellow February 2018 **REVIEWED BY:** A.D. Klinksiek PLAN DATE: OUTPUT 52 = Overlap A Green PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons HNTB NORTH CAROLINA, P.C. 343 E. Six Forks Road, Suite 2 Raleigh, North Carolina 27609 NC License No: C-1554 REVISIONS INIT. DATE LOGIC I/O PROCESSOR PROGRAMMING COMPLETE 343 E. Six Forks Road, Suite 200 Natasha R. Simmons 8/1/2018 EEDASSIGNATURE DATE 750 N.Greenfield Pkwy.Garner.NC 27529 SIG. INVENTORY NO. 03-1027T3