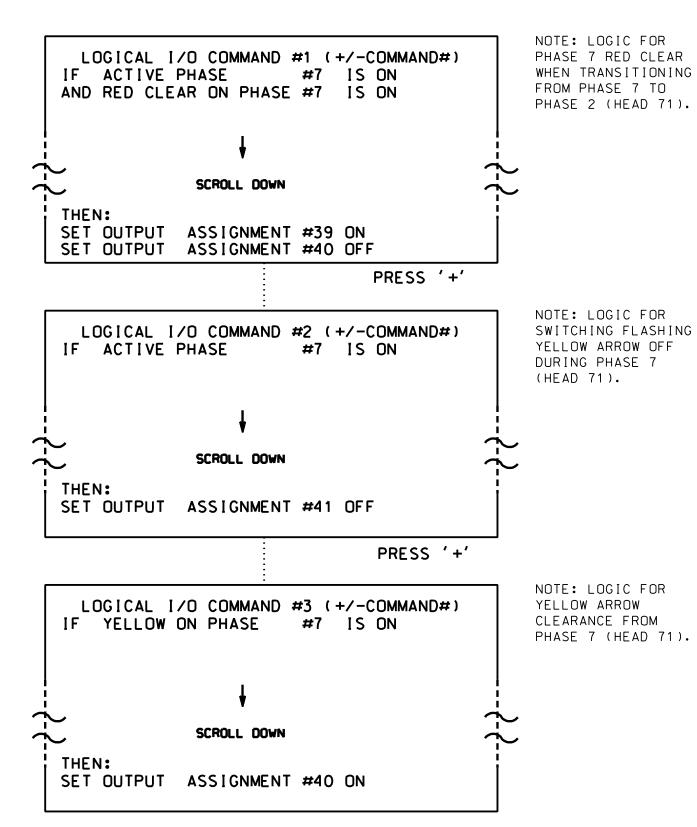
## PROJECT REFERENCE NO. Sig. 8. U-4902D

## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL). THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1. 2. AND 3.



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**OUTPUT REFERENCE SCHEDULE** USE TO INTERPRET LOGIC PROCESSOR

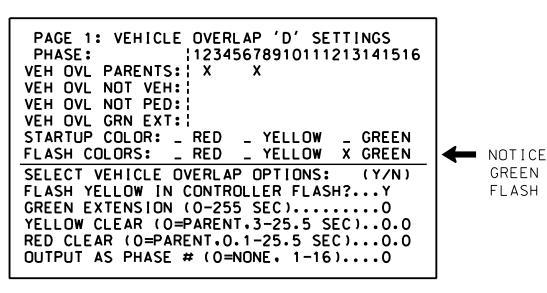
OUTPUT 39 = Overlap D Red OUTPUT 40 = Overlap D Yellow OUTPUT 41 = Overlap D Green

## OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' THREE TIMES



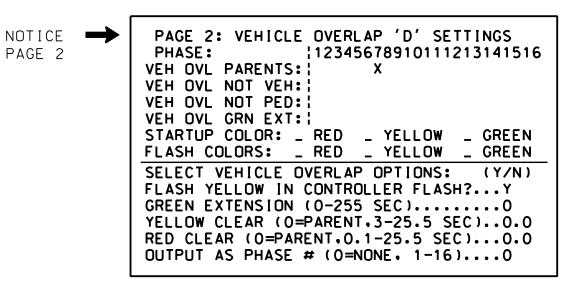
OVERLAP PROGRAMMING COMPLETE

## OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

PRESS '+' THREE TIMES

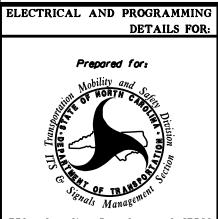


OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-1130 DESIGNED: February 2018 SEALED: 8-1-18 REVISED: N/A

Electrical Detail - Sheet 2 of 4 New Installation

**DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED** 



NB US 17 (Market Street) at U-Turn North of SR 2082 (Greenview Drive)

Division 03 New Hanover Co. February 2018 REVIEWED BY: A.D. Klinksiek

SIG. INVENTORY NO. 03-1130

TH CAROL

OFESSION.

031464

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PLAN DATE: REVISIONS

PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons INIT. DATE