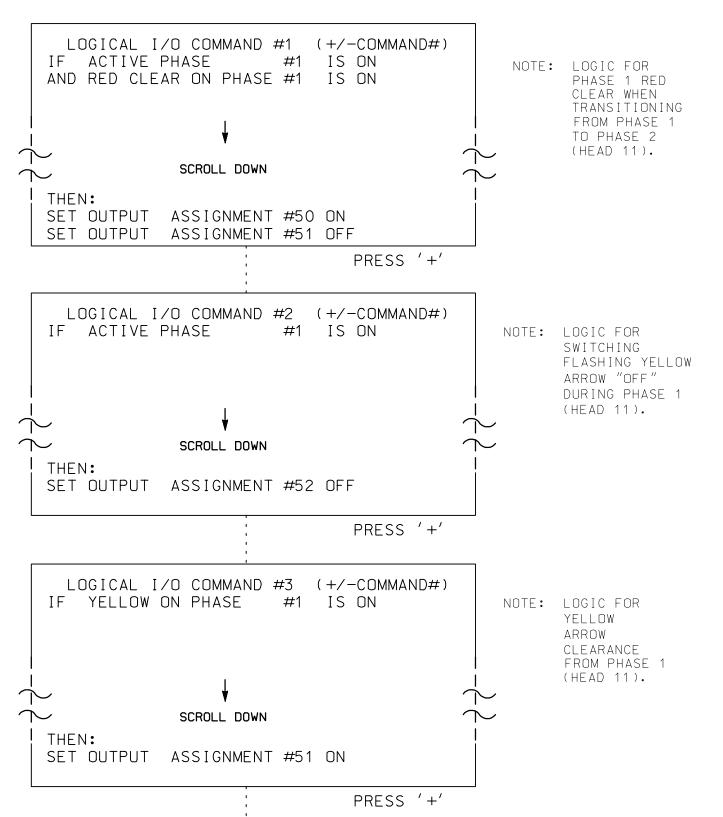
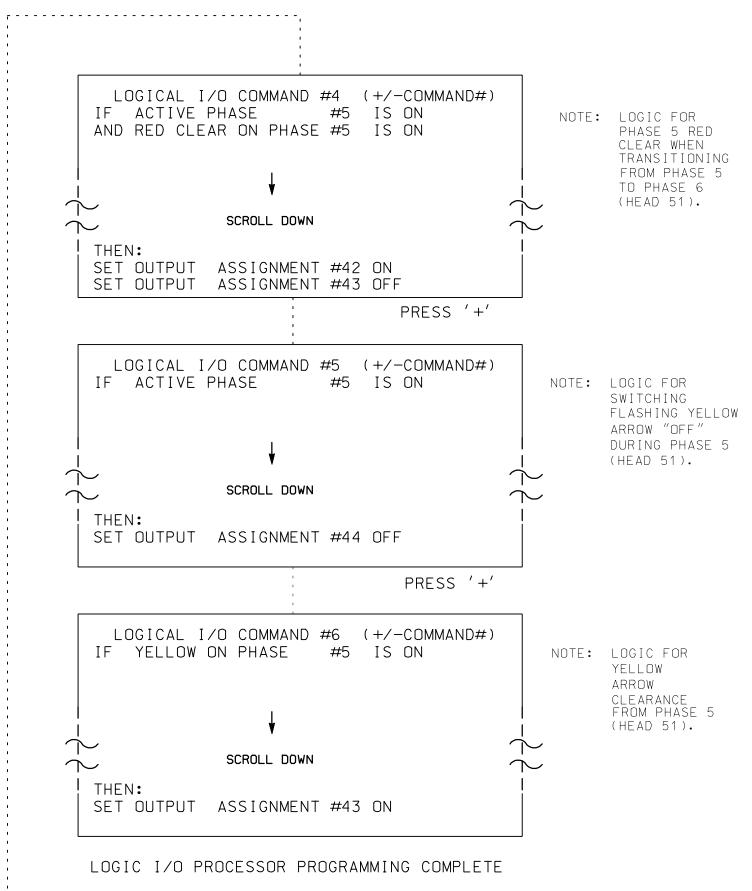
PROJECT REFERENCE NO. SIG-6 U-5840

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).





OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

NC Dept of Transportation

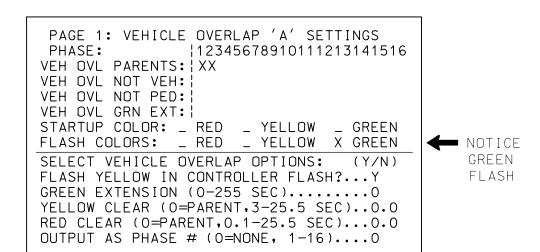
Division of Highways

R. N. Zinser

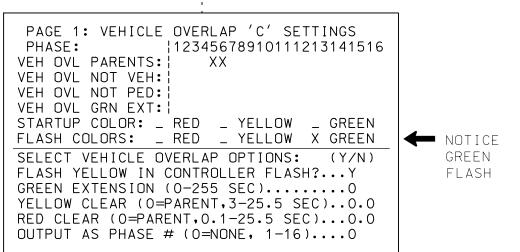
OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

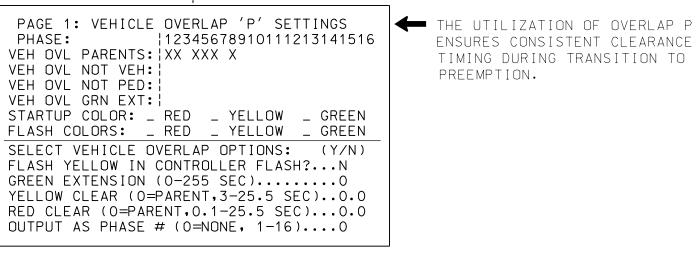
FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



PRESS '+' TWICE



USING + OR - KEY POSITION ON OVERLAP 'P'



OVERLAP PROGRAMMING COMPLETE

This plan supersedes the one signed and sealed on 2/15/2017.

REVISED:

Final Drawing Date: 7/10/2018 ITS & Signals Unit THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 14-Ø196T1 DESIGNED: JULY 2018 SEALED: 7/10/2018





ELECTRICAL AND PROGRAMMING SR 1547 (Old Airport Road)/ SR 1551 (Mills Gap Road) SR 1545 (Cane Creek Road) Henderson County

NE of Fletcher PLAN DATE: July 2018 REVIEWED BY: R Dubnicka REVIEWED BY: J Carroll PREPARED BY: J Trueblood REVISIONS

DOCUMENT NOT CONSIDERED FINAL UNLESS

ALL SIGNATURES COMPLETED

