LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, and 3.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
 LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE        #5  IS ON
AND RED CLEAR ON PHASE #5  IS ON

          SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #30 ON
SET OUTPUT  ASSIGNMENT #31 OFF
```

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

PRESS '+'

```
 LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE        #5  IS ON

          SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #32 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

PRESS '+'

```
 LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE     #5  IS ON

          SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #31 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 30 = Overlap C Red
OUTPUT 31 = Overlap C Yellow
OUTPUT 32 = Overlap C Green

Note: All outputs shown above have been remapped. See sheet 3 of this electrical detail.

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```
 PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE:          |12345678910111213141516
VEH OVL PARENTS:|    XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED   _ YELLOW   _ GREEN
FLASH COLORS:  _ RED   _ YELLOW   X GREEN
SELECT VEHICLE OVERLAP OPTIONS:    (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 10-1619T2
DESIGNED: December 2017
SEALED: 04-23-2018
REVISED: N/A

Electrical Detail - Sheet 2 of 3
Signal Upgrade
Temporary Design 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of: Transportation Mobility and Safety Division, ITS & Signals Management Section, State of North Carolina Department of Transportation
750 N.Greenfield Pkwy,Garner,NC 27529

SR 2136 (Gilead Road)
at
I-77 NB Ramps

Division 10 | Mecklenburg Co. | Huntersville

PLAN DATE:	December 2017	REVIEWED BY:	A.D. Klinksiek
PREPARED BY:	A.H. Thornburg	REVIEWED BY:	N.R. Simmons

REVISIONS	INIT.	DATE

SEAL: NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 031464 NATASHA R. SIMMONS

DocuSigned by: Natasha R. Simmons 4/23/2018
SIGNATURE DATE

SIG. INVENTORY NO. 10-1619 T2