ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 1 ← NOTICE ACTION PLAN SF BIT "1" Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 5

END PROGRAMMING

ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select | 5. TIME BASE

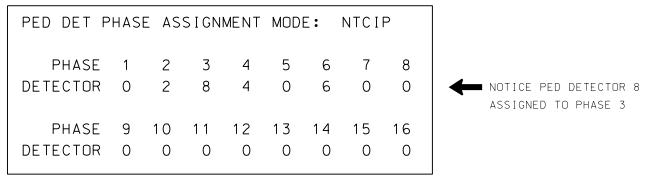
2. From TIME BASE Submenu select | 2. ACTION PLAN |

ACTION PLAN...[1] PATTERN..... AUTO SYS OVERRIDE.... NO TIMING PLAN..... O SEQUENCE..... O VEH DETECTOR PLAN.. 2 DET LOG.....NONE FLASH..... -- RED REST..... NO VEH DET DIAG PLN... O PED DET DIAG PLN...O DIMMING ENABLE.. NO PRIORITY RETURN. NO PED PR RETURN.. NO QUEUE DELAY.... NO PMT COND DELAY NO PHASE 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 PHASE 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 SPC FCT X . . . X . . . (1-8) AUX FCT . . (1-3) 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5

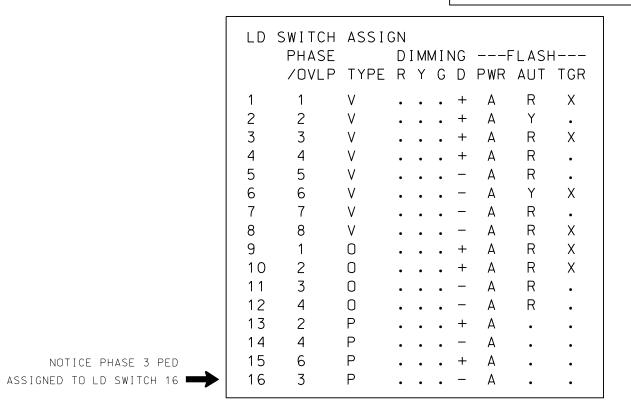
ECONOLITE ASC/3-2070 PED 3 PROGRAMMING ASSIGNMENT DETAIL

(program controller as shown)

- 1. From Main Menu select 6. DETECTORS
- 2. From DETECTOR Submenu select | 3. PED DETECTOR INPUT ASSIGNMENT



- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN



ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

PHASING	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN DEFAULT PHASING	1	NONE
ACTIONS REQUIRED TO RUN ALTERNATE PHASING	2	1, 5

IMPORTANT: IF ALT, PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

Modifies overlap parent phases

for heads 11 and 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A

and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0224 DESIGNED: March 2018 SEALED: 03-29-2018 REVISED: N/A

Final Design

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Electrical Detail - Sheet 2 of 3

Stantec

Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 ELECTRICAL AND PROGRAMMING US 401 Business (Raeford Road) DETAILS FOR: Prepared in the Offices of:

Purdue Drive/ SteinMart Driveway

March 2018 REVIEWED BY: L Overn

DOCUMENT NOT CONSIDERED FINAL

UNLESS ALL SIGNATURES COMPLETED

Division 6 Cumberland County Fayetteville PLAN DATE: PREPARED BY: G B Spell REVIEWED BY: REVISIONS INIT. DATE